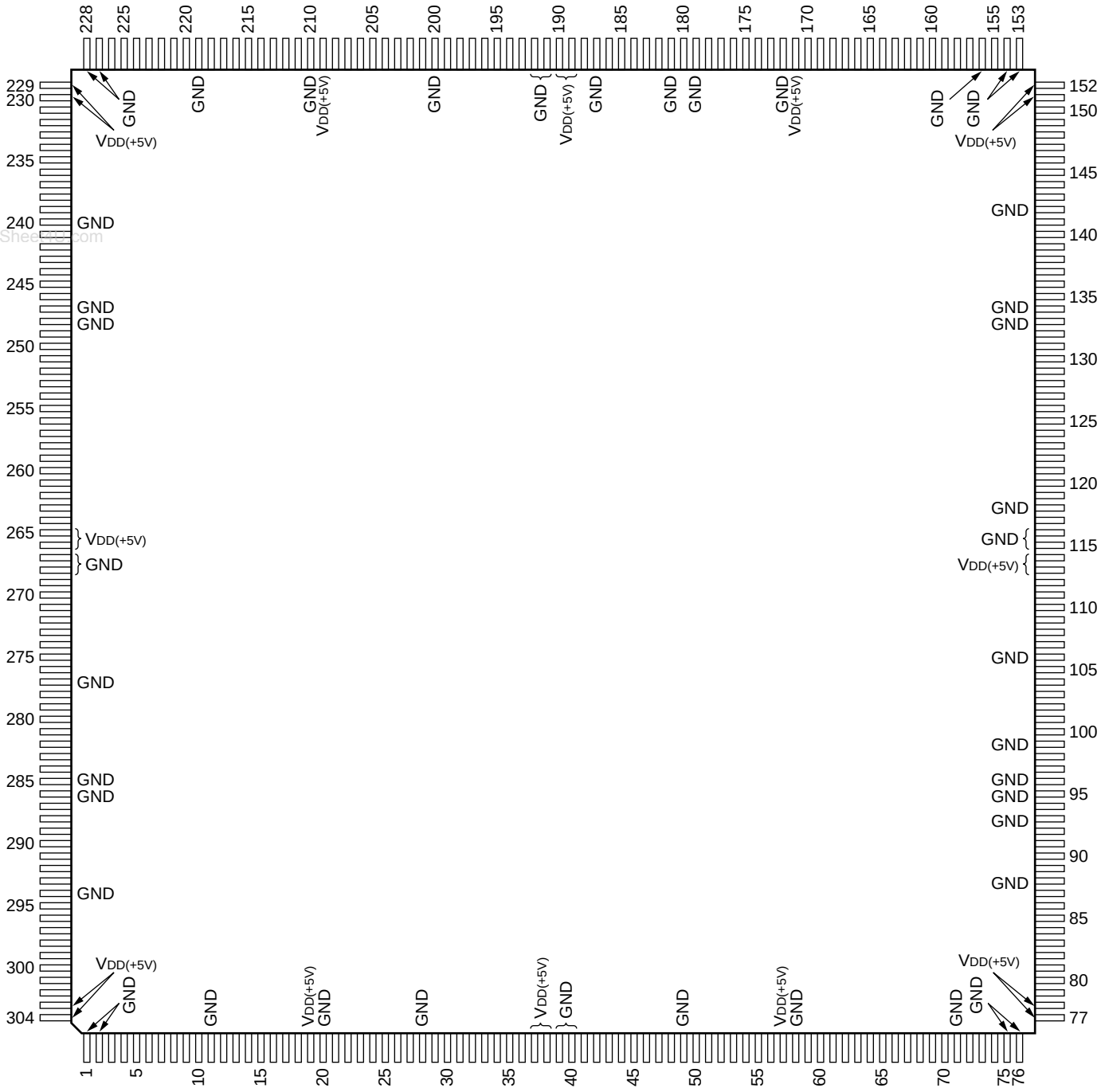
C-MOS GATE ARRAY

–TOP VIEW–



(V_{DD} = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	62	I	C1C2A	123	O	AL0	184	O	PLVAR	245	I	XRES1
2	—	GND	63	O	MUTGA	124	O	AL1	185	O	WDIESEL	246	I	CLK10
3	I/O	RD8	64	I	D2A	125	O	AL2	186	O	PLREF	247	—	GND
4	I/O	RD9	65	I	D3A	126	O	AL3	187	—	GND	248	—	GND
5	I/O	RD10	66	I	D4A	127	O	LTCRDR	188	I	PLCK	249	I	RMEMEN
6	I/O	RD11	67	I	D7A	128	I	TWAIT	189	—	V_{DD}	250	I	PMEMEN
7	I/O	RD12	68	O	IDDT	129	O	TCAD5	190	—	V_{DD}	251	I	SPEN
8	I/O	RD13	69	O	BCK	130	O	BSGN	191	—	GND	252	I	RIOEN
9	I/O	RD14	70	I	LRLK	131	O	XLE	192	—	GND	253	I	PIOEN
10	I/O	RD15	71	—	GND	132	O	RAMCS	193	O	EXSYD	254	I	DIOEN
11	—	GND	72	I	256FSB	133	—	GND	194	O	SWPD	255	I	TCEN
12	O	PGM LD0	73	O	XTAL2EN	134	—	GND	195	O	EXSYA	256	I	TEST6
13	O	PGM LD1	74	I	XTAL44K	135	I	RDSY	196	O	SWPA	257	I	TEST7
14	O	PGM LD2	75	—	GND	136	O	MKSY	197	O	TMRCK	258	O	XWAIT
15	I	TEST1	76	—	GND	137	O	C1	198	I	IOWRAK	259	O	MWRR
16	I	TEST2	77	—	V_{DD}	138	O	C2	199	I	IORDAK	260	O	MRDR
17	O	PGM SD0	78	—	V_{DD}	139	O	C5	200	—	GND	261	O	TRGAR
18	O	PGM SCK	79	I	XTAL48K	140	O	LE4	201	I/O	DB15	262	O	TRGBR
19	—	V_{DD}	80	O	XTAL1EN	141	O	LE8	202	I/O	DB14	263	O	MWRP
20	—	GND	81	I	TEST5	142	—	GND	203	I/O	DB13	264	O	MRDR
21	O	ADDT	82	O	RFWD	143	I/O	TD0	204	I/O	DB12	265	—	V_{DD}
22	O	RCPB	83	O	XREW	144	I/O	TD1	205	I/O	DB11	266	—	V_{DD}
23	O	MUTGD	84	O	XC168	145	I/O	TD2	206	I/O	DB10	267	—	GND
24	I	DDTD	85	O	CFWD	146	I/O	TD3	207	I/O	DB9	268	—	GND
25	I	MNTGD	86	O	XC16	147	I/O	TD4	208	I/O	DB8	269	O	TRGAP
26	I	REPBD	87	O	SER	148	I/O	TD5	209	—	V_{DD}	270	O	TRGBP
27	I	REDTD	88	—	GND	149	I/O	TD6	210	—	GND	271	I	DSPSDIP
28	—	GND	89	O	RCDTA	150	I/O	TD7	211	I/O	DB7	272	I	DSPSDIR
29	O	RFSFD	90	O	RCDTD	151	—	V_{DD}	212	I/O	DB6	273	O	DSPSDOP
30	I	ATD2D	91	I	ARE	152	—	V_{DD}	213	I/O	DB5	274	O	DSPSDOR
31	I	SBSYD	92	I	LSWP	153	—	GND	214	I/O	DB4	275	O	TRGSDOP
32	I	SVRFD	93	—	GND	154	—	GND	215	I/O	DB3	276	O	TRGSDOR
33	O	DPGD	94	I	CLKO	155	I	LCLK	216	I/O	DB2	277	—	GND
34	I	TEST3	95	—	GND	156	—	GND	217	I/O	DB1	278	O	RA0
35	I	CKRD	96	—	GND	157	O	GENSY	218	I/O	DB0	279	O	RA1
36	I	C1C2D	97	O	CHRF	158	O	LSHSY	219	—	GND	280	O	RA2
37	—	V_{DD}	98	O	CHVF	159	I	LTCIN	220	O	AB15	281	O	RA3
38	—	V_{DD}	99	—	GND	160	O	LTCOUT	221	O	AB14	282	O	RA4
39	—	GND	100	O	ATD2	161	O	TCGINT	222	O	AB13	283	O	RA5
40	—	GND	101	I	DFG	162	I	VSYNC	223	O	AB12	284	O	RA6
41	I	D2D	102	O	SVCK	163	O	FSPLS	224	O	AB11	285	—	GND
42	I	D3D	103	I	EXT96	164	O	SAW600	225	O	AB10	286	—	GND
43	I	D4D	104	I	RFDETA	165	I	SYAB	226	O	AB9	287	O	RA7
44	I	D7D	105	I	RFDETD	166	O	SELPS	227	—	GND	288	O	RA8
45	I	ATD1	106	—	GND	167	O	CLVS	228	—	GND	289	O	RA9
46	I	SRVS	107	I	DTAD	168	O	WIND	229	—	V_{DD}	290	O	RAS
47	I	DDTA	108	O	DOUT	169		FRAM	230	—	V_{DD}	291	O	CAS
48	I	C94MA	109	O	EMP	170		FOUT	231	O	AB8	292	O	OE
49	—	GND	110	O	DEEMP	171	—	V_{DD}	232	O	AB7	293	O	WR
50	I	MNTGA	111	O	MSVF	172	—	GND	233	O	AB6	294	—	GND
51	I	REPBA	112	O	MSRF	173	I	CSYNC	234	O	AB5	295	I/O	RD0
52	I	REDT-A	113	—	V_{DD}	174	O	FSHL	235	O	AB4	296	I/O	RD1
53	O	RFSFA	114	—	V_{DD}	175	O	VCOIN	236	O	AB3	297	I/O	RD2
54	I	ATD2A	115	—	GND	176	O	DATFRM	237	O	AB2	298	I/O	RD3
55	I	SBSYA	116	—	GND	177	O	SYAO	238	O	AB1	299	I/O	RD4
56	I	SVRFA	117	I	VCLK	178	O	SYDO	239	O	AB0	300	I/O	RD5
57	—	V_{DD}	118	—	GND	179	—	GND	240	—	GND	301	I/O	RD6
58	—	GND	119	I	DL0	180	I	AESIN	241	I	MRD	302	I/O	RD7
59	O	DPGA	120	I	DL1	181	—	GND	242	I	MWR	303	—	V_{DD}
60	I	TEST4	121	I	DL2	182	O	AESOUT	243	I	BUFEN	304	—	V_{DD}
61	I	CKRA	122	I	DL3	183	I	WDFS	244	I	BUFRW			

RD8 - RD15	;	DATA BUS FOR DRAM
PGMLD0 - PGMLD2	;	LOAD SIGNAL OUTPUTS FOR DSP PROGRAM DATA
TEST1 - TEST7	;	TEST INPUTS (NORMINALY ”L”)
PGMSDO	;	DSP PROGRAM SERIAL DATA OUTPUT
PGMSCK	;	SHIFT CLOCK OUTPUT FOR PGMSDO
ADDT	;	AD DATA (REC DATA) OUTPUT
RCPB	;	REC/PB SIGNAL OUTPUT (TO RF BOARD)
MUTGD	;	MUTE SIGNAL OUTPUT FOR CXD1008 (TRAILING) (”H” : MUTE)
DDTD	;	TRAILING DA DATA (PB/REC MONITOR) INPUT
MNTGD	;	ERROR MONITOR OUTPUT TO D7 - D0 (WHEN : ”H”)
REPBD	;	REC/PB SWITCHING SIGNAL INPUT FROM CXD1009 (TRAILING)
REDTD	;	REC DATA INPUT FROM CXD1009 (TRAILING)
RFSFD	;	RF SAFE-D OUTPUT
ATD2D	;	ATD2-D INPUT
SBSYD	;	SBSY-D INPUT
SVRFD	;	SVRF-D INPUT
DPGD	;	DELAY EDDP-D OUTPUT
CKRD	;	FS/SYSTEM CLOCK (4.704MHz TYPE)
C1C2D	;	C1/C2 DISCRIMIATION SIGNAL INPUT
D2D, D3D, D4D, D7D	;	DATA BUS FOR CXD1008/1009 (TRAILING)
ATD1	;	OFF TRACK DETECTING SIGNAL INPUT
SRVS	;	FS CLOCK (128KHz TYPE)
DDTA	;	DADT-A INPUT
C94MA	;	FS CLOCK (9.408MHz TYPE) INPUT
MNTGA	;	ERROR MONITOR OUTPUT TO D7 - D0 (WHEN : ”H”)
REPBA	;	REC/PB SWITCHING SIGNAL FROM CXD1009 (LEADING)
REDT-A	;	REC DATA INPUT FROM CXD1009 (TRAILING)
RFSFA	;	RF SAFE-A OUTPUT
ATD2A	;	ATD2-A INPUT
SBSYA	;	SBSY-A INPUT
SVRFA	;	SVRF-A INPUT
DPGA	;	DELAY EDDP-A OUTPUT
CKRA	;	FS/SYSTEM CLOCK
C1C2A	;	C1/C2 DISCRIMINATION SIGNAL INPUT
MUTGA	;	MUTE OUTPUT TO CXD1008 (LEADING) (”H” : MUTE)
D2A, D3A, D4A, D7A	;	DATA BUS INPUTS FROM CXD1008/1009 (LEADING)
IDDT	;	LEVEL DETECTION SIGNAL OUTPUT
BCK	;	FS CLOCK (32 • FS) OUTPUT
LRLK	;	FS CLOCK (FS) INPUT
256FSB	;	FS CLOCK (256 • FS) INPUT
XTAL2EN	;	44.1KHz X'TAL ENABLE SIGNAL OUTPUT
XTAL44K	;	44.1KHz *512 CLOCK INPUT
XTAL48K	;	48KHz *512 CLOCK INPUT
XTAL1EN	;	48KHz X'TAL ENABLE SIGNAL OUTPUT
RFWD	;	RF WINDOW OUTPUT
XREW, XC168, CFWD, XC16	;	RF PLL CONTROL SIGNAL OUTPUTS
SER	;	DPG GNE OUTPUT
RCDTA	;	REC DATA OUTPUT FOR LEADING (TO RF BOARD)
RCDTD	;	REC DATA OUTPUT FOR TRAILING (TO RF BOARD)
ARE	;	ARE INPUT

LSWP	;	LSWP INPUT
CLKO	;	CHANNEL PLL CLOCK INPUT
CHRF	;	CHANNEL PLL REFERENCE SIGNAL OUTPUT
CHVF	;	CHANNEL PLL COMPARISON SIGNAL OUTPUT
ATD2	;	ATD-2 OUTPUT (TO RF BOARD)
DFG	;	DRUM FG INPUT
SVCK	;	SVCK INPUT
EXT96	;	EXTERNAL SOURCE INPUT
RFDETA	;	RF DET-A INPUT
RFDETD	;	RF DET-D INPUT
DTAD	;	AD DATA INPUT (FROM ADC)
DOUT	;	DATA OUTPUT
EMP	;	PRE-EMPHASIS ON SIGNAL OUTPUT
DEEMP	;	DE-EMPHASIS ON SIGNAL OUTPUT
MSVF	;	MASTER PLL REFERENCE SIGNAL OUTPUT
MSRF	;	MASTER PLL COMPARISON SIGNAL OUTPUT
VCCK	;	MASTER PLL CLOCK INPUT
DL0 - DL3	;	EXTERNAL DATA BUS FOR TC DATA
AL0 - AL3	;	EXTERNAL ADDRESS BUS FOR TC DATA
LTCRDR	;	LTC READER OUTPUT
TWAIT	;	TC WAIT INPUT
TCAD5	;	EXTERNAL ADDRESS BUS FOR TC DATA
BSGN	;	TIME CODE LOAD SIGNAL OUTPUT
XLE	;	TIME CODE LATCH SIGNAL OUTPUT
RAMCS	;	CHIP SELECT SIGNAL OUTPUT FOR EXTERNAL RAM
RDSY	;	TIME CODE SYNCHRONISED SIGNAL INPUT
MKSY	;	RESET SIGNAL OUTPUT OF INTERNAL RECMK COUNTER
C1, C2, C5	;	RESISTOR LATCH SIGNAL OUTPUTS
LE4, LE8	;	RESISTOR LOAD SIGNAL OUTPUTS
TD0 - TD7	;	TC DATA BUS
LCLK	;	TC GENERATOR CLOCK INPUT
GENSY	;	TC GENERATOR REFERENCE SIGNAL OUTPUT
LSHSY	;	TC GENERATOR COMPARISON SIGNAL OUTPUT
LTCIN	;	LTC INPUT
LTCOUT	;	LTC OUTPUT
TCGINT	;	TC GENERATOR INTERRUPT REQUEST INPUT
VSYNC	;	VSYNC INPUT
FSPLS	;	FRAME COMPARISON SIGNAL OUTPUT
SAW600	;	SAW 600 OUTPUT
SYAB	;	SYAB OUTPUT
SELPS	;	SELPS OUTPUT
CLVS	;	CLVS OUTPUT
WIND	;	WIND OUTPUT
FRAM	;	FRAM OUTPUT
FOUT	;	FOUT OUTPUT
CSYNC	;	CSYNC OUTPUT
FSHL	;	FSHL OUTPUT
VCOIN	;	VCOIN OUTPUT
DATFRM	;	DATA FRAME OUTPUT
SYAO	;	SBSY-A OUTPUT

SYDO	; SBSY-D OUTPUT
AESIN	; AES/EBU SIGNAL INPUT
AESOUT	; AES/EBU SIGNSL OUTPUT
WDFS	; WORD CLOCK INPUT
PLVAR	; DI PLL COMPARISON SIGNAL INPUT
WDIESEL	; WORD SWITCHING SIGNAL OUTPUT
PLREF	; DI PLL REFERENCE SIGNAL OUTPUT
PLCK	; DI PLL CLOCK INPUT
EXSYD	; EXSY-D OUTPUT
SWPD	; SWITCHING PULSE-D OUTPUT
EXSYA	; EXSY-A OUTPUT
SWPA	; SWITCHING PULSE-A OUTPUT
TMRCK	; TMRCK OUTPUT
IOWRAK	; IO WRITE SIGNAL INPUT
IORDAK	; IO READ SIGNAL INPUT
DB15 - DB0	; DATA BUS
AB15 - AB0	; ADDRESS BUS
MRD	; MRD INPUT
MWR	; MWR INPUT
BUFEN	; BUFEN INPUT
BUFRW	; BUFRW INPUT
XRES1	; RESET INPUT
CLK10	; 10MHz CLOCK INPUT
RMEMEN	; CHIP SELECT INPUT FOR MEM BLOCK (RECORDER MEM AREA)
PMEMEN	; CHIP SELECT INPUT FOR MEM BLOCK (PLAYER MEM AREA)
SPEN	; CHIP SELECT INPUT FOR SP BLOCK
RIOEN	; CHIP SELECT INPUT FOR MEM BLOCK (RECORDER I/O AREA)
PIOEN	; CHIP SELECT INPUT FOR MEM BLOCK (PLAYER I/O AREA)
DIOEN	; CHIP SELECT INPUT FOR DIO BLOCK
TCEN	; CHIP SELECT INPUT FOR TC BLOCK
XWAIT	; WAIT SIGNAL OUTPUT
MWRR	; MEMORY WRITE MONITOR OUTPUT FOR RECORDER
MRDR	; MEMORY READ MONITOR OUTPUT FOR RECORDER
TRGAR	; TRIGGER A MONITOR OUTPUT FOR RECORDER
TRGBR	; TRIGGER B MONITOR OUTPUT FOR RECORDER
MWRP	; MEMORY WRITE MONITOR OUTPUT FOR PLAYER
MRDP	; MEMORY READ MONITOR OUTPUT FOR PLAYER
TRGAP	; TRIGGER A MONITOR OUTPUT FOR PLAYER
TRGBP	; TRIGGER B MONITOR OUTPUT FOR PLAYER
DSPSDIP	; SERIAL DATA INPUT FOR PLAYER (FROM DSP)
DSPSDIR	; SERIAL DATA INPUT FOR RECORDER (FROM DSP)
DSPSDOP	; SERIAL DATA OUTPUT FOR PLAYER (TO DSP)
DSPSDOR	; SERIAL DATA OUTPUT FOR RECORDER (TO DSP)
TRGSDOP	; TRIGGER DATA OUTPUT FOR PLAYER (TO DSP)
TRGSDOR	; TRIGGER DATA OUTPUT FOR RECORDER (TO DSP)
RA0 - RA9	; ADDRESS BUS FOR DRAM
RAS	; RAS OUTPUT
CAS	; CAS OUTPUT
OE	; OE OUTPUT
WR	; WR OUTPUT
RD0 - RD7	; DATA BUS FOR DRAM