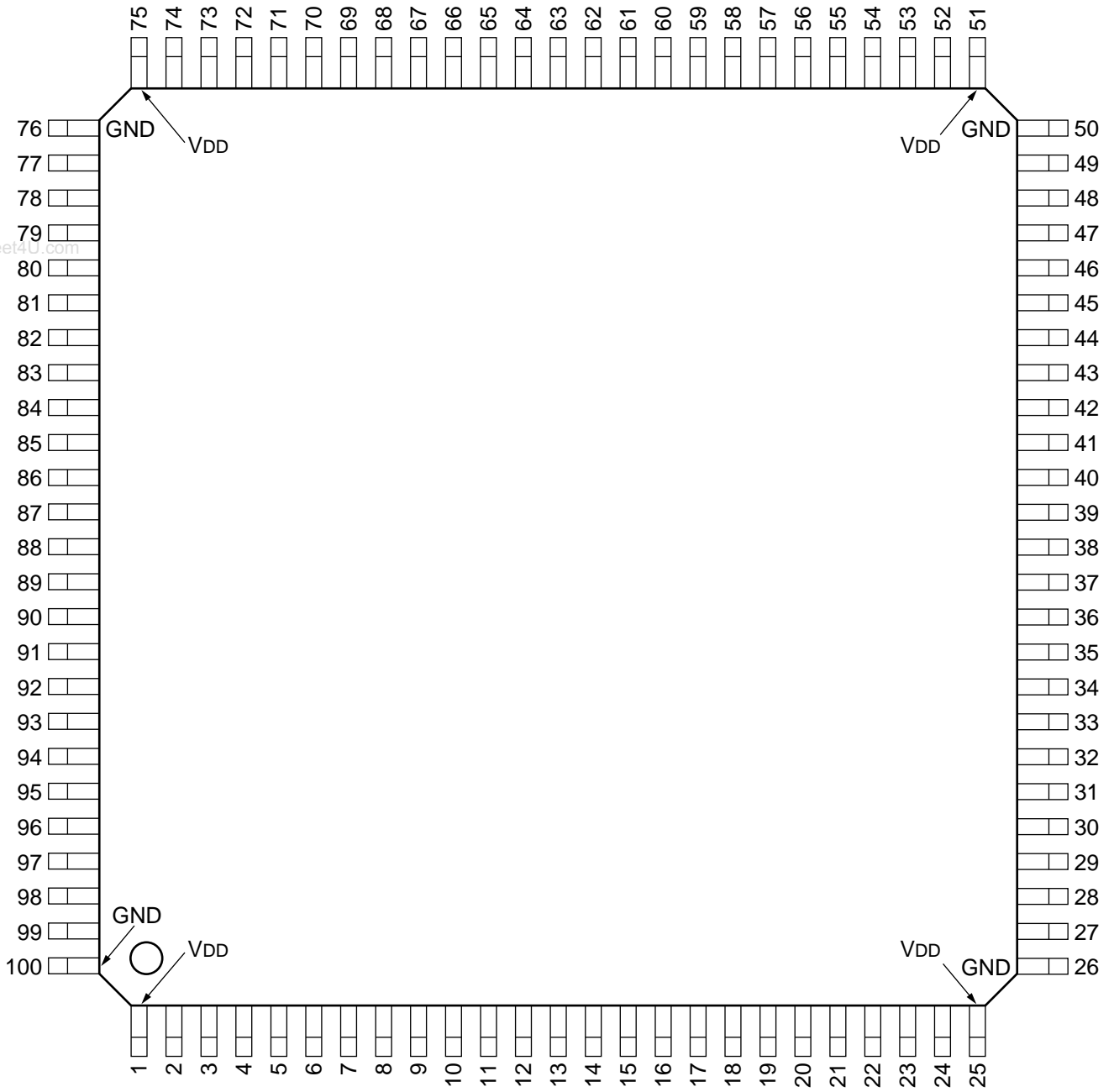


C-MOS INPUT VIDEO PROCESSOR (GATE ARRAY)

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	26	—	GND	51	—	V _{DD}	76	—	GND
2	I	TST	27	O	TS2	52	I	EXFL	77	I	TSTOL
3	O	OUT9	28	I	CKX	53	I	EXCF	78	I	TSTRS
4	O	OUT8	29	I	RST	54	I	TSTTRS	79	O	F0
5	O	OUT7	30	I	CS	55	I	TRSSW	80	O	V0
6	O	OUT6	31	I/O	SDA	56	I	EMRLAL	81	I	TSTID7
7	O	OUT5	32	I	SADD	57	I	LALTSW	82	I	TSTID6
8	O	OUT4	33	I	CKD	58	O	CPTRS	83	I	TSTID5
9	O	OUT3	34	I	WCK	59	O	WLALT	84	I	TSTID4
10	O	OUT2	35	I	TS4	60	O	RLALT	85	I	TSTID3
11	O	OUT1	36	I	TS5	61	—	N.C.	86	I	TSTID2
12	O	OUT0	37	I	TS6	62	—	N.C.	87	I	TSTID1
13	I	REFH	38	I	DIN9	63	—	N.C.	88	I	TSTID0
14	—	N.C.	39	I	DIN8	64	—	N.C.	89	I	IDSW
15	I	RFLD	40	I	DIN7	65	—	N.C.	90	—	N.C.
16	I	RCFP	41	I	DIN6	66	—	N.C.	91	—	N.C.
17	I	RCK	42	I	DIN5	67	O	ID7	92	—	N.C.
18	O	TRS	43	I	DIN4	68	O	ID6	93	—	N.C.
19	I	SMPL	44	I	DIN3	69	O	ID5	94	—	N.C.
20	O	WPT1	45	I	DIN2	70	O	ID4	95	O	PKDET
21	O	WPT0	46	I	DIN1	71	O	ID3	96	O	CPAPCR
22	O	TS0	47	I	DIN0	72	O	ID2	97	O	CPFFCR
23	I	RPT0	48	I	EXH	73	O	ID1	98	O	CRSUM
24	O	TS1	49	O	TS3	74	O	ID0	99	O	PARITY
25	O	V _{DD}	50	—	GND	75	—	V _{DD}	100	—	GND

47	DIN0	OUT0	12
46	DIN1	OUT1	11
45	DIN2	OUT2	10
44	DIN3	OUT3	9
43	DIN4	OUT4	8
42	DIN5	OUT5	7
41	DIN6	OUT6	6
40	DIN7	OUT7	5
39	DIN8	OUT8	4
38	DIN9	OUT9	3
15	RFLD	TRS	18
16	RCFP		
17	RCK	WPT0	21
34	WCK	WPT1	20
23	RPT0	TS0	22
19	SMPL	TS1	24
13	REFH	TS2	27
48	EXH	TS3	49
52	EXFL		
53	EXCF	ID0	74
		ID1	73
2	TST	ID2	72
35	TS4	ID3	71
36	TS5	ID4	70
37	TS6	ID5	69
77	TSOL	ID6	68
78	TSRS	ID7	67
29	RST	F0	79
30	CS	V0	80
28	CKX		
32	SADD		
31	SDA		
33	CKD		

- DIN0 - DIN9 : DIGITAL VIDEO/KEY INPUTS
(DIN0 : LSB DIN9 : MSB)
- RFLD : REFERENCE FIELD
- RCFP : REFERENCE CFP
- RCK : REFERENCE CLOCK
- WCK : WRITE CLOCK
- RPT0 : READ PORT 0
- SMPL : SAMPLING PULSE INPUT
- REFH : REFERENCE H
- EXH : ANALOG REFERENCE H
- EXFL : ANALOG REFERENCE FIELD
- EXCF : ANALOG REFERENCE CF
- RST : RESET INPUT
- CS : CHIP SELECT
- CKX : WRITE TIMING PULSE
- SADD : SERIAL ADDRESS INPUT
- SDA : SERIAL DATA INPUT
- CKD : SERIAL CONTROL CLOCK
- OUT0 - OUT9 : DIGITAL VIDEO/KEY OUTPUTS
(OUT0 : LSB OUT9 : MSB)
- TRS : WCLR PULSE
- WPT0, 1 : WRITE PORT0, 1