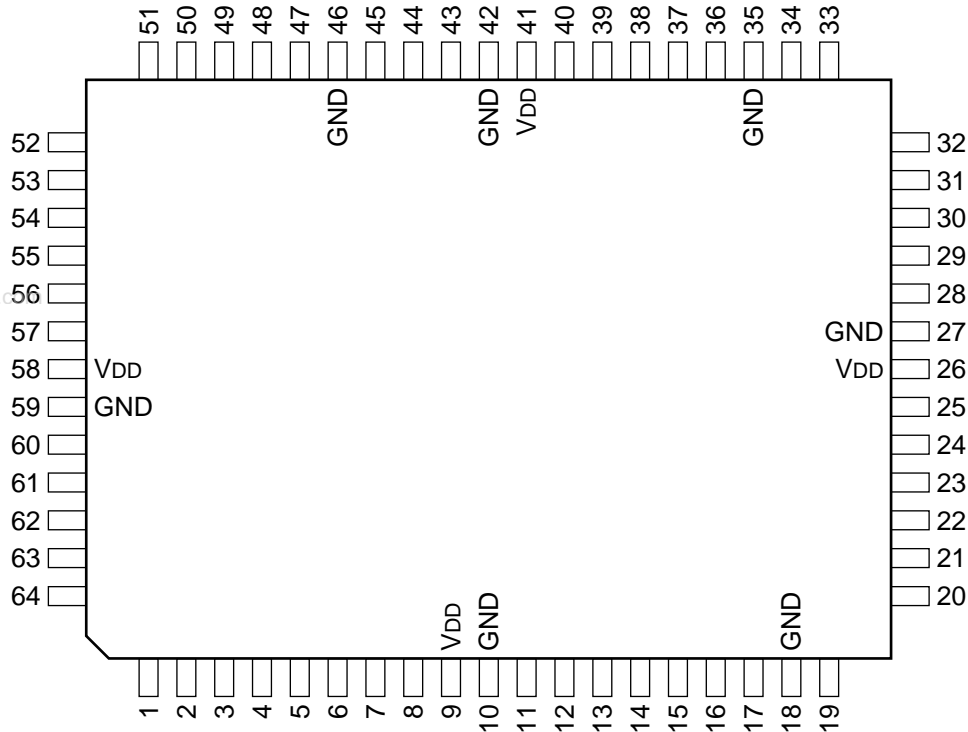


C-MOS LINEAR PHASE DIGITAL SERVO

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	D0	17	I	RST	33	O	SWX	49	I	FD1
2	I	D1	18	—	GND	34	O	C2F	50	I	FD2
3	I	D2	19	O	IOR	35	—	GND	51	I	FD3
4	I	D3	20	O	IOW	36	O	CAP PD	52	I	FD4
5	I	D4	21	O	DPWM	37	O	CSR02	53	I	FD5
6	I	D5	22	O	RPWM	38	O	CSR01	54	I	FD6
7	I	D6	23	O	CPWM	39	O	CSR00	55	I	FD7
8	I	D7	24	O	FGSW	40	O	C1F	56	I	SWH
9	—	VDD	25	O	DDIR	41	—	VDD	57	I	SWL
10	—	GND	26	—	VDD	42	—	GND	58	—	VDD
11	I	A0	27	—	GND	43	O	CPLL NG	59	—	GND
12	I	A1	28	O	CP/DR TIM	44	O	CK32	60	I	CA
13	I	A2	29	O	D1F	45	O	CK AD	61	I	RA
14	I	CS0	30	O	CFG B	46	—	GND	62	I	CB
15	I	IO STB	31	O	CFG A	47	I	20MCLK	63	I	RB
16	I	R/W	32	O	CSW00	48	I	FD0	64	I	DRFG

INPUT

20MCLK	: 20 MHz SYSTEM CLOCK
A0 - A2	: SERVO ADDRESS BUS
CA	: CAPSTAN FG (A)
CB	: CAPSTAN FG (B)
CS0	: CHIP SELECT
D0 - D7	: SERVO DATA BUS
DRFG	: DRUM FG
FD0 - FD7	: ANALOG FG A/D DATA BUS
IO STB	: I/O STROBE
R/W	: READ/WRITE
RA	: REEL FG (A) (SPARE)
RB	: REEL FG (B) (SPARE)
RST	: SERVO RESET
SWH	: SWH INPUT (SPARE)
SWL	: SWL INPUT (SPARE)

OUTPUT

C1F	: CAPSTAN PLL REFERENCE SIGNAL (1)
C2F	: CAPSTAN PLL REFERENCE SIGNAL (2)
CAP PD	: CAPSTAN PLL ERROR
CFG A	: CAPSTAN FG1
CFG B	: CAPSTAN FG2
CK AD	: CLOCK (SPARE)
CK32	: A/D CLOCK
CP/DR TIM	: CAPSTAN/DRUM SPEED REFERENCE
CPLL NG	: CAPSTAN PLL NG
CPWM	: CAPSTAN PWM SIGNAL
CSR00 - CSR02	: I/O READ (SPARE)
CSW00	: I/O WRITE (SPARE)
D1F	: DRUM PLL REFERENCE SIGNAL
DDIR	: DRUM ROTATION DIRECTION CONTROL
DPWM	: DRUM PWM SIGNAL
FGSW	: CAPSTAN/REEL FG SELECT
IO R	: I/O READ
IO W	: I/O WRITE
RPWM	: REEL PWM SIGNAL
SWX	: SW OUTPUT (SPARE)

