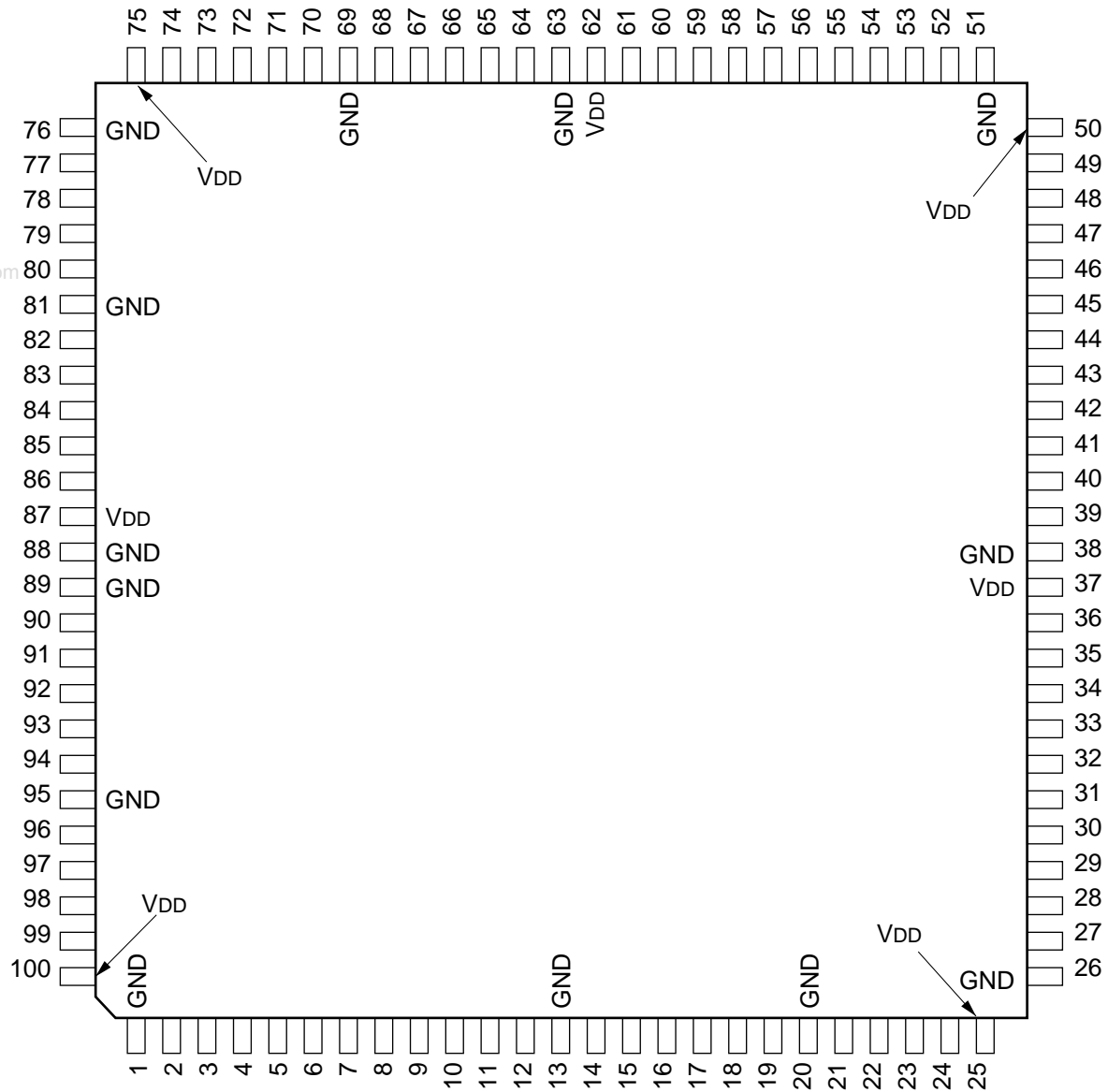


C-MOS AUDIO RE-MULTIPLEXER

—TOP VIEW—



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PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	GND	26	—	GND	51	—	GND	76	—	GND
2	I	IN0	27	I	TEST1	52	I	RESET	77	I	TEST0
3	I	IN1	28	I	ADRES0	53	I	DELAY1	78	O	RTN-ADRS
4	I	IN2	29	I	ADRES1	54	I	DELAY2	79	O	C-DID
5	I	IN3	30	I	ADRES2	55	I	AUX ENA	80	O	C-DBN
6	I	IN4	31	I	ADRES3	56	I	FREEZE	81	—	GND
7	I	IN5	32	I	ADRES4	57	I	SAMPLE	82	O	TEST11
8	I	IN6	33	I	ADRES5	58	I	FRAME	83	O	TEST12
9	I	IN7	34	I	ADRES6	59	I	TRS	84	O	TEST13
10	I	IN8	35	I	ADRES7	60	I	RCK	85	O	TEST14
11	I	IN9	36	I	CS1	61	O	ENA	86	O	SRESET
12	I	WCK	37	—	VDD	62	—	VDD	87	—	VDD
13	—	GND	38	—	GND	63	—	GND	88	—	GND
14	I	HD	39	I	CS2	64	O	OUT9	89	—	GND
15	I	A/O	40	I	CS3	65	O	OUT8	90	O	TOD0
16	I	CH-SEL1	41	I	TID0	66	O	OUT7	91	O	TOD1
17	I	CH-SEL2	42	I	TID1	67	O	OUT6	92	O	TOD2
18	I	IN-FORM	43	I	TID2	68	O	OUT5	93	O	TOD3
19	I	OUT-FORM	44	I	TID3	69	—	GND	94	O	TOD4
20	—	GND	45	I	TID4	70	O	OUT4	95	—	GND
21	O	AUDIO	46	I	TID5	71	O	OUT3	96	O	TOD5
22	O	OTHER	47	I	TID6	72	O	OUT2	97	O	TOD6
23	O	R-AUDIO	48	I	TID7	73	O	OUT1	98	O	TOD7
24	O	R-OTHER	49	I	TID8	74	O	OUT0	99	O	TOD8
25	—	VDD	50	—	VDD	75	—	VDD	100	—	VDD

INPUT

- ADRES0 - ADRES7 : TEST
- A/O : PREFERENCE SETTING OF MULTIPLE DATA (EFFECTIVE 19 PIN = GND)
(0 : PREFERENCE AUDIO DATA 1 : PREFERENCE OTHER ANCILLARY DATA)
- AUX - ENA : OUTPUT SELECT OF ANCILLARY DATA (EXCEPT AUDIO) (EFFECTIVE 56 PIN = 1)
(0 : ENABLE 1 : DISABLE)
- CH-SEL1, CH-SEL2 : GROUPE SETTING OF MULTIPLE AUDIO DATA
(EFFECTIVE 19 PIN = GND)

	GROUPE1 DID = 2FF	GROUPE2 DID = 1FD	GROUPE3 DID = 1FB	GROUPE4 DID = 2F9
CH-SEL1	0	1	0	1
CH-SEL2	0	0	1	1

0 : LOW LEVEL
1 : HIGH LEVEL

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- CS1, CS2, CS3 : TEST
- DELAY1, DELAY2 : MULTIPLE POINT SETTING OF ANCILLARY DATA (EXCEPT AUDIO)

	0 LINE	1 LINE	2 LINE	3 LINE
DELAY1	0	1	0	1
DELAY2	0	0	1	1

0 : LOW LEVEL
1 : HIGH LEVEL

- FRAME : FRAME FOR OUTPUT DATA (ACTIVE HIGH)
- FREEZE : MULTIPLE OPERATION SELECT OF AUDIO DATA (0 : ENABLE 1 : DISABLE)
- HD : INPUT DATA LINE PULSE (ACTIVE HIGH)
- IN0 - IN9 : DIGITAL VIDEO INPUT DATA (TTL INPUT)
- IN-FORM : INPUT FORMAT SETTING (0 : D1/525 1 : D2/525)
- OUT-FORM : OUTPUT FORMAT SETTING (0 : D2/525 1 : D1/525)
- RCK : CLOCK FOR OUTPUT DATA
- RESET : RESET (0 : RESET 1 : NORMAL)
- SAMPLE : OUTPUT SPACE SETTING OF AUDIO DATA 4 SAMPLES (0 : 6 LINE 1 : 5 LINE)
- TEST0, TEST1 : TEST
- TID0 - TID8 : TEST
- TRS : TRS, EAV FOR OUTPUT DATA (ACTIVE HIGH)
- WCK : DIGITAL VIDEO INPUT CLOCK

OUTPUT

- AUDIO : AUDIO AREA OF INPUT DATA
(1 : AUDIO 0 : OTHER (PULSE 2 mA OUTPUT))
- C-DBN : DISCONTINUOUS DETECT OF INPUT AUDIO DATA DBN
1 : CHANGE (PULSE 2 mA OUTPUT)
- C-DID : CHANGE DETECT WITH INPUT AUDIO DATA DID
1 : CHANGE (PULSE 2 mA OUTPUT)
- ENA : OUTPUT DATA ENABLE (ACTIVE LOW, 8 mA OUTPUT)
- OTHER : OTHER ANCILLARY AREA OF INPUT DATA
(1 : OTHER 0 : OTHER (PULSE 2 mA OUTPUT))
- OUT0 - OUT9 : OUTPUT DATA
- R-AUDIO : AUDIO AREA OF OUTPUT DATA
(1 : AUDIO 0 : OTHER (PULSE 2 mA OUTPUT))
- R-OTHER : OTHER ANCILLARY AREA OF OUTPUT DATA
(1 : OTHER ANCILLARY DATA 0 : OTHER (PULSE 2 mA OUTPUT))
- RTN-ADRS : ERROR OF INPUT AUDIO DATA
1 : ERROR (PULSE 2 mA OUTPUT)
- TEST11 - TEST14 : TEST
- TOD0 - TOD8 : TEST