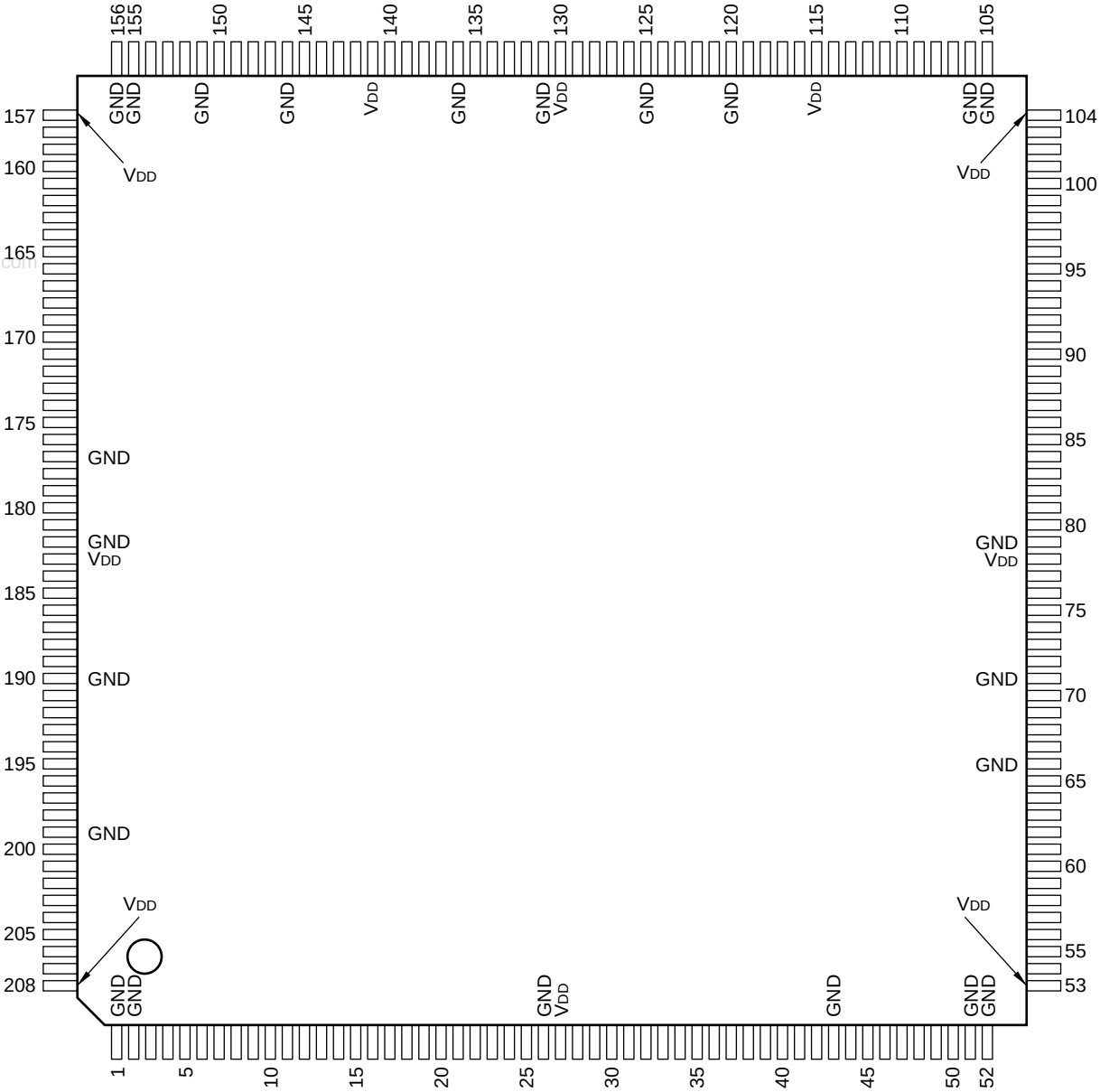
C-MOS VIDEO JOG MEMORY CONTROL (GATE ARRAY)

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	GND	43	—	GND	85	O	OTSTJV_3	127	I/O	BSD_DQ15	169	O	OSD_AD3
2	—	GND	44	I	IREF_HD	86	O	OTSTJV_4	128	I/O	BSD_DQ16	170	O	OSD_AD4
3	I	IDEC2_0	45	I	IREF_VD	87	O	OTSTJV_5	129	I/O	BSD_DQ17	171	O	OSD_AD5
4	I	IDEC2_1	46	I	IREF_CF	88	O	OTSTJV_6	130	—	VDD	172	O	OSD_AD6
5	I	IDEC2_2	47	I	ISDIE_HD	89	O	OTSTJV_7	131	—	GND	173	O	OSD_AD7
6	I	IDEC2_3	48	I	ISDIE_CF	90	I	IHOTSTJV	132	I/O	BSD_DQ20	174	O	OSD_AD8
7	I	IDEC2_4	49	I	IVPR_HDO	91	I	IHITSTJV	133	I/O	BSD_DQ21	175	O	OSD_AD9
8	I	IDEC2_5	50	I	IVPR_CFO	92	I	$\overline{\text{IRE6}}$	134	I/O	BSD_DQ22	176	O	OSD_AD10
9	I	IDEC2_6	51	—	GND	93	I	$\overline{\text{IRE7}}$	135	I/O	BSD_DQ23	177	—	GND
10	I	IDEC2_7	52	—	GND	94	I	$\overline{\text{IRE8}}$	136	—	GND	178	O	OFI_RE1
11	I	IDEC2_HD	53	—	VDD	95	I	$\overline{\text{IF4OE}}$	137	I/O	BSD_DQ24	179	O	OFI_RE2
12	I	IDEC2_VD	54	O	OVIDEO_0	96	I	ISEL1	138	I/O	BSD_DQ25	180	O	OFI_RE3
13	I	IDEC2_CF	55	O	OVIDEO_1	97	I	ISEL2	139	I/O	BSD_DQ26	181	O	OFI_RE4
14	I	IDEC2_PR	56	O	OVIDEO_2	98	I	ISEL3	140	I/O	BSD_DQ27	182	—	GND
15	I	IHDCTSTM	57	O	OVIDEO_3	99	I	ISEL4	141	—	VDD	183	—	VDD
16	I	IDLY1_0	58	O	OVIDEO_4	100	I	ITSTJV_0	142	I/O	BSD_DQ30	184	O	OFI_RSTR
17	I	IDLY1_1	59	O	OVIDEO_5	101	I	ITSTJV_1	143	I/O	BSD_DQ31	185	O	OFI_WE1
18	I	IDLY1_2	60	O	OVIDEO_6	102	I	ITSTJV_2	144	I/O	BSD_DQ32	186	O	OFI_WE2
19	I	IDLY1_3	61	O	OVIDEO_7	103	I	ITSTJV_3	145	I/O	BSD_DQ33	187	O	OFI_WE3
20	I	IDLY1_4	62	O	OHD	104	—	VDD	146	—	GND	188	O	OFI_WE4
21	I	IDLY1_5	63	O	OVD	105	—	GND	147	I/O	BSD_DQ34	189	O	OFI_RSTW
22	I	IDLY1_6	64	O	OCFI	106	—	GND	148	I/O	BSD_DQ35	190	—	GND
23	I	IDLY1_7	65	O	OPRT	107	I	ITSTJV_4	149	I/O	BSD_DQ36	191	I	I27MCLK
24	I	IDLY1_HD	66	—	GND	108	I	ITSTJV_5	150	I/O	BSD_DQ37	192	I	$\overline{\text{POR}}$
25	I	IDLY1_VD	67	O	ODLYCFI	109	I	ITSTJV_6	151	—	GND	193	I	$\overline{\text{ILSYSRST}}$
26	—	GND	68	O	OSDI_CF0	110	I	ITSTJV_7	152	O	$\overline{\text{OSD\_CS}}$	194	I	IH525
27	—	VDD	69	O	OSDI_CF1	111	I/O	BSD_DQ00	153	O	$\overline{\text{OSD\_RAS}}$	195	I	$\overline{\text{IDPR\_CS}}$
28	I	IDLY1_CF	70	O	OSDI_CF2	112	I/O	BSD_DQ01	154	O	$\overline{\text{OSD\_CAS}}$	196	I	$\overline{\text{IDPR\_STR}}$
29	I	IDLY1_PR	71	—	GND	113	I/O	BSD_DQ02	155	—	GND	197	I	IDPR_ST0
30	I	IDCTSTD	72	O	$\overline{\text{ORE6}}$	114	I/O	BSD_DQ03	156	—	GND	198	I	IDPR_ST1
31	I	IDEC1_0	73	O	$\overline{\text{ORE7}}$	115	—	VDD	157	—	VDD	199	—	GND
32	I	IDEC1_1	74	O	$\overline{\text{ORE8}}$	116	I/O	BSD_DQ04	158	O	$\overline{\text{OSD\_WE}}$	200	I/O	BDPR_D0
33	I	IDEC1_2	75	O	$\overline{\text{OF4OE}}$	117	I/O	BSD_DQ05	159	O	OSD_BS	201	I/O	BDPR_D1
34	I	IDEC1_3	76	O	OSEL1	118	I/O	BSD_DQ06	160	O	OSD_DQM0	202	I/O	BDPR_D2
35	I	IDEC1_4	77	O	OSEL2	119	I/O	BSD_DQ07	161	O	OSD_DQM1	203	I/O	BDPR_D3
36	I	IDEC1_5	78	—	VDD	120	—	GND	162	O	OSD_DQM2	204	I/O	BDPR_D4
37	I	IDEC1_6	79	—	GND	121	I/O	BSD_DQ10	163	O	OSD_DQM3	205	I/O	BDPR_D5
38	I	IDEC1_7	80	O	OSEL3	122	I/O	BSD_DQ11	164	O	OSD_DQM4	206	I/O	BDPR_D6
39	I	IDEC1_HD	81	O	OSEL4	123	I/O	BSD_DQ12	165	O	OSD_DQM5	207	I/O	BDPR_D7
40	I	IDEC1_VD	82	O	OTSTJV_0	124	I/O	BSD_DQ13	166	O	OSD_AD0	208	—	VDD
41	I	IDEC1_CF	83	O	OTSTJV_1	125	—	GND	167	O	OSD_AD1			
42	I	IDEC1_PR	84	O	OTSTJV_2	126	I/O	BSD_DQ14	168	O	OSD_AD2			

INPUT

I27MCLK	; CLOCK
IDCTSTD	; TEST
IDEC1_0 - IDEC1_7	; INPUT VIDEO-1
IDEC1_CF	; INPUT-1 CF INFORMATION
IDEC1_HD	; INPUT-1 H DRIVE
IDEC1_PR	; INPUT-1 PARITY
IDEC1_VD	; INPUT-1 V DRIVE
IDEC2_0 - IDEC2_7	; INPUT VIDEO-2
IDEC2_CF	; INPUT-2 CF INFORMATION
IDEC2_HD	; INPUT-2 H DRIVE
IDEC2_PR	; INPUT-2 PARITY
IDEC2_VD	; INPUT-2 V DRIVE
IDLY1_0 - IDLY1_7	; INPUT VIDEO-3
IDLY1_CF	; INPUT-3 CF INFORMATION
IDLY1_HD	; INPUT-3 H DRIVE
IDLY1_PR	; INPUT-3 PARITY
IDLY1_VD	; INPUT-3 V DRIVE
$\overline{\text{IDPR\_CS}}$	; CPU CHIP SELECT
IDPR_ST0, IDPR_ST1	; CPU STATUS 0, 1
$\overline{\text{IDPR\_STR}}$	; CPU STROBE
$\overline{\text{IF4OE}}$	; INTERNAL USED
IH525	; H: 525/L: 625 MODE
IHDCTSTM	; TEST
IHITSTJV	; TEST
IHOTSTJV	; TEST
$\overline{\text{ILSYSRST}}$	; CPU RESET
$\overline{\text{IRE6}}$ - $\overline{\text{IRE8}}$	; INTERNAL USED
IREF_CF	; REFERENCE CF INFORMATION
IREF_HD	; REFERENCE H DRIVE
IREF_VD	; REFERENCE V DRIVE
ISDIE_CF	; REFERENCE CF INFORMATION
ISDIE_HD	; REFERENCE H DRIVE
ISEL1 - ISEL4	; INTERNAL USED
ITSTJV0 - ITSTJV7	; TEST
IVPR_CFO	; VIDEO PROCESSOR CF INFORMATION OUTPUT
IVPR_HDO	; VIDEO PROCESSOR H DRIVE OUTPUT
$\overline{\text{POR}}$	; POWER ON RESET

OUTPUT

OCFI	; OUTPUT CF INFORMATION
ODLYCFI	; DELAYED CF INFORMATION
OHD	; OUTPUT H DRIVE
$\overline{\text{OF4OE}}$	; INTERNAL USED
OFI_RE1 - OFI_RE4	; FRAME MEMORY READ ENABLE 1 - 4
OFI_RSTR	; FRAME MEMORY READ RESET
OFI_RSTW	; FRAME MEMORY WRITE RESET
OFI_WE1 - OFI_WE4	; FRAME MEMORY WRITE ENABLE 1 - 4
OPRT	; OUTPUT PARITY
$\overline{\text{ORE6}}$ - $\overline{\text{ORE8}}$	; INTERNAL USED
OSD_AD0 - OSD_AD10	; SDRAM ADDRESS BUS
OSD_BS	; SDRAM BANK SELECT
$\overline{\text{OSD\_CAS}}$	; SDRAM COLUMN ADDRESS STROBE
$\overline{\text{OSD\_CS}}$	; SDRAM CHIP SELECT
OSD_DQM0 - OSD_DQM5	; SDRAM INPUT/OUTPUT MASK 0 - 5
OSDI_CF0 - OSDI_CF2	; TIMING PULSE
$\overline{\text{OSD\_RAS}}$	; SDRAM ROW ADDRESS STROBE
$\overline{\text{OSD\_WE}}$	; SDRAM WRITE ENABLE
OSEL1 - OSEL4	; INTERNAL USED
OTSTJV_0 - OTSTJV_7	; TEST
OVD	; OUTPUT V DRIVE
OVIDEO_0 - OVIDEO_7	; OUTPUT VIDEO 0-7

INPUT/OUTPUT

BDPR_D0 - BDPR_D7	; CPU DATA BUS
BSD_DQ00 - BSD_DQ07	; SDRAM DATA BUS-0
BSD_DQ10 - BSD_DQ17	; SDRAM DATA BUS-1
BSD_DQ20 - BSD_DQ27	; SDRAM DATA BUS-2
BSD_DQ30 - BSD_DQ37	; SDRAM DATA BUS-3