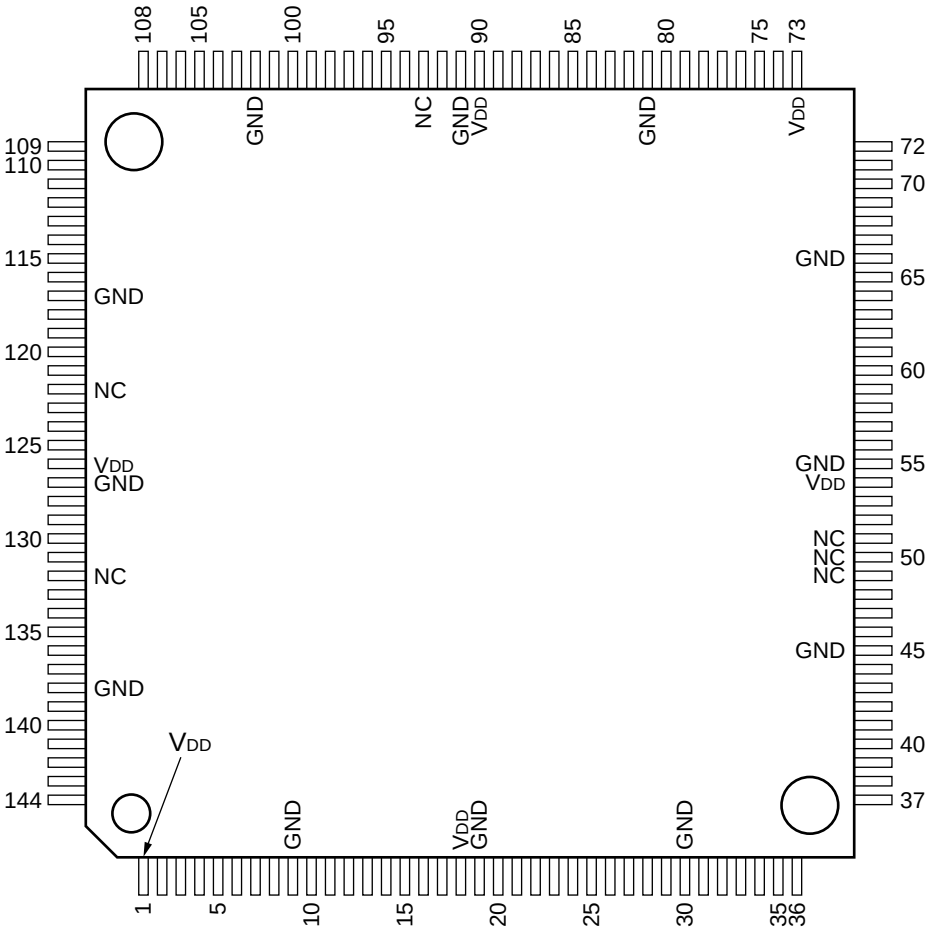
C-MOS TIMING GENERATOR AND VIDEO DATA SELECTOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V_{DD}	37	O	VD6	73	—	V_{DD}	109	I/O	SYIO7
2	O	HD0	38	O	CF6	74	I	AINPR	110	I/O	SYIO6
3	O	VD0	39	O	FR6	75	I	AINCF	111	I/O	SYIO5
4	O	CF0	40	O	GOP6	76	I	AINVD	112	I/O	SYIO4
5	O	FR0	41	O	HD7	77	I	AINHD	113	I/O	SYIO3
6	O	GOP0	42	O	VD7	78	I	AIN9	114	I/O	SYIO2
7	O	HD1	43	O	CF7	79	I	AIN8	115	I/O	SYIO1
8	O	VD1	44	O	FR7	80	I	AIN7	116	I/O	SYIO0
9	—	GND	45	—	GND	81	—	GND	117	—	GND
10	O	CF1	46	O	GOP7	82	I	AIN6	118	I	STAT1
11	O	FR1	47	O	SVREF	83	I	AIN5	119	I	STAT0
12	O	GOP1	48	O	SWSET	84	I	AIN4	120	I	STRB
13	O	HD2	49	—	NC	85	I	AIN3	121	I	$\overline{CS}$
14	O	VD2	50	—	NC	86	I	AIN2	122	—	NC
15	O	CF2	51	—	NC	87	I	AIN1	123	I	AUCK
16	O	FR2	52	O	RSV1	88	I	AIN0	124	I	AUPIN
17	O	GOP2	53	O	RSV0	89	I	$\overline{CLR}$	125	O	AUPOUT
18	—	V_{DD}	54	—	V_{DD}	90	—	V_{DD}	126	—	V_{DD}
19	—	GND	55	—	GND	91	—	GND	127	—	GND
20	O	HD3	56	O	ENCFR	92	O	SWAB	128	I/O	ADVCF2
21	O	VD3	57	O	ENCGOP	93	—	NC	129	I/O	ADVCF1
22	O	CF3	58	O	OUTPR	94	I	BINPR	130	I/O	ADVCF0
23	O	FR3	59	O	OUTCF	95	I	BINCF	131	I	TEST5
24	O	GOP3	60	O	OUTVD	96	I	BINVD	132	—	NC
25	O	HD4	61	O	OUTHD	97	I	BINHD	133	I	TEST3
26	O	VD4	62	O	OUT9	98	I	BIN9	134	I	TEST2
27	O	CF4	63	O	OUT8	99	I	BIN8	135	I	TEST1
28	O	FR4	64	O	OUT7	100	I	BIN7	136	I	TEST0
29	O	GOP4	65	O	OUT6	101	I	BIN6	137	I	CK
30	—	GND	66	—	GND	102	—	GND	138	—	GND
31	O	HD5	67	O	OUT5	103	I	BIN5	139	I	ICF2
32	O	VD5	68	O	OUT4	104	I	BIN4	140	I	ICF1
33	O	CF5	69	O	OUT3	105	I	BIN3	141	I	ICF0
34	O	FR5	70	O	OUT2	106	I	BIN2	142	I	IVST
35	O	GOP5	71	O	OUT1	107	I	BIN1	143	I	REFHD
36	O	HD6	72	O	OUT0	108	I	BIN0	144	I	REFCF

INPUT

AIN0 - AIN9	;	VIDEO DATA (FOR CH-A)
AINCF	;	COLOR FRAMING INFORMATION (FOR CH-A)
AINHD	;	HORIZONTAL DRIVE (FOR CH-A)
AINPR	;	PARITY BIT (FOR CH-A)
AINVD	;	VERTICAL DRIVE (FOR CH-A)
AUCK	;	24 MHz AUDIO CLOCK
AUPIN	;	AUDIO PULSE
BIN0 - BIN9	;	VIDEO DATA (FOR CH-B)
BINCF	;	COLOR FRAMING INFORMATION (FOR CH-B)
BINHD	;	HORIZONTAL DRIVE (FOR CH-B)
BINPR	;	PARITY BIT (FOR CH-B)
BINVD	;	VERTICAL DRIVE (FOR CH-B)
CK	;	27 MHz SYSTEM CLOCK
$\overline{CLR}$	;	CLEAR
$\overline{CS}$	;	CHIP SELECT
ICF0 - ICF2	;	INPUT COLOR FRAMING INFORMATION FROM TBC BUFFER
IVST	;	INPUT VERTICAL START FROM TBC BUFFER
REFCF	;	REFERENCE COLOR FRAMING
REFHD	;	REFERENCE HD
STAT0, STAT1	;	STATUS BIT
STRB	;	STROBE PULSE
TEST0 - TEST5	;	TEST

OUTPUT

AUPOUT	;	AUDIO PULSE
CF0 - CF7	;	COLOR FRAMING INFORMATION
ENCFR	;	REFERENCE FRAME PULSE (FOR READ)
ENCGOP	;	REFERENCE GOP PULSE (FOR WRITE)
FR0 - FR7	;	FRAME
GOP0 - GOP7	;	GOP
HD0 - HD7	;	HORIZONTAL DRIVE
OUT0 - OUT9	;	SELECT VIDEO DATA
OUTCF	;	COLOR FRAMING INFORMATION FOR SELECT DATA
OUTHD	;	HORIZONTAL DRIVE FOR SELECT DATA
OUTPR	;	PARITY BIT FOR SELECT DATA
OUTVD	;	VERTICAL DRIVE FOR SELECT DATA
RSV0, RSV1	;	RESERVE DATA
SVREF	;	SERVO REFERENCE PULSE
SWAB	;	CH-A/CH-B DATA SELECT
SWSET	;	COLOR FRAMING SETTING PULSE
VD0 - VD7	;	VERTICAL DRIVE

INPUT/OUTPUT

ADVCF0 - ADVCF2	;	0.5 FIELD ADVANCED REFERENCE CF INF.
SYIO0 - SYIO7	;	SYSTEM CONTROL DATA BUS