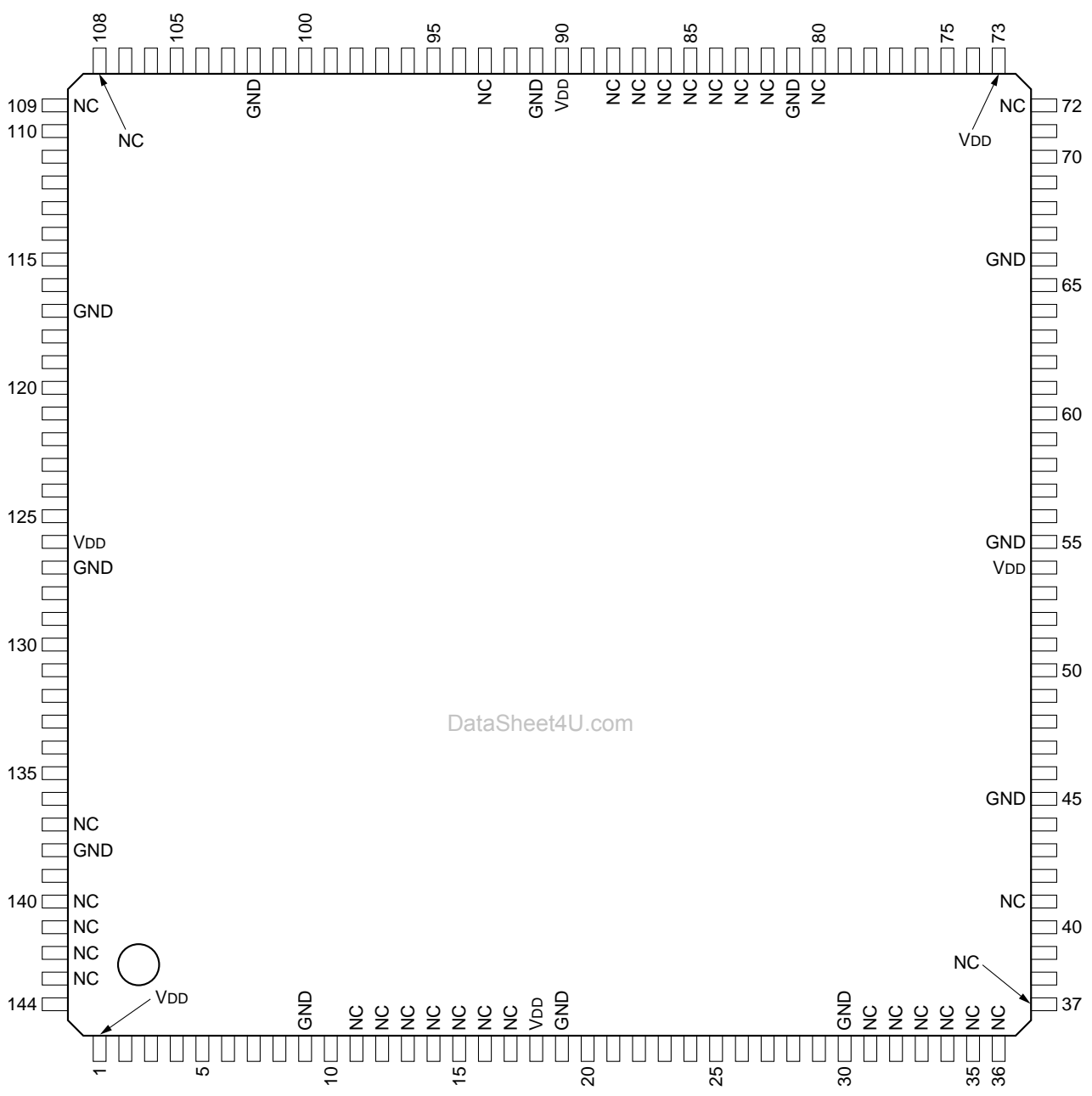


C-MOS HD-SDI COPROCESSOR

—TOP VIEW—



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PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD	37	—	NC	73	—	VDD	109	—	NC
2	I	IP AUDA12	38	O	OP AUEFLG	74	O	OP MYFLG1	110	I	IP YDATA0
3	I	IP AUDA34	39	O	OP YADFLG	75	O	OP MYFLG2	111	I	IP YDATA1
4	I	IP AUDA56	40	O	OP CADFLG	76	O	OP MYFLG3	112	I	IP YDATA2
5	I	IP AUDA78	41	—	NC	77	O	OP MYFLG4	113	I	IP YDATA3
6	I	IP CKUA	42	O	OP CK74	78	O	OP MYFLG5	114	I	IP YDATA4
7	I	IP FSA	43	O	OP EXTV	79	I	IP RESET	115	I	IP YDATA5
8	I	IP CKUA	44	O	OP EXTH	80	—	NC	116	I	IP YDATA6
9	—	GND	45	—	GND	81	—	GND	117	—	GND
10	I	IP FSB	46	O	OP EXTF	82	—	NC	118	I	IP YDATA7
11	—	NC	47	O	OP CDATA10	83	—	NC	119	I	IP YDATA8
12	—	NC	48	O	OP CDATA9	84	—	NC	120	I	IP YDATA9
13	—	NC	49	O	OP CDATA8	85	—	NC	121	I	IP YDATA10
14	—	NC	50	O	OP CDATA7	86	—	NC	122	I	IP CDATA0
15	—	NC	51	O	OP CDATA6	87	—	NC	123	I	IP CDATA1
16	—	NC	52	O	OP CDATA5	88	—	NC	124	I	IP CDATA2
17	—	NC	53	O	OP CDATA4	89	O	OP SYRW	125	I	IP CDATA3
18	—	VDD	54	—	VDD	90	—	VDD	126	—	VDD
19	—	GND	55	—	GND	91	—	GND	127	—	GND
20	O	OP AUUCKPH1	56	O	OP CDATA3	92	I	IP SYRW	128	I	IP CDATA4
21	O	OP AUUCKPH2	57	O	OP CDATA2	93	—	NC	129	I	IP CDATA5
22	O	OP AUEFLG12	58	O	OP CDATA1	94	I	IP XMM	130	I	IP CDATA6
23	O	OP AUEFLG34	59	O	OP CDATA0	95	I	IP SYCS	131	I	IP CDATA7
24	O	OP AUEFLG56	60	O	OP YDATA10	96	I	IP SYCK	132	I	IP CDATA8
25	O	OP AUEFLG78	61	O	OP YDATA9	97	I	IP SYSTA1	133	I	IP CDATA9
26	O	OP AUDA12	62	O	OP YDATA8	98	I	IP SYSTA0	134	I	IP CDATA10
27	O	OP AUDA34	63	O	OP YDATA7	99	I/O	BP SYIF7	135	I	IP EXTF
28	O	OP AUDA56	64	O	OP YDATA6	100	I/O	BP SYIF6	136	I	IP EXTH
29	O	OP AUDA78	65	O	OP YDATA5	101	I/O	BP SYIF5	137	—	NC
30	—	GND	66	—	GND	102	—	GND	138	—	GND
31	—	NC	67	O	OP YDATA4	103	I/O	BP SYIF4	139	I	IP CK74
32	—	NC	68	O	OP YDATA3	104	I/O	BP SYIF3	140	—	NC
33	—	NC	69	O	OP YDATA2	105	I/O	BP SYIF2	141	—	NC
34	—	NC	70	O	OP YDATA1	106	I/O	BP SYIF1	142	—	NC
35	—	NC	71	O	OP YDATA0	107	I/O	BP SYIF0	143	—	NC
36	—	NC	72	—	NC	108	—	NC	144	O	OP ERR