DATA SHEET



DARLINGTON POWER TRANSISTOR 2SD1592

NPN SILICON TRIPLE DIFFUSED TRANSISTOR (DARLINGTON CONNECTION) FOR HIGH-VOLTAGE LOW-SPEED SWITCHING

FEATURES

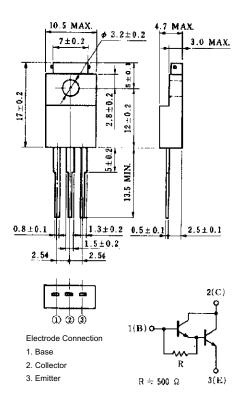
- High DC current gain due to Darlington connection
- · Low collector saturation
- Reverse deterrence type
- Ideal for use in devices such as pulse motor drivers and relay drivers of PC terminals, and ignitors of general-purpose engines.
- Mold package that does not require an insulating board or insulation bushing

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	Vсво	500	V
Collector to emitter voltage	VCEO	+300, –10	V
Emitter to base voltage	Vebo	10	V
Collector current	IC(DC)	5.0	А
Collector current	IC(pulse)*	10	А
Base current	IB(DC)	0.5	А
Total power dissipation	P⊤ (Tc = 25°C)	30	W
Total power dissipation	P⊤ (Ta = 25°C)	1.5	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

* PW \leq 300 μ s, duty cycle \leq 10%

PACKAGE DRAWING (UNIT: mm)



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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

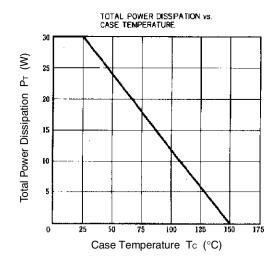
www.DataSarameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	Vcb = 400 V, IE = 0			10	μA
DC current gain	hfe1*	Vce = 2.0 V, Ic = 2.0 A	400		3,000	
DC current gain	hfe2*	Vce = 2.0 V, Ic = 3.0 A	100			
Collector saturation voltage	V _{CE(sat)} *	Ic = 2 A, Iв = 5 mA		1.0	1.5	V
Base saturation voltage	V _{BE(sat)} *	Ic = 2 A, Iв = 5 mA		1.6	2.0	V
Turn-on time	ton	Ic = 3.0 A, Ib1 = $-I_{B2}$ = 30 mA RL = 50 Ω , Vcc \cong 150 V		1.0		μs
Storage time	tstg			12		μs
Fall time	tr			6		μs

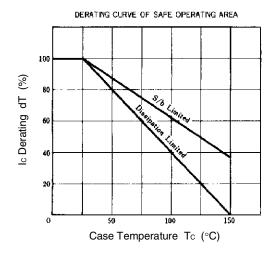
* Pulse test PW \leq 350 μ s, duty cycle \leq 2%

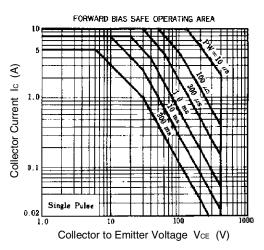
hfe CLASSIFICATION

Marking	М	L	к
hfe	400 to 800	600 to 1,200	1,000 to 3,000

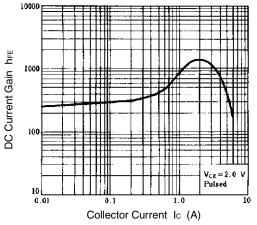
TYPICAL CHARACTERISTICS (Ta = 25°C)

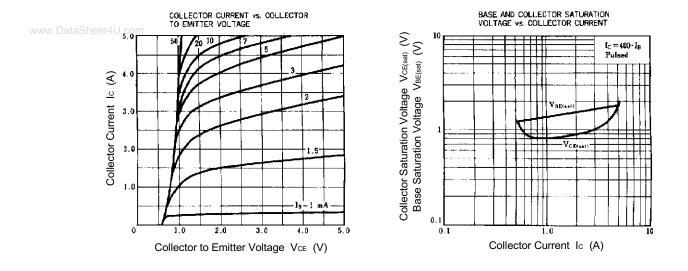






DC CURRENT GAIN vs. COLLECTOR CURRENT





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