

**Silicon NPN Power Transistors**

**2SD2095**

**DESCRIPTION**

- With TO-3P(H)IS package
- Built-in damper diode
- High voltage ,high speed
- Low collector saturation voltage

**APPLICATIONS**

- For color TV horizontal output applications

**PINNING**

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

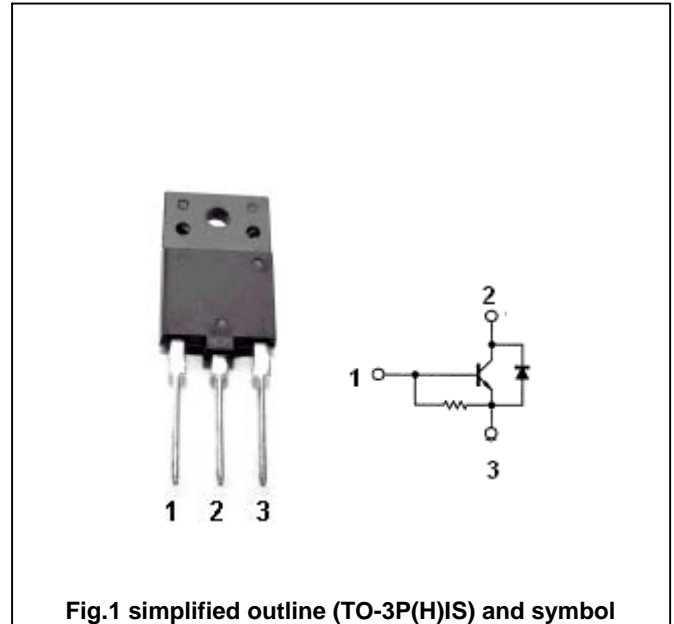


Fig.1 simplified outline (TO-3P(H)IS) and symbol

**ABSOLUTE MAXIMUM RATINGS AT Tc=25**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	1500	V
$V_{CEO}$	Collector-emitter voltage	Open base	600	
$V_{EBO}$	Emitter-base voltage	Open collector	5	V
$I_C$	Collector current		5	A
$I_B$	Base current		2.5	A
$P_C$	Collector power dissipation		50	W
$T_j$	Junction temperature		150	
$T_{stg}$	Storage temperature		-55~150	

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## CHARACTERISTICS

T<sub>j</sub>=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>EB0</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =200mA , I <sub>C</sub> =0	5			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =3.5A; I <sub>B</sub> =0.8A		3.0	5.0	V
V <sub>BEsat</sub>	Emitter-base saturation voltage	I <sub>C</sub> =3.5A; I <sub>B</sub> =0.8A			1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =500V; I <sub>E</sub> =0			10	μ A
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =1A ; V <sub>CE</sub> =5V	8			
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =0.1A ; V <sub>CE</sub> =10V		3		MHz
C <sub>OB</sub>	Collector output capacitance	I <sub>E</sub> =0 ; V <sub>CB</sub> =10V;f=1MHz		105		pF
V <sub>F</sub>	Diode forward voltage	I <sub>F</sub> =5A		1.6	2.0	V
t <sub>f</sub>	Fall time	I <sub>CP</sub> =3.5A ; I <sub>B1(end)</sub> =0.8A		0.5	1.0	μ s

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PACKAGE OUTLINE

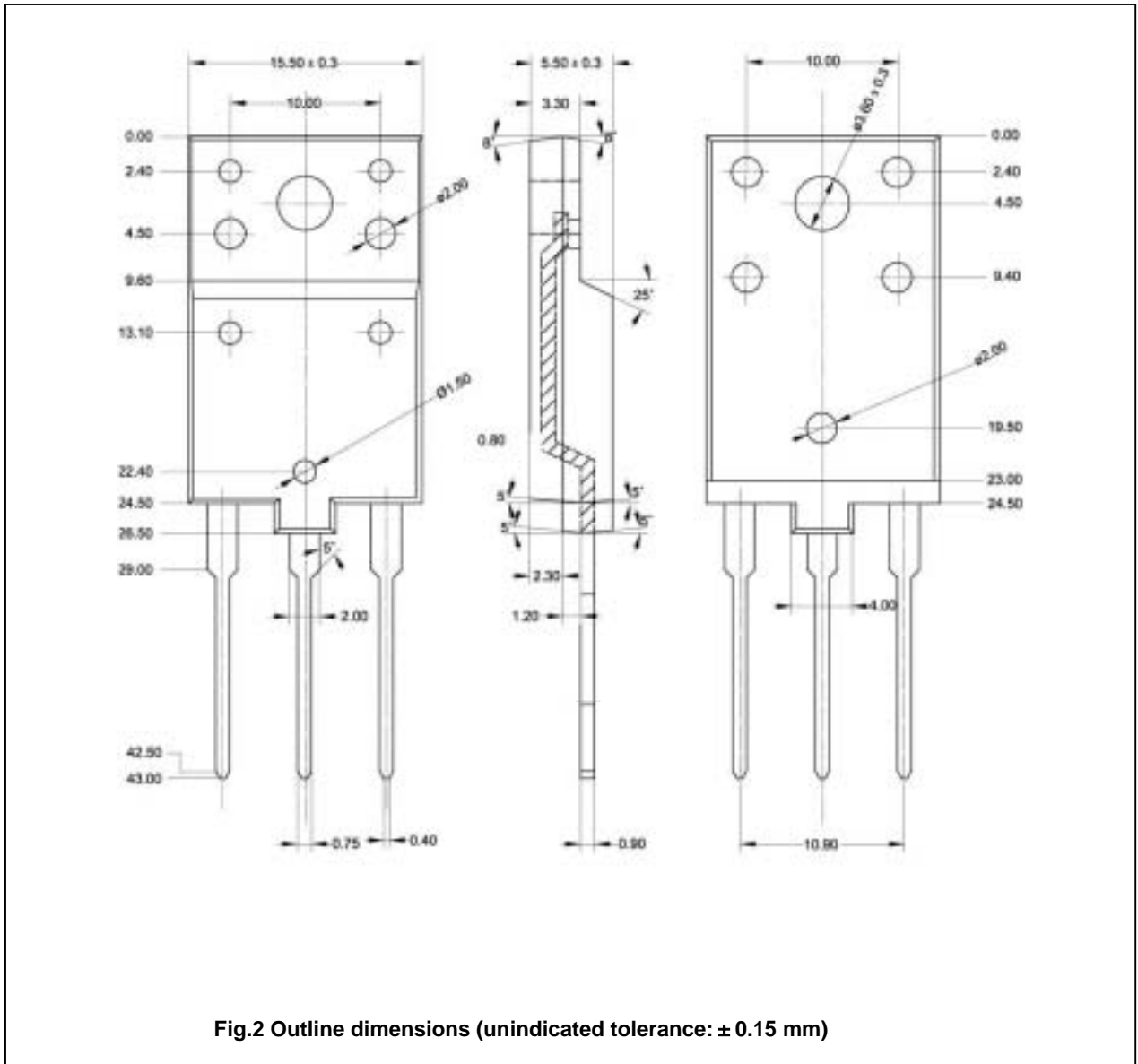


Fig.2 Outline dimensions (unindicated tolerance: ± 0.15 mm)