

## D20N06E

20 Amps, 55 Volts N-CHANNEL Power MOSFET

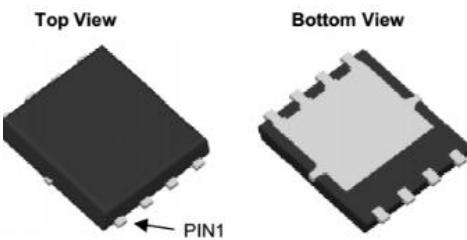
### FEATURE

- 20A, 55V,  $R_{DS(ON)MAX}=15\text{m}\Omega$ ,  $V_{GS}=10\text{V}/5\text{A}$
- Low gate charge
- Low  $C_{iss}$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

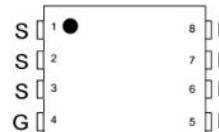
### APPLICATION

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- LCD/LED back light

DFN5\*6

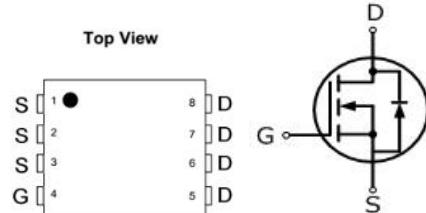


Top View



Bottom View

Top View



### GENERAL DESCRIPTION

The D20N06E is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The D20N06E meet the RoHS and Green product requirement, 100% EAS guaranteed with full function reliability approved.

### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	D20N06E	UNIT
Drain-Source Voltage	$V_{DSS}$	55	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	
Continuous Drain Current	$I_D$	20	A
Pulsed Drain Current (Note 1)	$I_{DM}$	80	
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	20	mJ
Avalanche Current	$I_{AS}$	20	A
Reverse Diode dv/dt (Note 3)	dv/dt	5.5	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	°C
Channel Temperature	$T_{CH}$	150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	260	°C

### Thermal Characteristics

Parameter	Symbol	MAX	Units
Thermal resistance, Channel to Case	$R_{th(ch-c)}$	2.7	°C/W
Thermal resistance, Channel to Ambient	$R_{th(ch-a)}$	55	°C/W
Maximum Power Dissipation	$T_c=25^\circ\text{C}$	$P_D$	W

Electrical Characteristics ( $T_c=25^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\text{uA}$	55	—	—	V
Breakdown Temperature Coefficient $/\Delta T_J$	$\Delta \text{BV}_{\text{DSS}}$	Reference to $25^\circ\text{C}$ , $\text{I}_D=250\text{uA}$	—	0.036	—	$\text{V}/^\circ\text{C}$
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=55\text{V}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1	$\mu\text{A}$
	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=55\text{V}, \text{V}_{\text{GS}}=0\text{V} (\text{T}_J = 55^\circ\text{C})$	—	—	5	$\mu\text{A}$
Gate-Body Leakage Current, Forward	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	$\pm 100$	nA
<b>On Characteristics</b>						
Gate-Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=250\text{uA}$	1	—	3	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=5\text{A}$	—	11.5	15	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=5\text{A}$	—	12.7	17	
<b>Dynamic Characteristics</b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1.0\text{MHz}$	—	1340	—	pF
Output Capacitance	$\text{C}_{\text{oss}}$		—	123	—	pF
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		—	10	—	pF
<b>Switching Characteristics</b>						
Turn-On Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=30\text{V}, \text{R}_G=3\Omega,$ $\text{R}_L=1.5\Omega$ $\text{V}_{\text{GS}}=10\text{V}$ (Note 4,5)	—	6	—	ns
Turn-On Rise Time	$t_r$		—	2.5	—	ns
Turn-Off Delay Time	$t_{\text{d(off)}}$		—	22	—	ns
Turn-Off Fall Time	$t_f$		—	2.5	—	ns
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=30\text{V}, \text{I}_D=20\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$ (Note 4,5)	—	21	—	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		—	4.7	—	nC
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		—	2.6	—	nC
<b>Drain-Source Body Diode Characteristics and Maximum Ratings</b>						
Continuous Diode Forward Current	$\text{I}_S$	$\text{V}_G = \text{V}_D = 0\text{V}$ , Force Current	—	—	20	A
Pulsed Diode Forward Current	$\text{I}_{\text{SM}}$		—	—	80	A
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_S=1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1	V

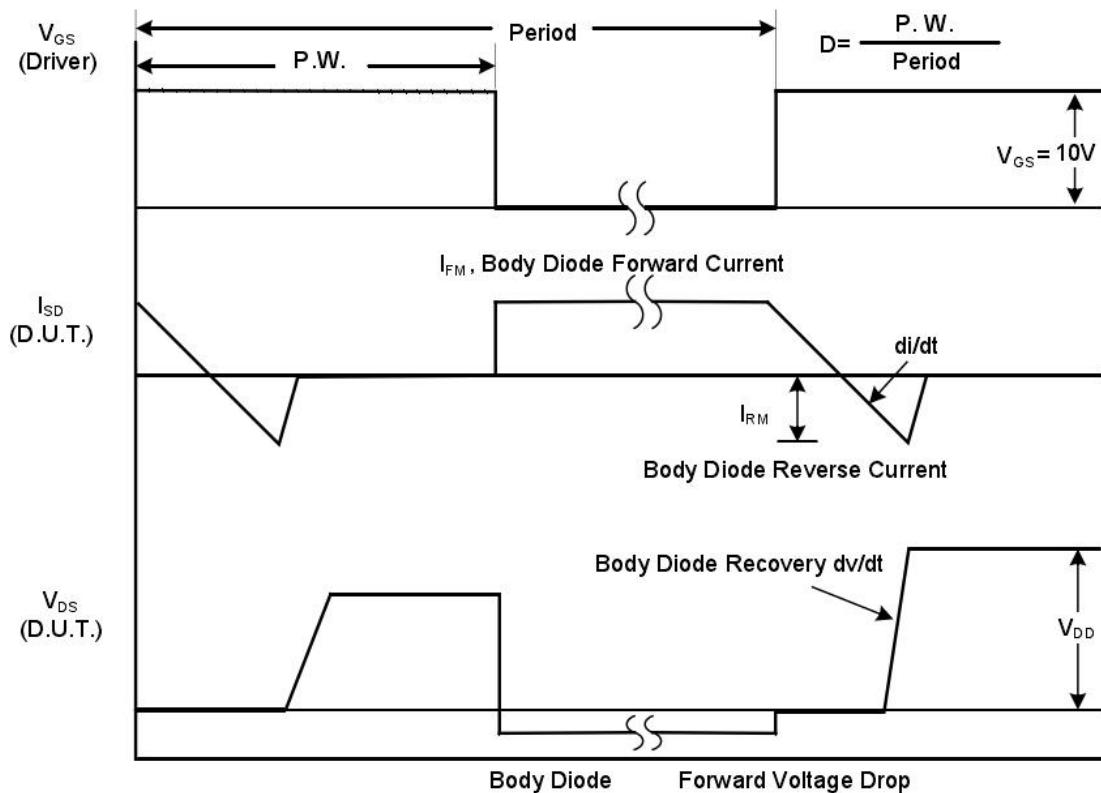
#### Notes

- Repetitive Rating: pulse width limited by maximum junction temperature.
- $\text{V}_{\text{DD}}=25\text{V}, \text{L}=0.1\text{mH}, \text{R}_g=25\Omega, \text{I}_{\text{AS}}=20\text{A}$ , starting  $\text{T}_J=25^\circ\text{C}$ .
- $\text{I}_{\text{SD}} \leq \text{I}_{\text{D}}, \text{dI}/\text{dt}=200\text{A/us}, \text{V}_{\text{DD}} \leq \text{BV}_{\text{DSS}}$ , starting  $\text{T}_J=25^\circ\text{C}$ , Pulse width  $\leq 300\text{us}$ ; duty cycle  $\leq 2\%$ .
- Repetitive rating; pulse width limited by maximum junction temperature.

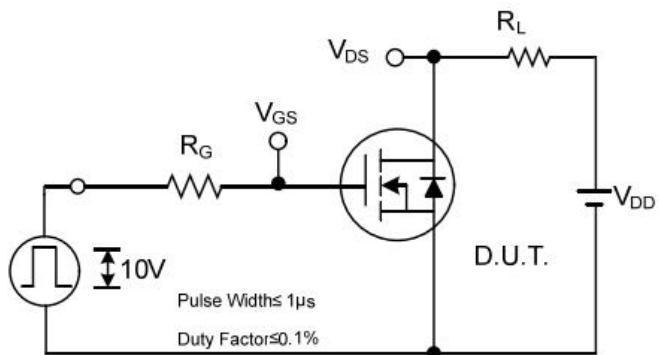
## RATING AND CHARACTERISTIC CURVES



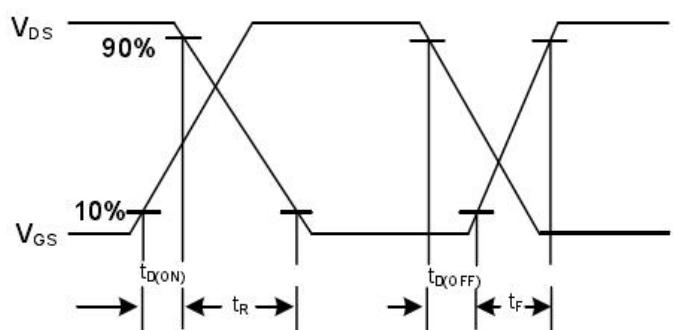
Peak Diode Recovery dv/dt Test Circuit



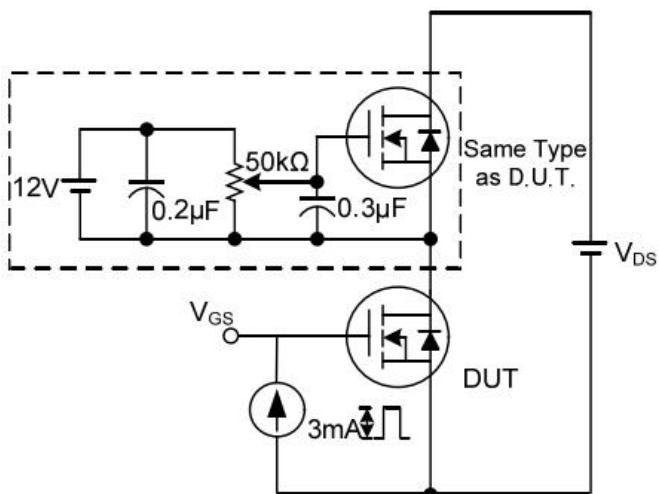
Peak Diode Recovery dv/dt Waveforms



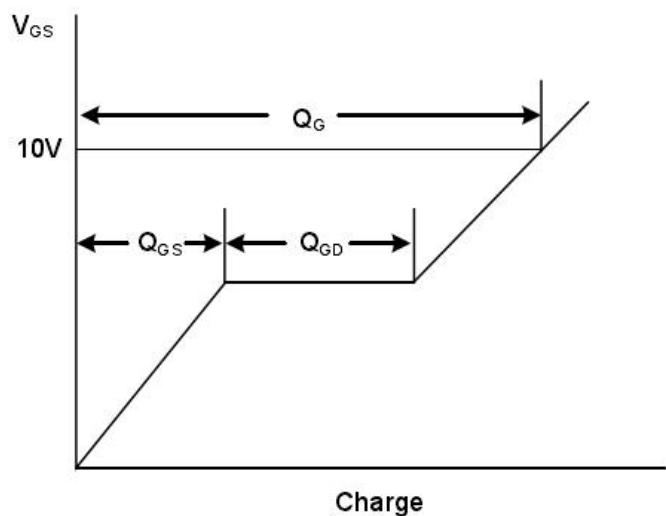
Switching Test Circuit



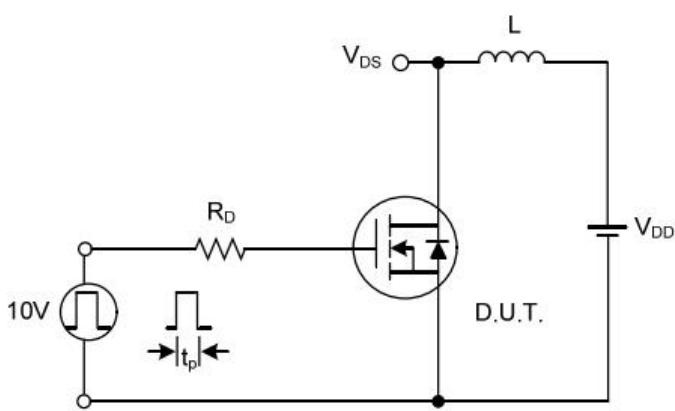
Switching Waveforms



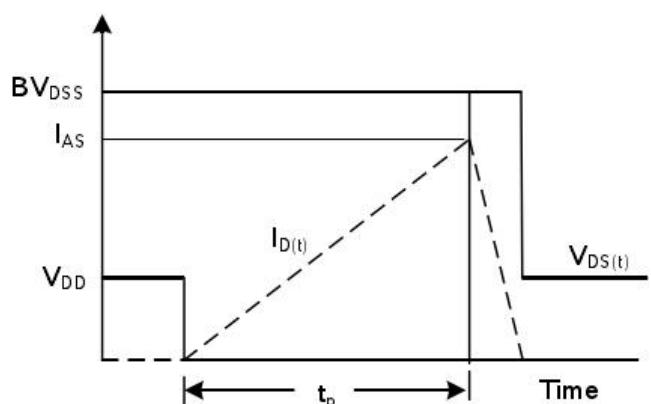
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

## RATING AND CHARACTERISTIC CURVES

