

D3CE60VE

General Rectifying Diodes 600V, 3.5A

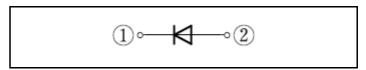
Feature

- Ultra-small SMD
- Ultra-thin PKG=1.0mm
- · Based on AEC-Q101
- · Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): CE
Package (JEITA Code): SC-110B

Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tl=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	°C
Junction temperature	Tj		-55 to 150	°C
Repetitive peak reverse voltage	V_{RRM}		600	V
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, TI=93°C	3.5	Α
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, TI=103°C	3	Α
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C *	1.2	А
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C *	0.8	А
Surge forward current	I _{FSM}	50Hz sine wave, Non-repetitive 1 cycle peak value, Tj=25°C	60	Α
Surge forward current	I _{FSM1}	tp=1ms, sine wave, Non-repetitive, peak value, Tj=25°C 120		Α

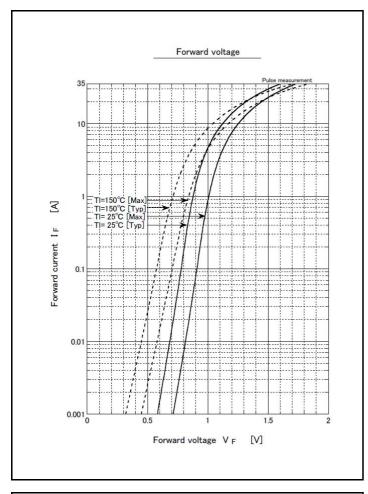
^{* :} See the original Specifications

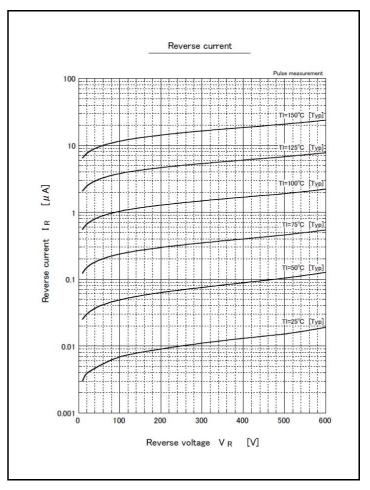
Electrical Characteristics (unless otherwise specified : TI=25°C)

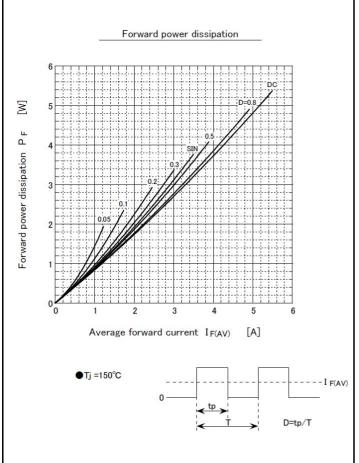
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	Oilit
Forward voltage	V _F	IF=3.5A, Pulse measurement			1.1	V
Reverse current	I _R	VR=600V, Pulse measurement			10	μΑ
Electro static dischange Capability	V _{ESD}	C=330pF, R=330Ω, Polarity±, Aerial discharge		25		kV
Thermal resistance	Rth(j-l)	Junction to lead			15	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On glass-epoxy substrate *			115	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On glass-epoxy substrate *			172	°C/W

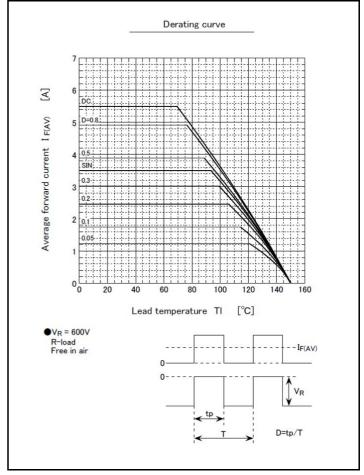
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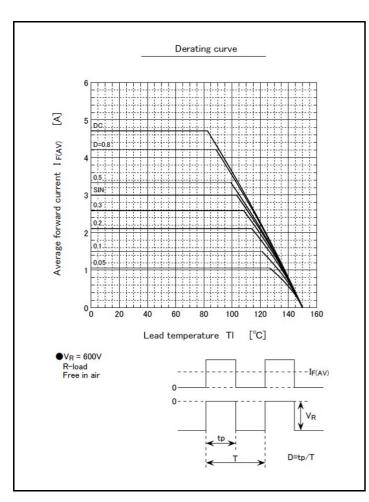
CHARACTERISTIC DIAGRAMS

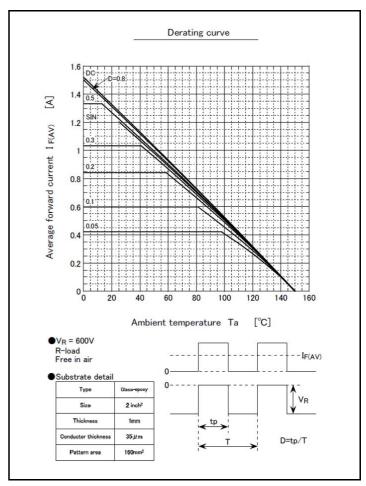


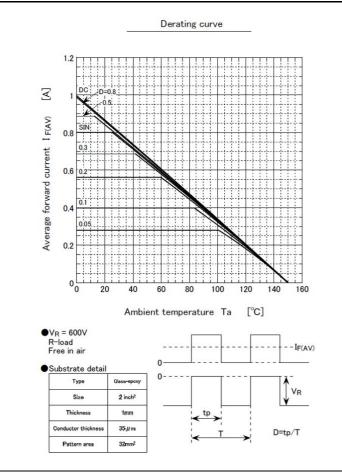


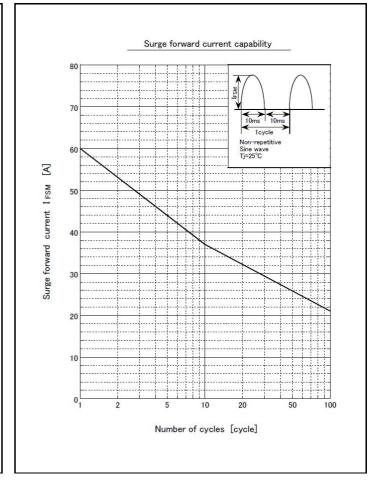


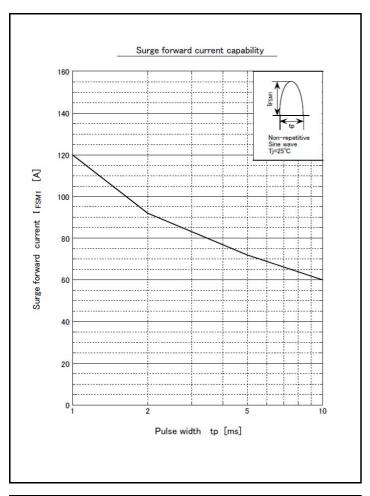


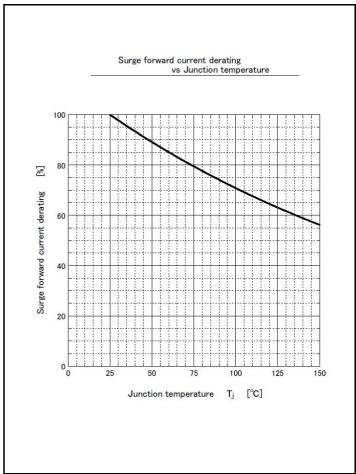


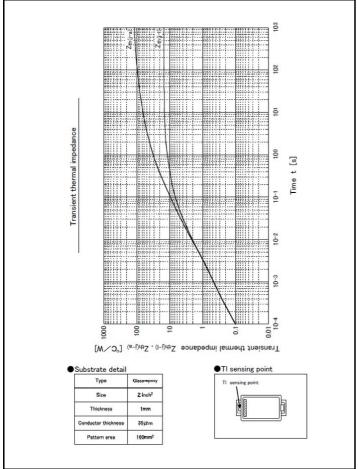


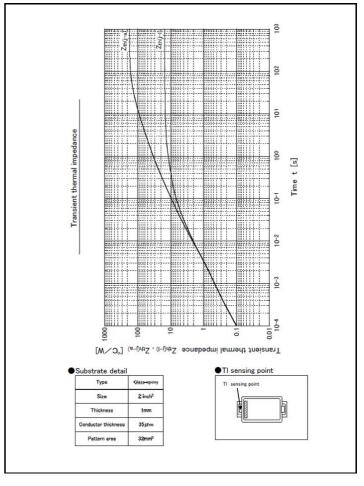








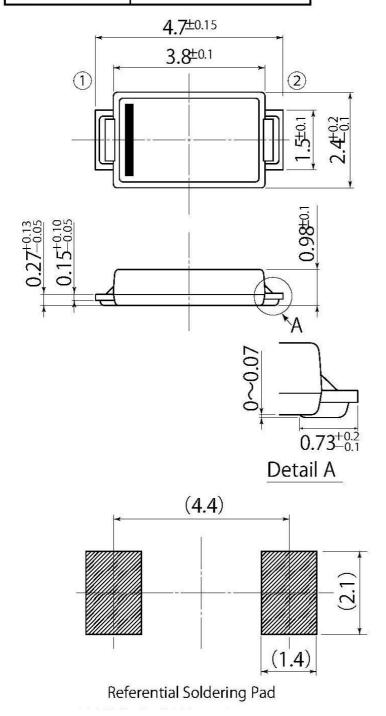


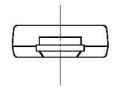


scale: 10/1

B5

JEDEC Code	()
JEITA Code	SC-110B
House Name	CE





[•] Optimize soldering pad to the board design and soldering condition.

Notes

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(Special applications)

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