

D5FEC3SH

Schottky Barrier Diodes
30V, 5A

Feature

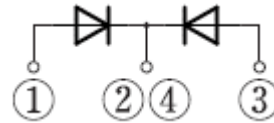
- SMD
- Ultra low V_F
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FE
Package (JEDEC Code): TO-252AB similar
Package (JEITA Code): SC-63



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : $T_c=25^{\circ}C$)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T_{stg}		-55 to 125	$^{\circ}C$
Junction temperature	T_j		-55 to 125	$^{\circ}C$
Repetitive peak reverse voltage	V_{RRM}		30	V
Average forward current	$I_F(AV)$	50Hz sine wave, Resistance load, Rating for each diode $I_F(AV)/2$, $T_c=110^{\circ}C$ *	5	A
Surge forward current	I_{FSM}	50Hz sine wave, Non-repetitive, 1 cycle, Peak value, $T_j=25^{\circ}C$	100	A

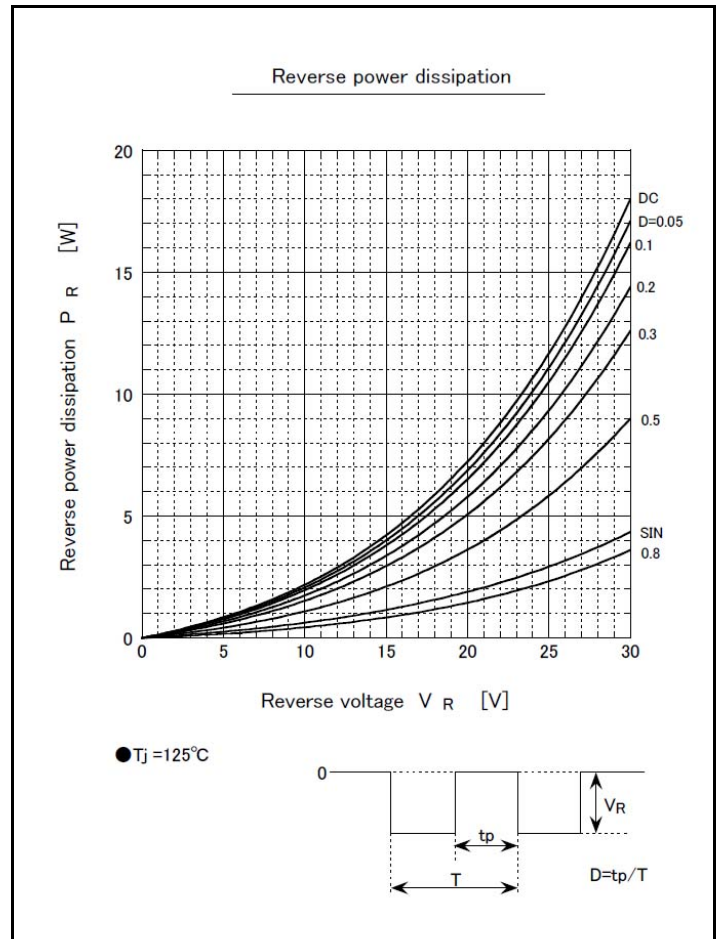
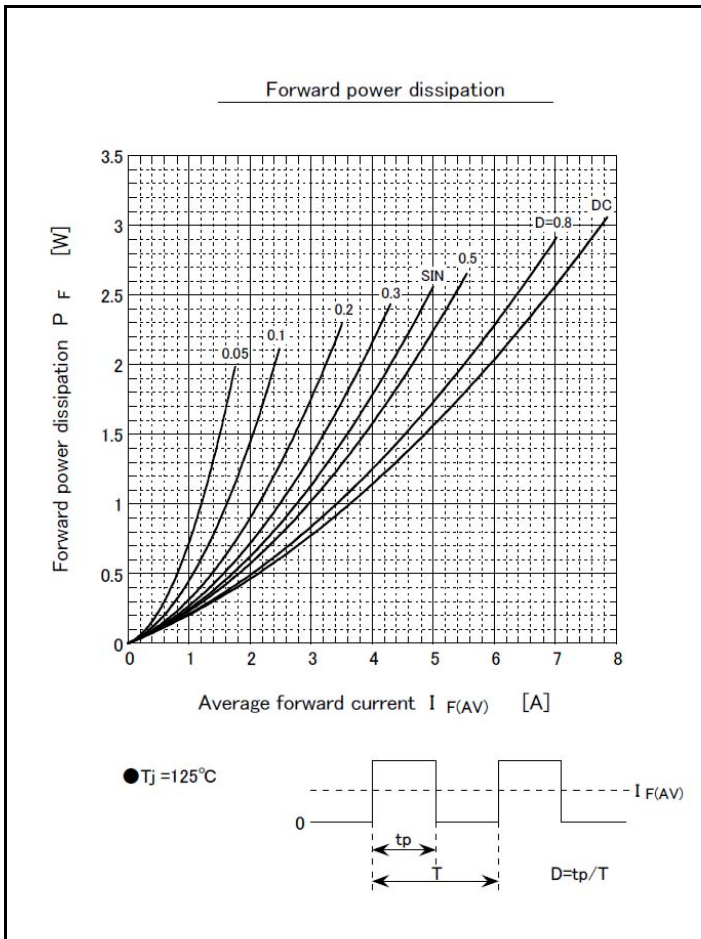
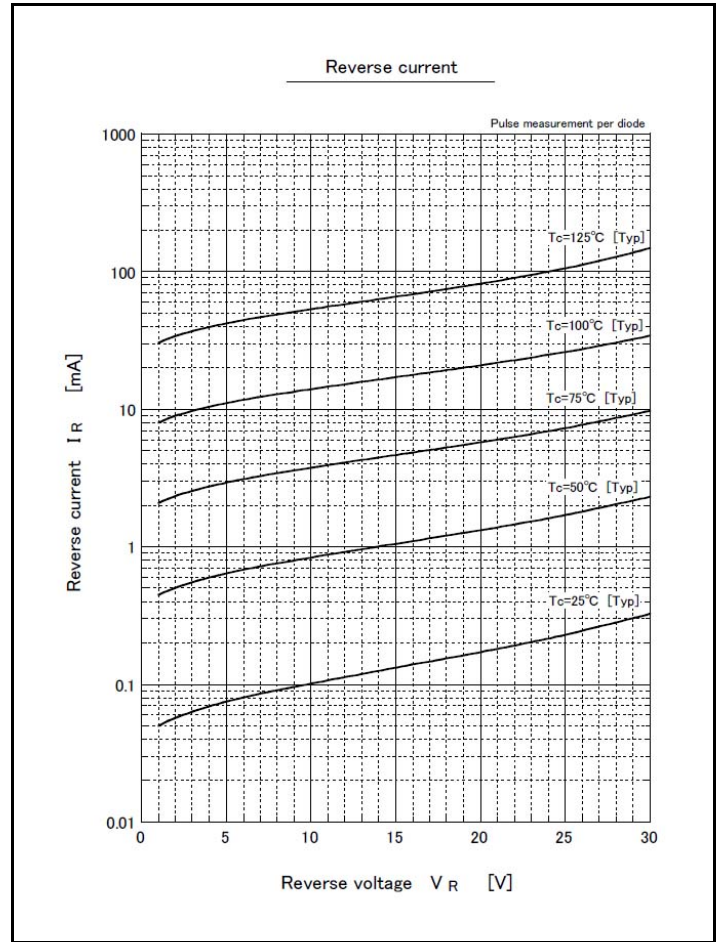
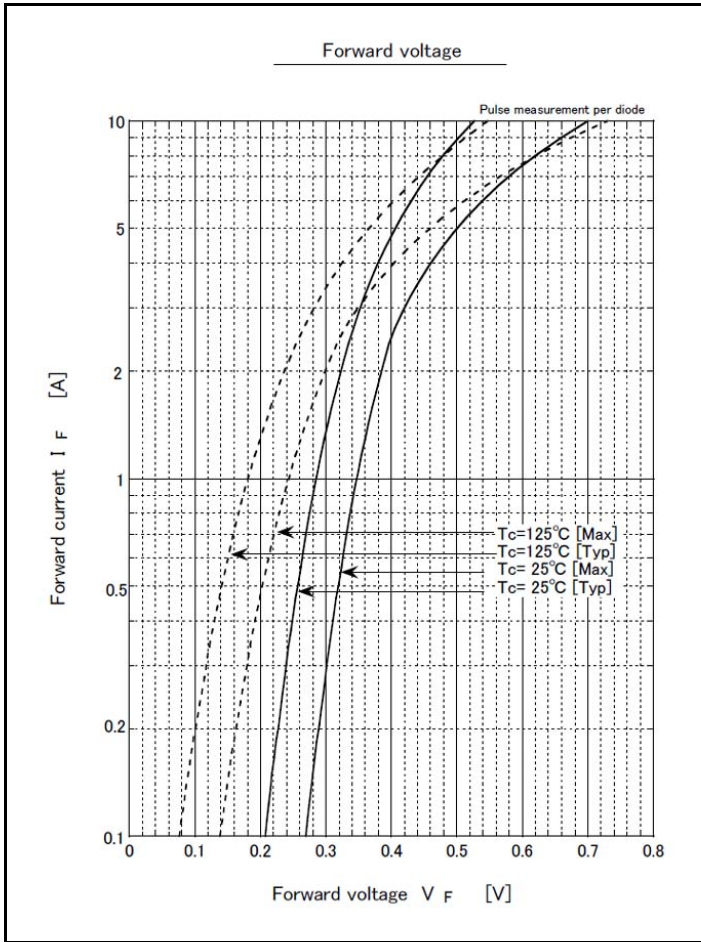
* :See the original Specifications

Electrical Characteristics (unless otherwise specified : $T_c=25^{\circ}C$)

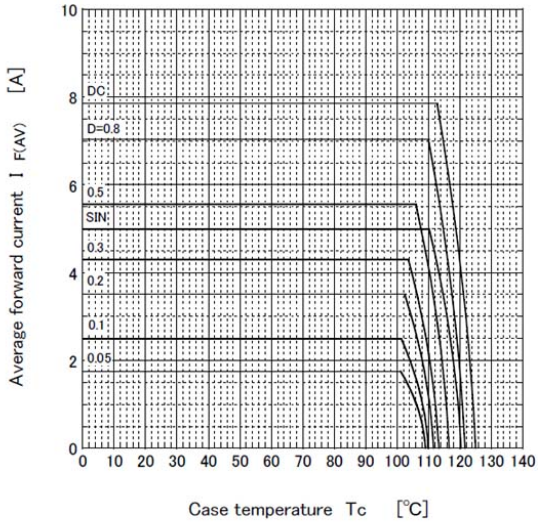
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Forward voltage	V_F	$I_F=2.5A$, Pulse measurement, per diode			0.4	V
Reverse current	I_R	$V_R=30V$, Pulse measurement, per diode			1.3	mA
Total capacitance	C_t	$f=1MHz$, $V_R=10V$, per diode		91		pF
Thermal resistance	$R_{th(j-c)}$	Junction to case, With heatsink *			4	$^{\circ}C/W$

* :See the original Specifications

CHARACTERISTIC DIAGRAMS



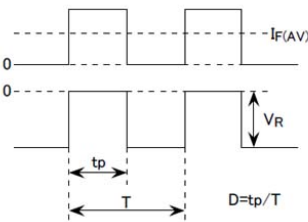
Derating curve



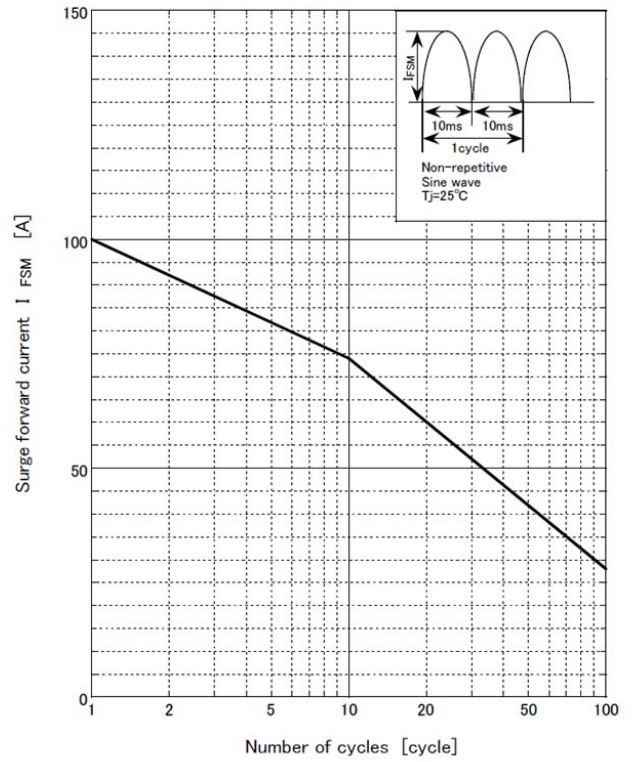
● $V_R = 15V$
R-load
With heatsink

● Substrate detail

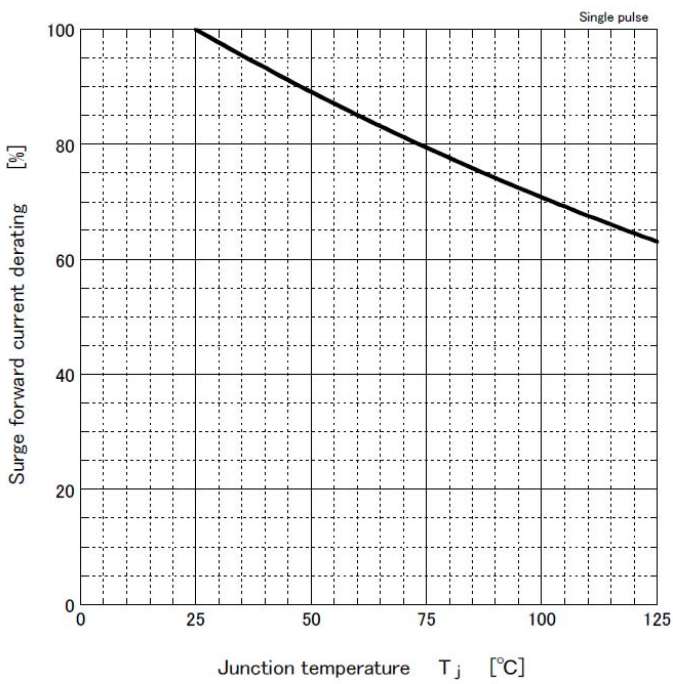
Type	Alumina
Size	1inch ²
Thickness	0.64mm
Conductor thickness	20 μ m
Pattern area	65mm ²



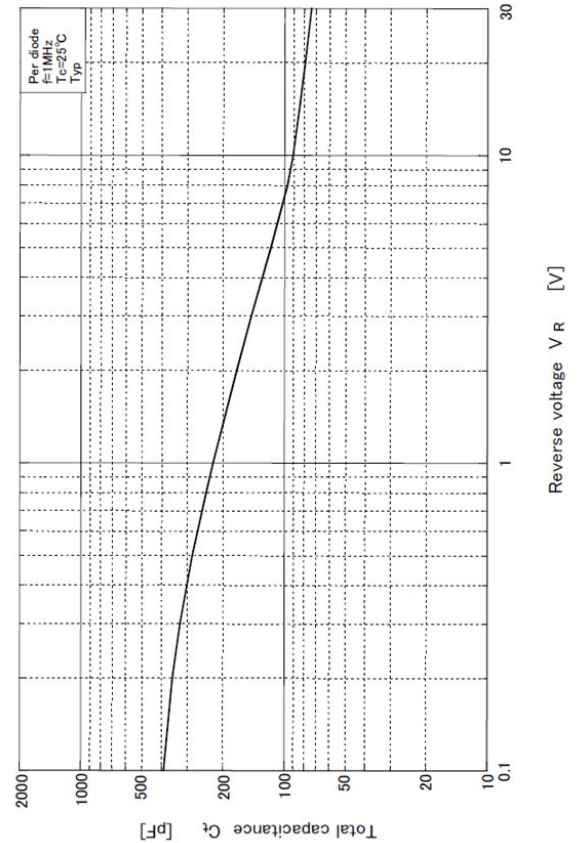
Surge forward current capability



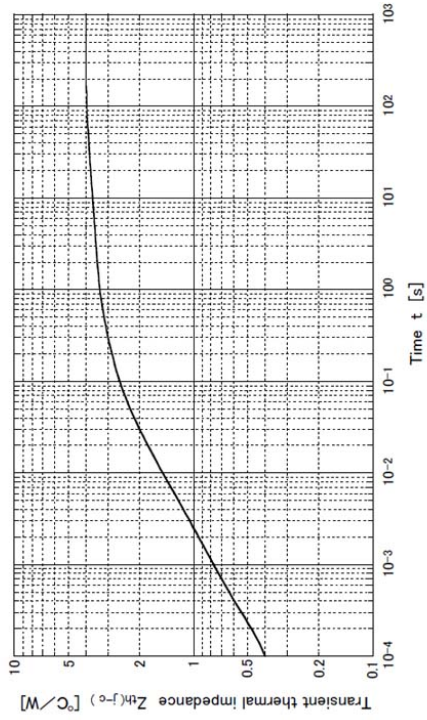
Surge forward current derating vs Junction temperature



Total capacitance



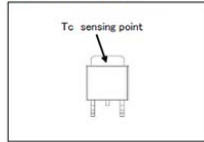
Transient thermal impedance



● Substrate detail

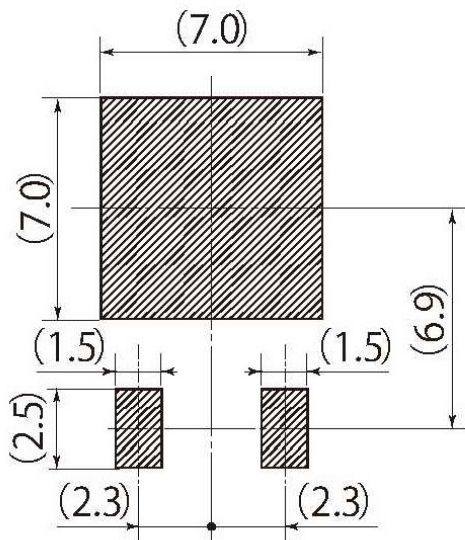
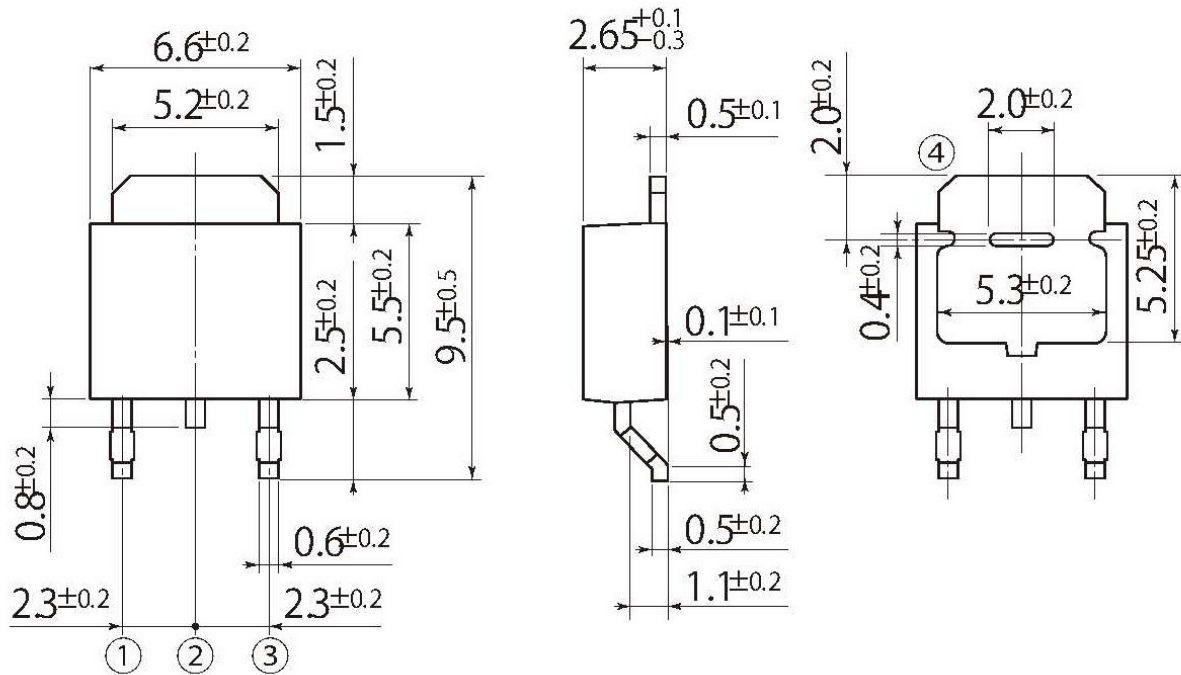
Type	Alumina
Size	1inch ²
Thickness	0.64mm
Conductor thickness	20 μ m
Pattern area	65mm ²

● Tc sensing point



G3

JEDEC Code	TO-252AB similar
JEITA Code	SC-63
House Name	FE



Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

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