



HIGH SPEED NPN POWER DARLINGTON TRANSISTORS

D67FP5,6,7

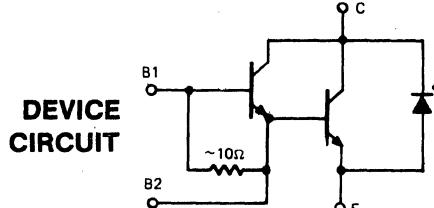
500-700 VOLTS
100 AMP, 312.5 WATTS

The D67FP is a high voltage NPN high current power darlington especially designed for use in PWM applications where fast and efficient switching is required. This device utilizes GE's latest advances in bipolar technology and features the D67 Package offering: collector isolation from heat sink, heat screw terminals for the emitter and collector and quick-connect terminals for Base 1 and Base 2.

The D67FP also features a discrete fast recovery antiparallel high power diode which eliminates the need for an external flyback diode in motor control and other inverter applications such as power supplies and UPS systems.

Features:

- Fast switching — $t_f(TYP)$ 0.6 μs
- High blocking voltage — V_{CEV} 500 to 700 Volts
- High current — $I_C(Peak)$ 150 Amps
- High gain — $hFE(MIN)$ 50 @ 100 Amps
- Discrete high power fast recovery diode
- Both Base 1 and Base 2 connections are available
- UL recognized industrial package



*NOTE: The collector-emitter diode is a discrete fast-recovery high power diode.

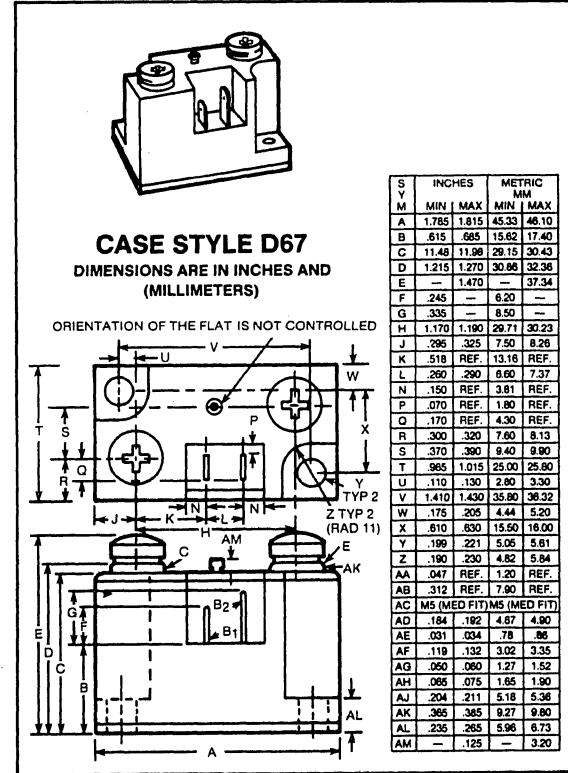
maximum ratings ($T_C = 25^\circ C$) (unless otherwise noted)

RATING	SYMBOL	D67FP5	D67FP6	D67FP7	UNITS
Collector-Emitter Voltage	V_{CEV}	500	600	700	Volts
Collector-Emitter Voltage	V_{CER}	400	450	500	Volts
Emitter Base Voltage	V_{EBO}	7	7	7	Volts
Collector Current — Continuous Peak (Repetitive) Peak (Non-Repetitive)	I_C I_{CM} I_{CSM}	100 150 250	100 150 250	100 150 250	A
Base Current — Continuous Peak (Non-Repetitive)	I_B I_{BM}	10 20	10 20	10 20	A
Total Power Dissipation @ $T_C = 25^\circ C$ Derate above $25^\circ C$	P_D	312.5 2.5	312.5 2.5	312.5 2.5	Watts $W/^\circ C$
Operating and Storage Junction Temperature Range	T_J, T_{STG}	-40 to +150	-40 to +150	-40 to +150	$^\circ C$
Isolation Voltage	V_{ISOL}	2500	2500	2500	$V_{(rms)}$

thermal characteristics

Thermal Resistance, (transistor) (diode)	$R_{\theta JC}$.40 1.5	.40 1.5	.40 1.5	$^\circ C/W$
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See page 845 for mounting and handling considerations.



electrical characteristics ($T_C = 25^\circ C$) (unless otherwise specified)

CHARACTERISTIC	SYMBOL	MIN	TYP	MAX	UNIT
off characteristics					
Collector-Emitter Sustaining Voltage ($I_C = 1.0A$, $R_{BE} = 10\Omega$)	D67FP5 D67FP6 D67FP7	$V_{CEO(sus)}$	400 450 500	— — —	Volts
Collector Cutoff Current ($V_{CE} = \text{Rated } V_{CEV}$, $V_{BE(\text{off})} = 1.5V$)	$T_J = 25^\circ C$ $T_J = 150^\circ C$	I_{CEV}	— —	1.0 2.5	mA
Emitter Cutoff Current ($V_{EB} = 5V$, $I_C = 0$)		I_{EBO}	—	10	mA

second breakdown

Second Breakdown with Base Forward Biased	FBSOA	SEE FIGURE 5
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on characteristics

DC Current Gain ($I_C = 150A$, $V_{CE} = 5V$) ($I_C = 100A$, $V_{CE} = 5V$) ($I_C = 40A$, $V_{CE} = 5V$)	h_{FE}	25 50 100	150 300 350	— — —	— — —
Collector-Emitter Saturation Voltage ($I_C = 150A$, $I_B = 10A$) ($I_C = 100A$, $I_B = 8A$) ($I_C = 40A$, $I_B = 4A$)	$V_{CE(\text{sat})}$	— — —	1.9 1.3 0.8	3.0 2.0 1.5	V
Base-Emitter Saturation Voltage ($I_C = 150A$, $I_B = 10A$) ($I_C = 100A$, $I_B = 8A$)	$V_{BE(\text{sat})}$	— —	2.75 2.3	3.5 3.0	V

switching characteristics

Resistive Load					
Delay Time	$V_{CE} = 250V$ $I_C = 100A$ $I_{B1} = 5A$, $-I_{B2} = 10A$ $t_p = 50 \mu\text{sec}$	t_d	—	0.1	0.5
Rise Time		t_r	—	0.45	1.0
Storage Time		t_s	—	3.2	5.0
Fall Time		t_f	—	1.0	3.0

e-c diode characteristics

Forward Voltage @ $T_J = 25^\circ C$ @ $T_J = 150^\circ C$	$(I_F = 100A)$	V_P	—	1.3	2.0	Volts
Reverse Recovery Time ($I_F = 100A$, $di/dt = 100A/\mu\text{sec}$, $V_{BE(\text{off})} = 1.5V$)		T_{rr}	—	0.5	1.0	μsec

TYPICAL CHARACTERISTICS

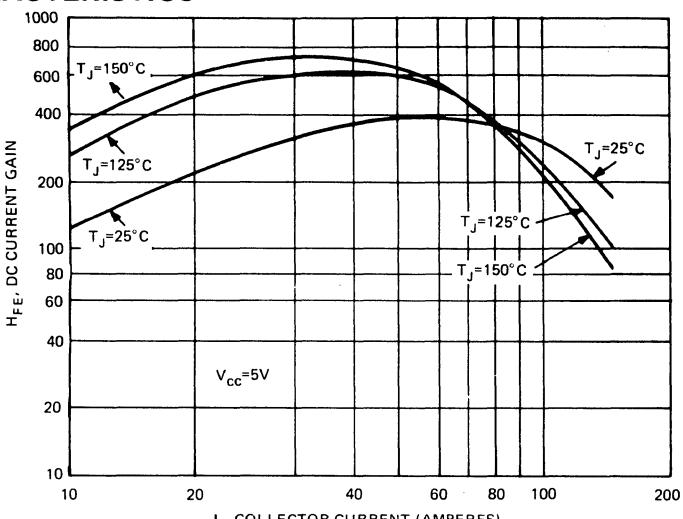
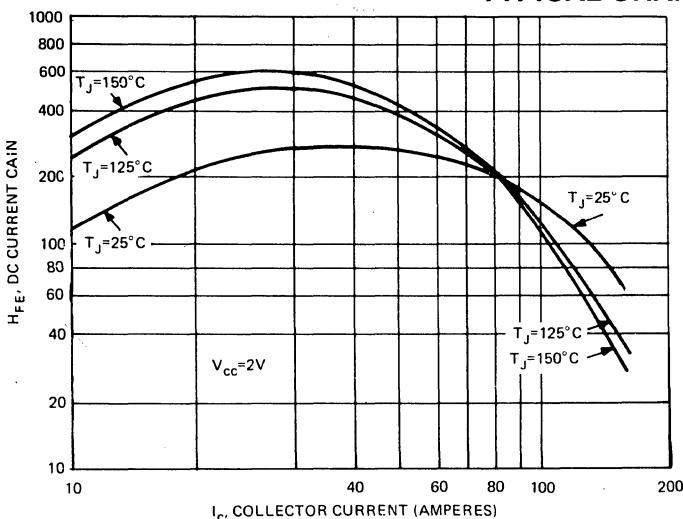


FIGURE 1. DC CURRENT GAIN ($V_{CE} = 2V$), TYPICAL

FIGURE 2. DC CURRENT GAIN ($V_{CE} = 5V$), TYPICAL

TYPICAL CHARACTERISTICS

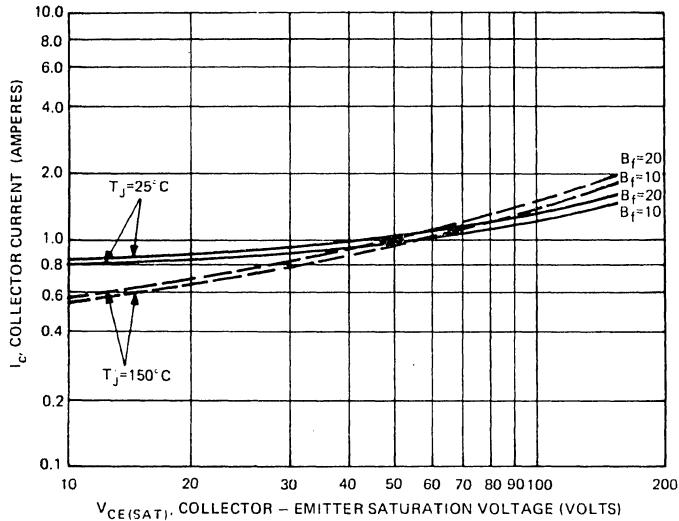


FIGURE 3. $V_{CE(SAT)}$ vs. I_C , TYPICAL

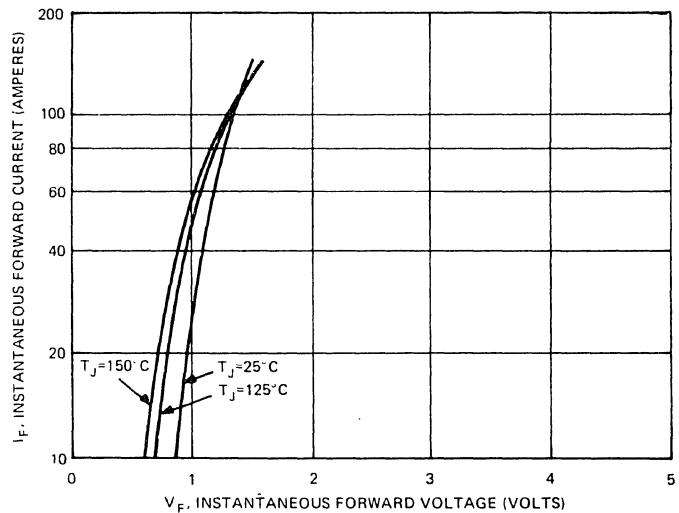


FIGURE 4. DIODE FORWARD CHARACTERISTICS

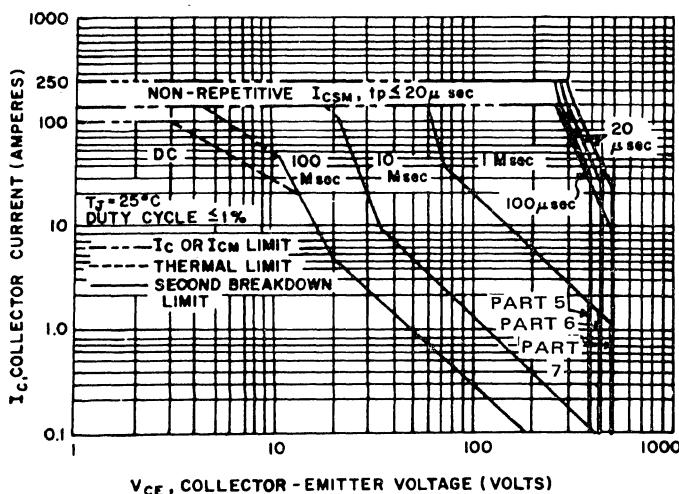


FIGURE 5. FORWARD BIAS SAFE OPERATING AREA

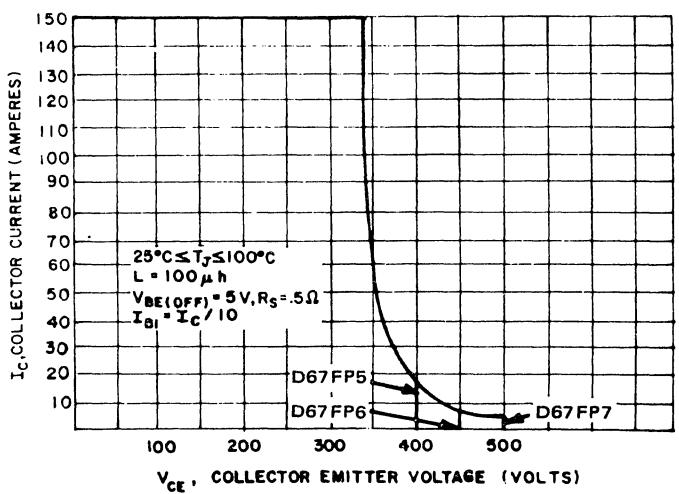
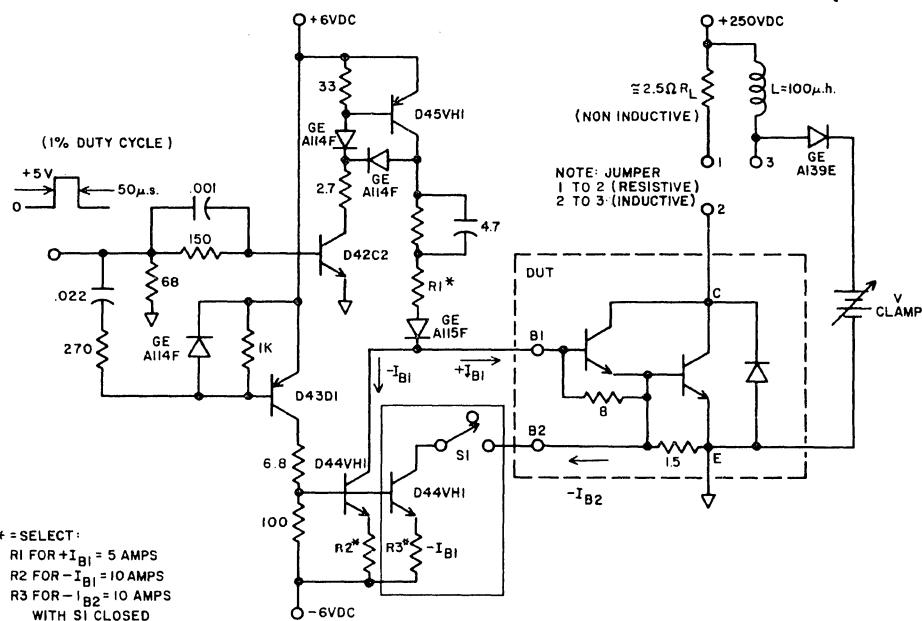


FIGURE 6. REVERSE BIAS SAFE OPERATING AREA (CLAMPED)



**NOTE: UTILIZING SECOND BASE CONNECTION DURING TURN-OFF (SI CLOSED), TYPICAL REDUCTIONS IN TURN-OFF TIMES (t_{sf}, t_c) RANGE FROM 2:1 TO 10:1.
REDUCTION IS PROPORTIONAL TO I_B^2 .**

FIGURE 7. SWITCHING TIME TEST CIRCUIT