

D6FEC12ST

Schottky Barrier Diodes
120V, 6A

Feature

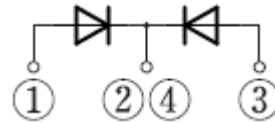
- SMD
- Tj=175°C
- Ultra low I_R
- Based on AEC-Q101
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FE
Package (JEDEC Code): TO-252AB similar
Package (JEITA Code): SC-63



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : T_c=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T _{stg}		-55 to 175	°C
Junction temperature	T _j		175	°C
Repetitive peak reverse voltage	V _{RRM}		120	V
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, Rating for each diode I _{F(AV)} /2, T _c =154°C	6	A
Surge forward current	I _{FSM}	50Hz sine wave, Non-repetitive, 1 cycle, Peak value, T _j =25°C	100	A
Surge forward current	I _{FSM1}	t _p =1ms, T _j =25°C, Non-repetitive	180	A

※ :See the original Specifications

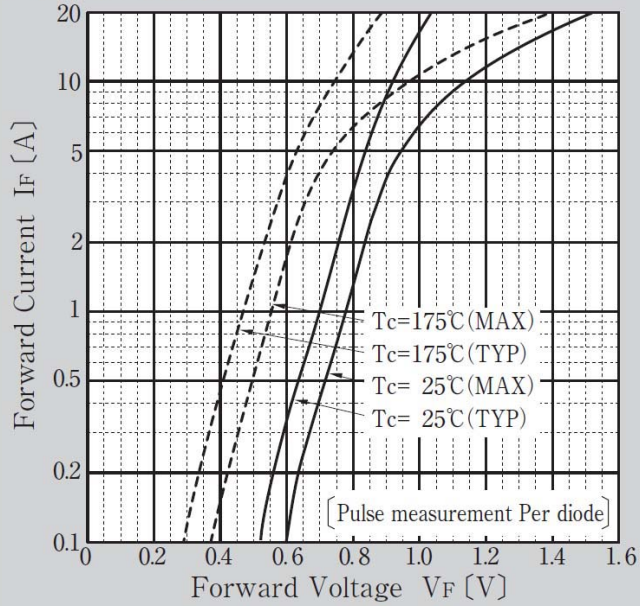
Electrical Characteristics (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Forward voltage	V _F	I _F =3A, Pulse measurement, per diode			0.87	V
Reverse current	I _R	V _R =120V, Pulse measurement, per diode			0.008	mA
Total capacitance	C _t	f=1MHz, V _R =10V, per diode		60		pF
Thermal resistance	R _{th(j-c)}	Junction to case			4	°C/W

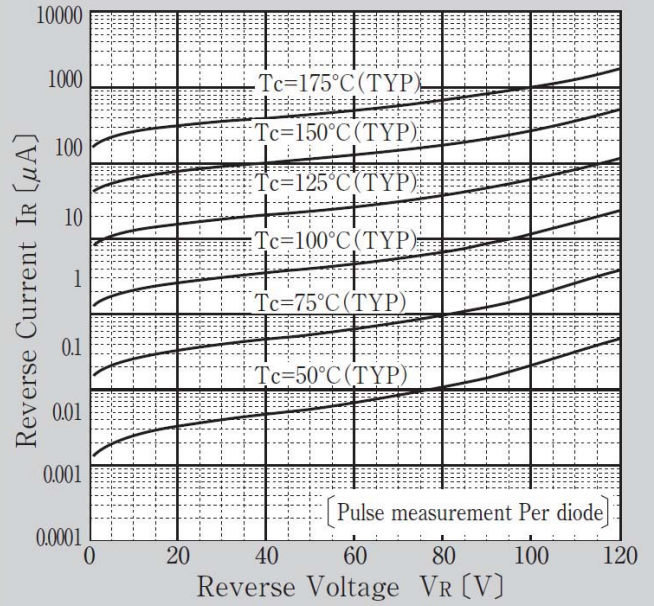
* : See the original Specifications

CHARACTERISTIC DIAGRAMS

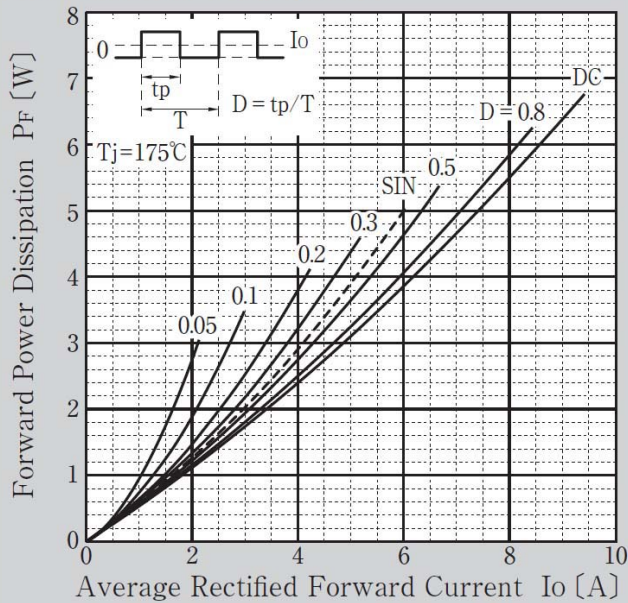
Forward Voltage



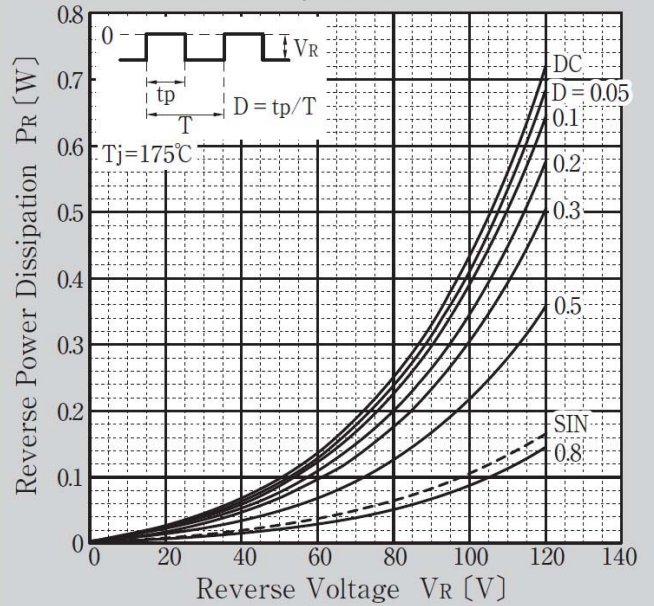
Reverse Current

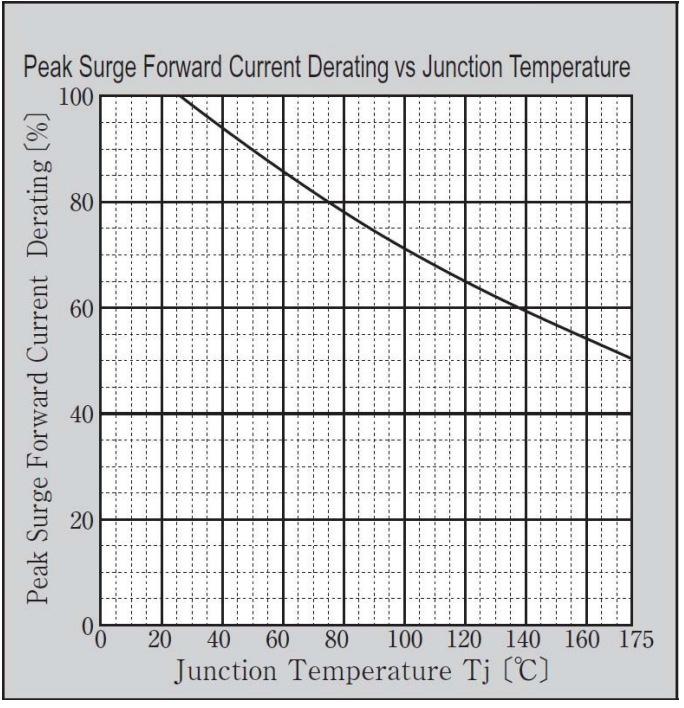
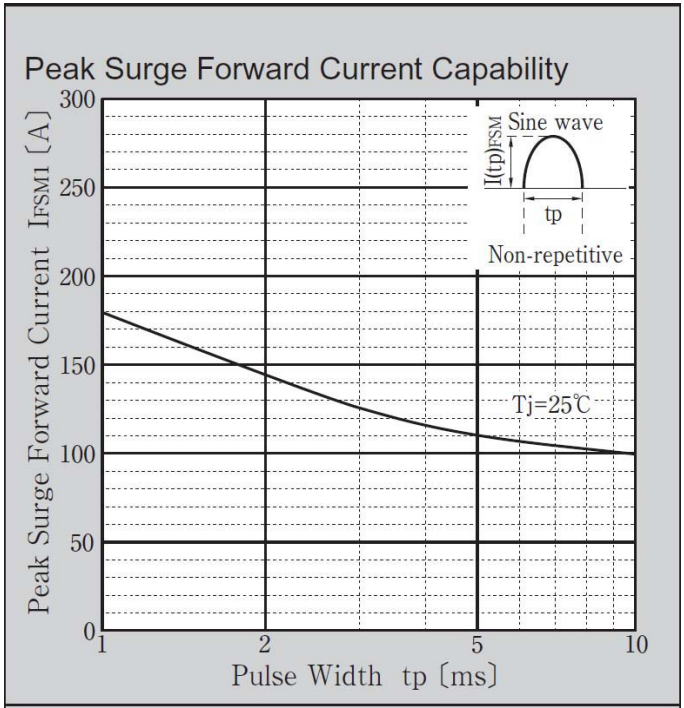
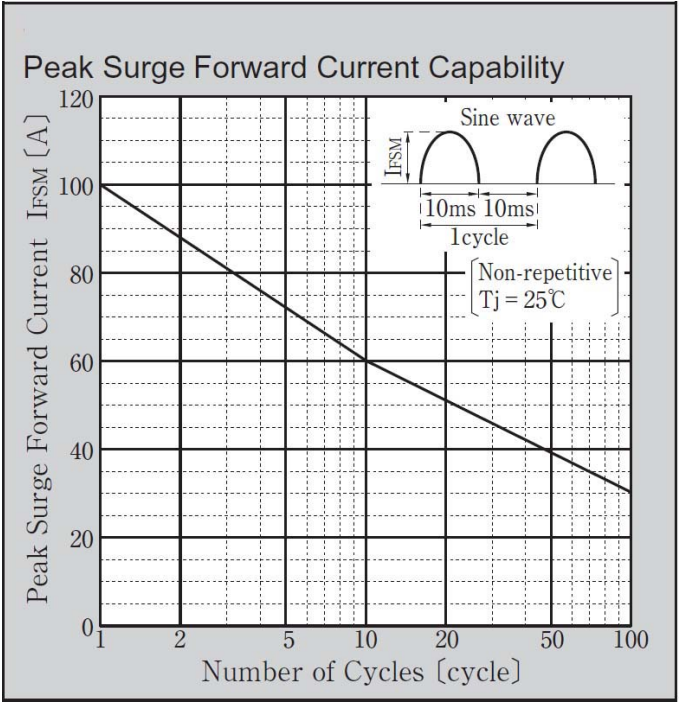
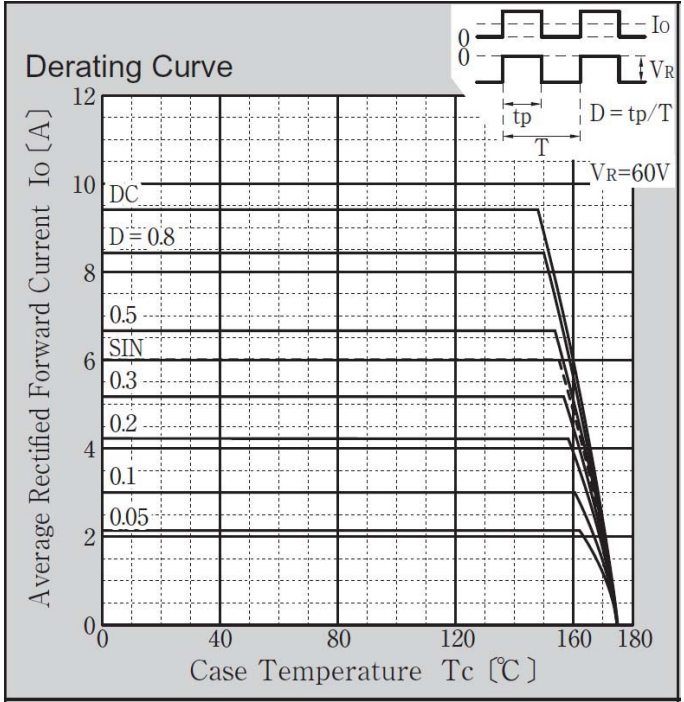


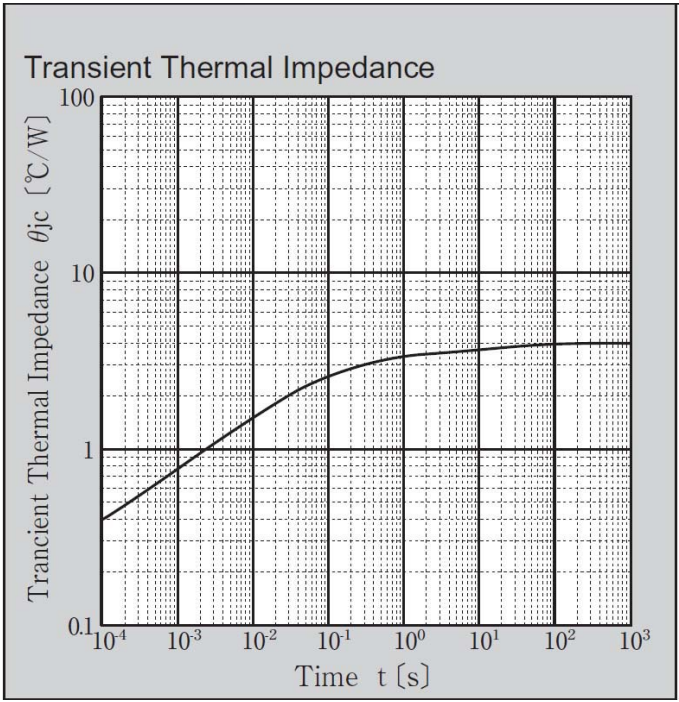
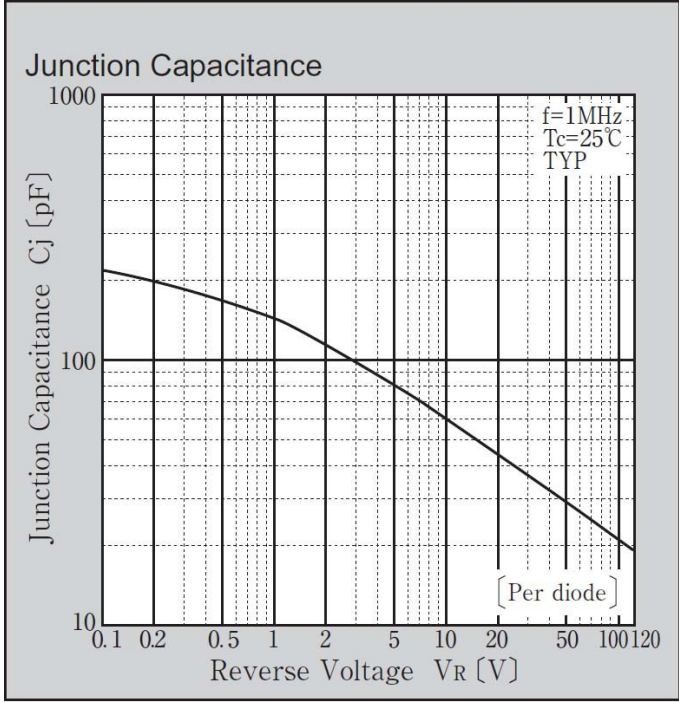
Forward Power Dissipation



Reverse Power Dissipation

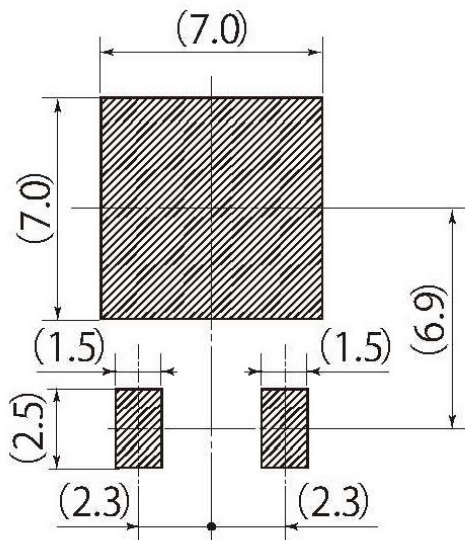
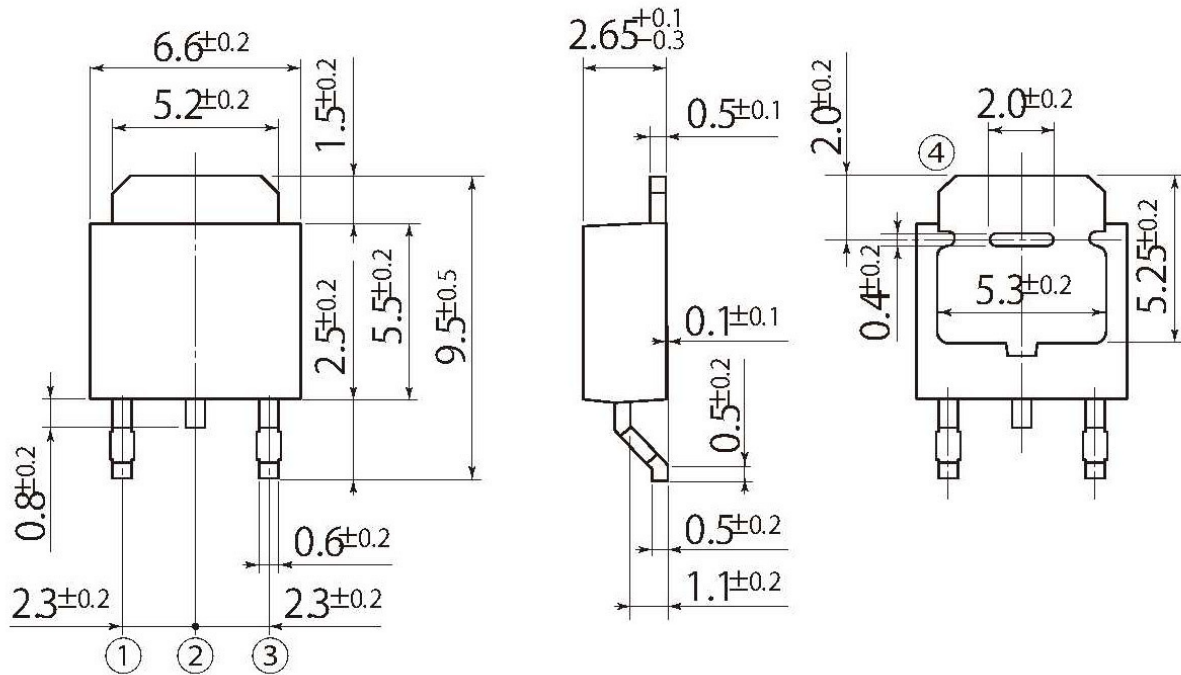






G3

JEDEC Code	TO-252AB similar
JEITA Code	SC-63
House Name	FE



Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

Notes

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