



SURFACE-MOUNT PNP POWER TRANSISTORS

D71Y1.5T1

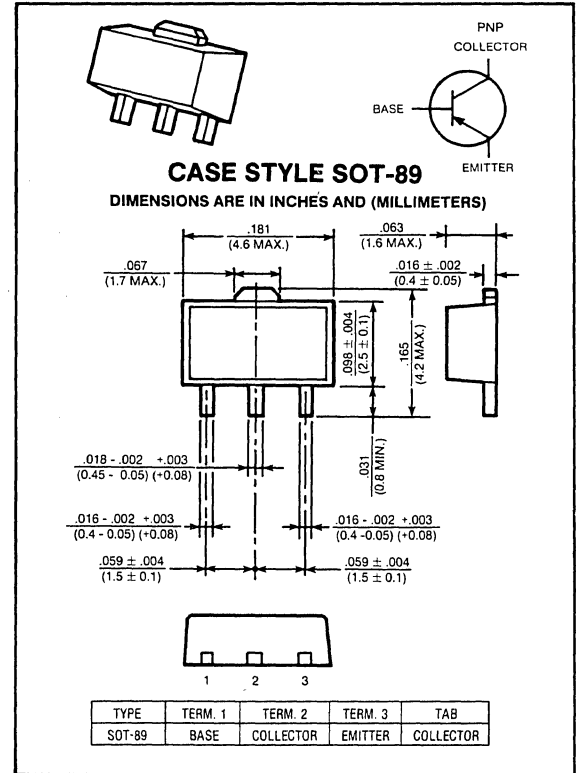
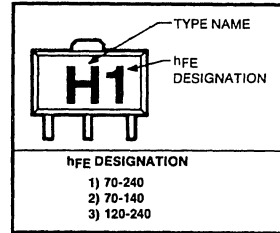
**-30 VOLTS
-1.5mAMP, 500mWATTS**

Designed for audio frequency amplifier applications.

Features:

- Suitable for output stage of 3 watt amplifier
- $P_D=1 \sim 2W$ (Mounted on ceramic substrate)
- Small flat package
- Complementary to D70Y1.5T1
- See page 840 for mounting and handling considerations.

MARKING SYSTEM



maximum ratings ($T_A = 25^\circ C$) (unless otherwise specified)

RATING	SYMBOL	D71Y1.5T1	UNITS
Collector-Emitter Voltage	V_{CEO}	-30	Volts
Collector-Base Voltage	V_{CBO}	-30	Volts
Emitter Base Voltage	V_{EBO}	-5	Volts
Collector Current — Continuous	I_C	-1.5	A
Base Current — Continuous	I_B	-0.3	A
Total Power Dissipation @ $T_C = 25^\circ C$ @ $T_C = 25^\circ C^{(1)}$	P_D	500 1000	mWatts
Operating and Storage Junction Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ C$

thermal characteristics⁽²⁾

(1) Mounted on ceramic substrate (250mm² x 0.8t).

(2) See page 841 for mounting and thermal considerations.

electrical characteristics ($T_A = 25^\circ\text{C}$) (unless otherwise specified)

CHARACTERISTIC	SYMBOL	MIN	TYP	MAX	UNIT
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off characteristics

Collector-Emitter Breakdown Voltage ($I_C = -10\text{mA}$, $I_B = 0$)	$V_{(BR)CEO}$	-30	—	—	Volts
Emitter Base Breakdown Voltage ($I_E = -1\text{mA}$, $I_C = 0$)	$V_{(BR)EBO}$	-5	—	—	V
Collector Cutoff Current ($V_{CB} = -30\text{V}$, $I_E = 0$)	I_{CBO}	—	—	-100	nA
Emitter Cutoff Current ($V_{EB} = -5\text{V}$, $I_C = 0$)	I_{EBO}	—	—	-100	nA

on characteristics

DC Current Gain ⁽³⁾ ($I_C = -500\text{mA}$, $V_{CE} = -2\text{V}$)	h_{FE}	100	—	320	—
Collector-Emitter Saturation Voltage ($I_C = -1.5\text{mA}$, $I_B = -0.03\text{A}$)	$V_{CE(sat)}$	—	—	-2.0	V
Base-Emitter Voltage ($V_{CE} = -2\text{V}$, $I_C = -500\text{mA}$)	$V_{BE(on)}$	—	—	-1.0	V

(3) See page 44 for h_{FE} range.

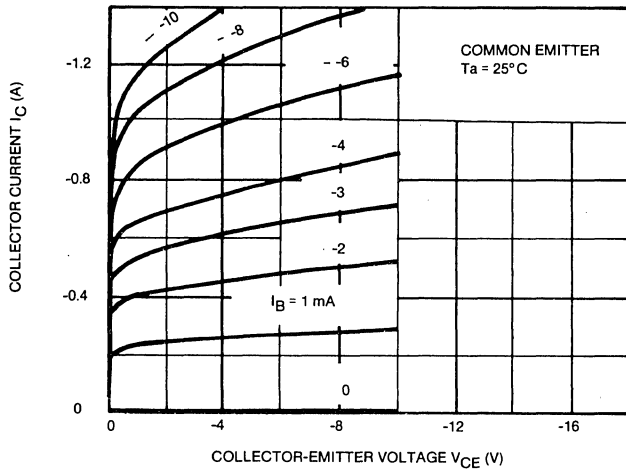


FIG. 1 $I_C - V_{CE}$

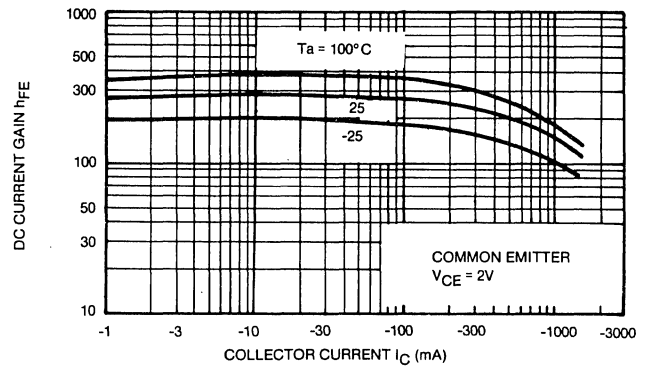


FIG. 2 $h_{FE} - I_C$

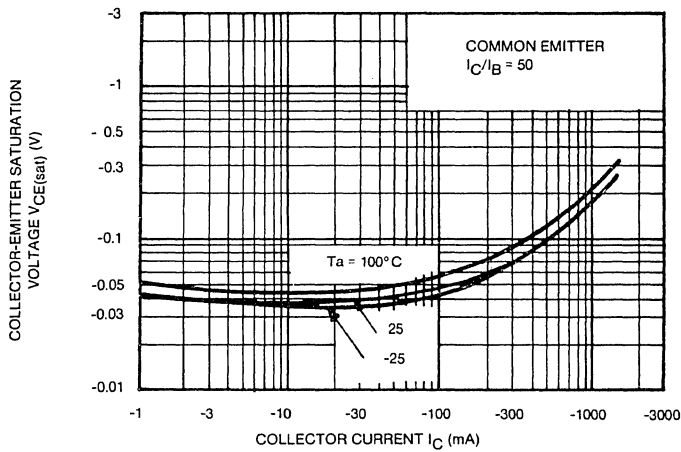


FIG. 3 $V_{CE(sat)} - I_C$

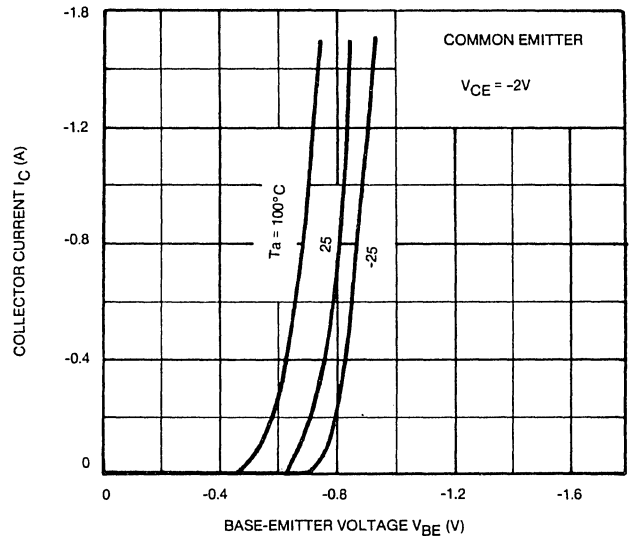


FIG. 4 $I_C - V_{BE}$

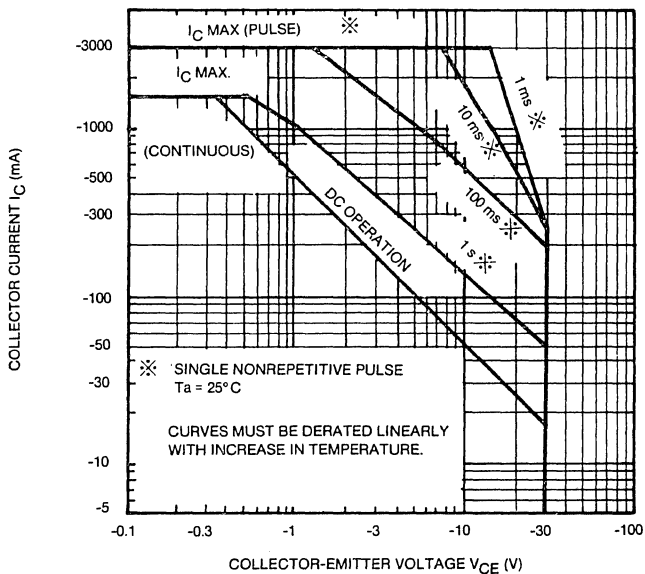


FIG. 5 SAFE OPERATING AREA

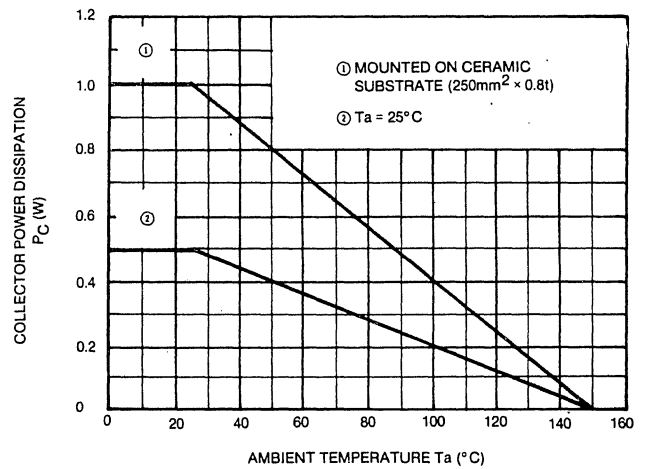


FIG. 6 $P_C - T_a$