

PNP POWER DARLINGTON TRANSISTOR ARRAY



Designed for high power switching applications, hammer drive, pulse motor drive and inductive load drive applications.

Features:

- High reliability small-sized available (3 in 1)
- Epoxy single-inline package (8 pin)
- High collector power dissipation: PD = 3W @ TA = 25°C (Three device action)
- High collector current: IC = -2A (Max.)
- High DC current gain: hFE = 2000 (Min.) @ VCE = -2V, IC = -1A



ARRAY CONFIGURATION



maximum ratings ($T_A = 25^{\circ}C$) (unless otherwise specified)

RATING	SYMBOL	D75FY2D	UNITS
Collector-Emitter Voltage	V _{CEO}	-80	Volts
Collector-Base Voltage	V _{CBO}	-80	Volts
Emitter Base Voltage	V _{EBO}	-8	Volts
Collector Current — Continuous Peak	I _C I _{CM}	-2 -3	A
Base Current — Continuous	۱ _B	-0.5	A
Collector Power Dissipation (One Device Action, $T_A = 25^{\circ}$ C)	PD	1.8	Watts
Collector Power Dissipation (Three Device Action, T _A = 25°C)	PD	3.0	Watts
Operating and Storage Junction Temperature Range	T _J , T _{STG}	-55 to +150	°C

thermal characteristics

Thermal Resistance, Junction to Ambient	Σ R _{θJA}	41.7	°C/W
Maximum Lead Temperature for Soldering Purpose: 1/6" from Case for 5 Seconds	ΤL	260	°C

CHARACTERISTIC	SYMBOL	MIN	ТҮР	MAX	UNIT
off characteristics					
Collector-Emitter Breakdown Voltage (I _C = 1mA, I _B = 0)	V _{BR(CEO)}	-80	_		Volts
Collector-Base Breakdown Voltage (I _C = -10mA, I _E = 0)	V _{BR(CBO)}	-80		—	Volts
Collector Cutoff Current (V _{CB} = -80V, I _E = 0)	Ісво	_		-10	μΑ
Collector Cutoff Current (V _{CE} = -80V, I _B = 0)	ICEO	—	_	-10	μA
Emitter Cutoff Current (V _{EB} = -8V, I _C = 0)	I _{EBO}	_		-4	mA

on characteristics

DC Current Gain (I _C = -1A, V _{CE} = -2V)	h _{FE}	2000			
Collector-Emitter Saturation Voltage (I _C = -1A, I _B = -1mA)	V _{CE(sat)}	_	_	-1.5	Volts
Base-Emitter Saturation Voltage (I _C = -1A, I _B = -1mA)	V _{BE(sat)}	—		-2.0	Volts

switching characteristics

Turn-on Time	$V_{CC} = 30V$	t _{on}		0.4	-	μs
Storage Time	$I_{B1} = -I_{B2} = 1mA$	t _{stg}	—	2.0		
Fall Time	Duty Cycle = 1%	t _f	—	0.4	_	



FIG. 1 SWITCHING TIME TEST CIRCUIT