POWER-MOS FET
FIELD EFFECT POWER TRANSISTOR

IRFD1Z0,1Z1
D82AL2,K2

0.5 AMPERES
100, 60 VOLTS
$R_{DS(ON)}$ = 2.4 Ω

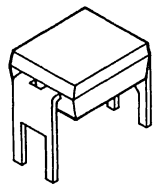
This series of N-Channel Enhancement-mode Power MOSFETs utilizes GE's advanced Power DMOS technology to achieve low on-resistance with excellent device ruggedness and reliability.

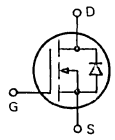
This design has been optimized to give superior performance in most switching applications including: switching power supplies, inverters, converters and solenoid/relay drivers. Also, the extended safe operating area with good linear transfer characteristics makes it well suited for many linear applications such as audio amplifiers and servo motors.

Features

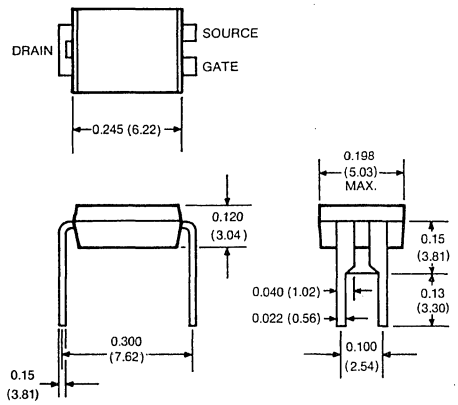
- Polysilicon gate — Improved stability and reliability
- No secondary breakdown — Excellent ruggedness
- Ultra-fast switching — Independent of temperature
- Voltage controlled — High transconductance
- Low input capacitance — Reduced drive requirement
- Excellent thermal stability — Ease of paralleling

N-CHANNEL





CASE STYLE 4-PIN DIP
DIMENSIONS ARE IN INCHES AND (MILLIMETERS)



maximum ratings (T_A = 25°C) (unless otherwise specified)

RATING	SYMBOL	IRFD1Z0/D82AL2	IRFD1Z1/D82AK2	UNITS
Drain-Source Voltage	V_{DSS}	100	60	Volts
Drain-Gate Voltage, R_{GS} = 1MΩ	V_{DGR}	100	60	Volts
Continuous Drain Current @ T_A = 25°C(1) @ T_A = 100°C	I_D	0.50 0.31	0.50 0.31	A A
Pulsed Drain Current(2)	I_{DM}	4.0	4.0	A
Gate-Source Voltage	V_{GS}	±20	±20	Volts
Total Power Dissipation @ T_A = 25°C Derate Above 25°C	P_D	1.2 9.6	1.2 9.6	Watts W/°C
Operating and Storage Junction Temperature Range	T_J, T_{STG}	-55 to 150	-55 to 150	°C

thermal characteristics

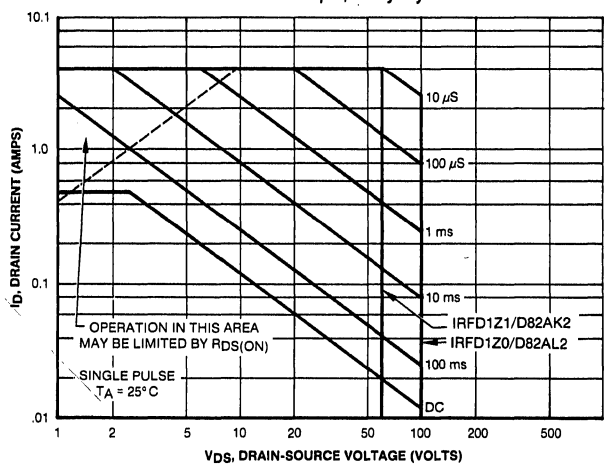
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	105	105	°C/W
Maximum Lead Temperature for Soldering Purposes: ⅛" from Case for 5 Seconds	T_L	300	300	°C

(1) Device mounted to vertical pc board in free air with drain lead soldered to 0.5 in. minimum copper run area.
(2) Repetitive Rating: Pulse width limited by max. junction temperature.

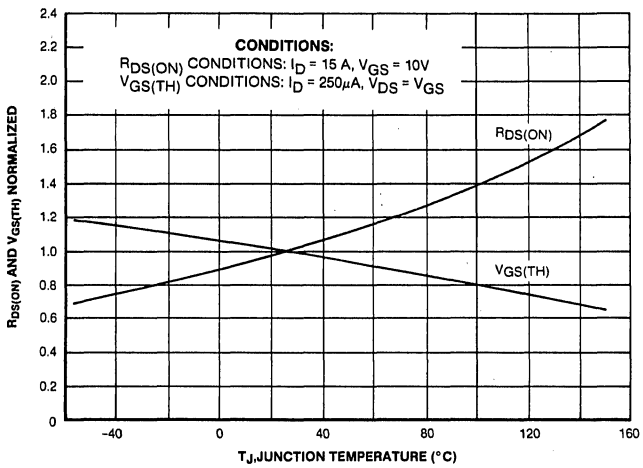
electrical characteristics (T_C = 25°C) (unless otherwise specified)

CHARACTERISTIC		SYMBOL	MIN	TYP	MAX	UNIT
off characteristics						
Drain-Source Breakdown Voltage (V_{GS} = 0V, I_D = 250 μA)	IRFD1Z0/D82AL2 IRFD1Z1/D82AK2	BV_{DSS}	100 60	— —	— —	Volts
Zero Gate Voltage Drain Current (V_{DS} = Max Rating, V_{GS} = 0V, T_A = 25°C) (V_{DS} = Max Rating, × 0.8, V_{GS} = 0V, T_A = 125°C)		I_{DSS}	— —	— —	250 1000	μA
Gate-Source Leakage Current (V_{GS} = ±20V)		I_{GSS}	—	—	±500	nA
on characteristics*						
Gate Threshold Voltage (V_{DS} = V_{GS}, I_D = 250μA)	T_A = 25°C	$V_{GS(TH)}$	2.0	—	4.0	Volts
Drain Source On-State Voltage (V_{GS} = 10V)	I_D = 0.25A I_D = 0.50A I_D = 0.25A, T_A = 125°C	$V_{DS(ON)}$	— — —	0.55 1.10 0.90	0.6 — —	Volts
Static Drain-Source On-State Resistance (V_{GS} = 10V, I_D = 0.25A)		$R_{DS(ON)}$	—	2.2	2.4	Ohms
Forward Transconductance (V_{DS} = 10V, I_D = 0.25A)		g_{fs}	—	0.2	—	mhos
dynamic characteristics						
Input Capacitance	V_{GS} = 0V	C_{iss}	—	36	70	pF
Output Capacitance	V_{DS} = 25V	C_{oss}	—	20	30	pF
Reverse Transfer Capacitance	f = 1 MHz	C_{rss}	—	7	10	pF
switching characteristics*						
Turn-on Delay Time	V_{DS} = 30V	$t_{d(on)}$	—	6	—	ns
Rise Time	I_D = 0.25A, V_{GS} = 15V	t_r	—	6	—	ns
Turn-off Delay Time	R_{GEN} = 50Ω, R_{GS} = 12.5Ω	$t_{d(off)}$	—	12	—	ns
Fall Time	($R_{GS\ (EQUIV.)}$ = 10Ω)	t_f	—	7	—	ns
source-drain diode ratings and characteristics*						
Continuous Source Current		I_S	—	—	0.5	A
Pulsed Source Current		I_{SM}	—	—	4.0	A
Diode Forward Voltage (T_A = 25°C, V_{GS} = 0V, I_S = 0.5A)		V_{SD}	—	0.9	1.5	Volts
Reverse Recovery Time (I_S = 0.5A, dI_S/dt = 100A/μs, V_{DS} = 40V Max., T_A = 125°C)		t_{rr}	—	65	—	ns

*Pulse Test: Pulse width ≤ 300 μs, duty cycle ≤ 2%



MAXIMUM SAFE OPERATING AREA



TYPICAL NORMALIZED $R_{DS(ON)}$ AND $V_{GS(TH)}$ VS. TEMP.