



# POWER-MOS FET

## FIELD EFFECT POWER TRANSISTOR

**IRF620,621  
D84CN2,M2**

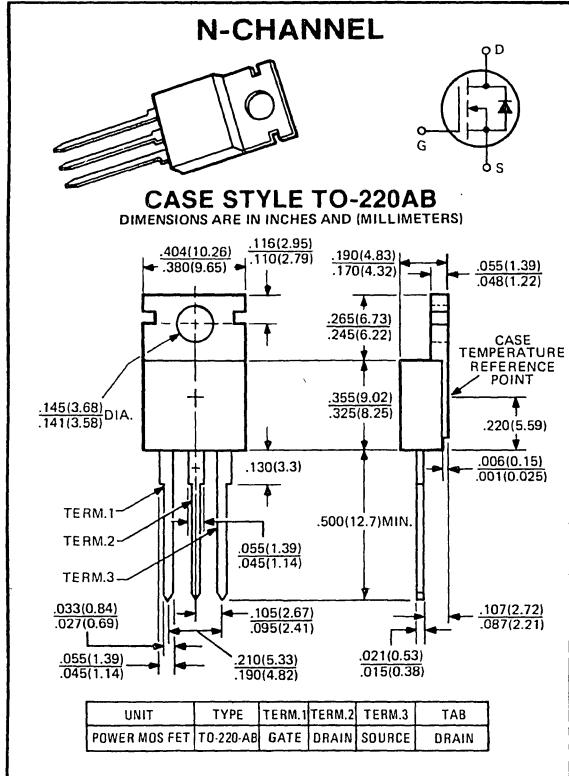
5 AMPERES  
200, 150 VOLTS  
 $R_{DS(ON)} = 0.8 \Omega$

This series of N-Channel Enhancement-mode Power MOSFETs utilizes GE's advanced Power DMOS technology to achieve low on-resistance with excellent device ruggedness and reliability.

This design has been optimized to give superior performance in most switching applications including: switching power supplies, inverters, converters and solenoid/relay drivers. Also, the extended safe operating area with good linear transfer characteristics makes it well suited for many linear applications such as audio amplifiers and servo motors.

### Features

- Polysilicon gate — Improved stability and reliability
- No secondary breakdown — Excellent ruggedness
- Ultra-fast switching — Independent of temperature
- Voltage controlled — High transconductance
- Low input capacitance — Reduced drive requirement
- Excellent thermal stability — Ease of paralleling



maximum ratings ( $T_C = 25^\circ C$ ) (unless otherwise specified)

RATING	SYMBOL	IRF620/D86CN2	IRF621/D84CM2	UNITS
Drain-Source Voltage	$V_{DSS}$	200	150	Volts
Drain-Gate Voltage, $R_{GS} = 1M\Omega$	$V_{DGR}$	200	150	Volts
Continuous Drain Current @ $T_C = 25^\circ C$ @ $T_C = 100^\circ C$	$I_D$	5 3	5 3	A A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	20	20	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	$\pm 20$	Volts
Total Power Dissipation @ $T_C = 25^\circ C$ Derate Above $25^\circ C$	$P_D$	40 3.2	40 3.2	Watts $W/^\circ C$
Operating and Storage Junction Temperature Range	$T_J, T_{STG}$	-55 to 150	-55 to 150	$^\circ C$

### thermal characteristics

Thermal Resistance, Junction to Case	$R_{\theta JC}$	3.12	3.12	$^\circ C/W$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	80	80	$^\circ C/W$
Maximum Lead Temperature for Soldering Purposes: $1/8''$ from Case for 5 Seconds	$T_L$	260	260	$^\circ C$

(1) Repetitive Rating: Pulse width limited by max. junction temperature.

# electrical characteristics ( $T_C = 25^\circ C$ ) (unless otherwise specified)

CHARACTERISTIC	SYMBOL	MIN	TYP	MAX	UNIT
<b>off characteristics</b>					
Drain-Source Breakdown Voltage ( $V_{GS} = 0V$ , $I_D = 250 \mu A$ )	IRF620/D84CN2 IRF621/D84DM2	BVDSS 150	200 150	—	Volts
Zero Gate Voltage Drain Current ( $V_{DS} = \text{Max Rating}$ , $V_{GS} = 0V$ , $T_C = 25^\circ C$ ) ( $V_{DS} = \text{Max Rating} \times 0.8$ , $V_{GS} = 0V$ , $T_C = 125^\circ C$ )	IDS	— —	— —	250 1000	μA
Gate-Source Leakage Current ( $V_{GS} = \pm 20V$ )	IGSS	—	—	±500	nA

## on characteristics\*

Gate Threshold Voltage ( $V_{DS} = V_{GS}$ , $I_D = 250 \mu A$ )	$T_C = 25^\circ C$	$V_{GS(TH)}$	2.0	—	4.0	Volts
On-State Drain Current ( $V_{GS} = 10V$ , $V_{DS} = 10V$ )		$I_{D(ON)}$	5.0	—	—	A
Static Drain-Source On-State Resistance ( $V_{GS} = 10V$ , $I_D = 2.5A$ )		$R_{DS(ON)}$	—	0.6	0.8	Ohms
Forward Transconductance ( $V_{DS} = 10V$ , $I_D = 2.5A$ )		$g_f$	1.2	1.8	—	mhos

## dynamic characteristics

Input Capacitance	$V_{GS} = 10V$	$C_{iss}$	—	385	600	pF
Output Capacitance	$V_{DS} = 25V$	$C_{oss}$	—	80	300	pF
Reverse Transfer Capacitance	$f = 1 MHz$	$C_{rss}$	—	15	80	pF

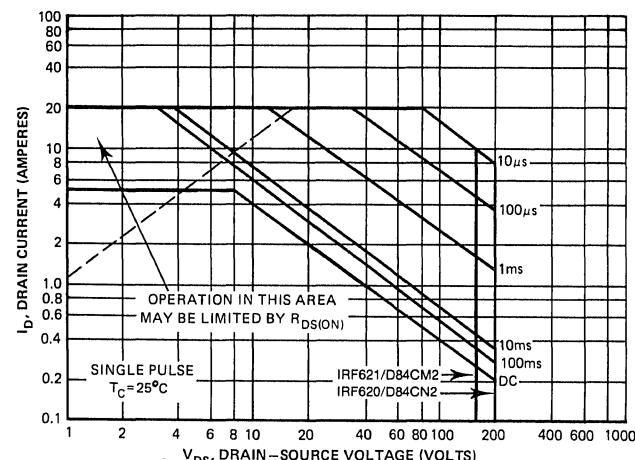
## switching characteristics\*

Turn-on Delay Time	$V_{DS} = 90V$	$t_{d(on)}$	—	15	—	ns
Rise Time	$I_D = 2.5A$ , $V_{GS} = 15V$	$t_r$	—	10	—	ns
Turn-off Delay Time	$R_{GEN} = 50\Omega$ , $R_{GS} = 12.5\Omega$	$t_{d(off)}$	—	30	—	ns
Fall Time	( $R_{GS}$ (EQUIV.) = $10\Omega$ )	$t_f$	—	10	—	ns

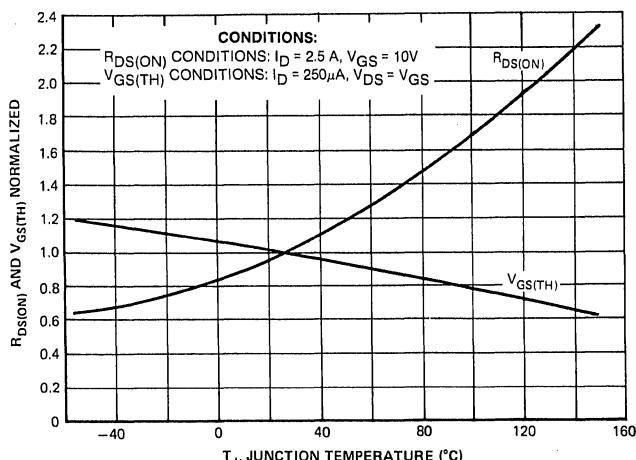
## source-drain diode ratings and characteristics\*

Continuous Source Current	$I_S$	—	—	5	A
Pulsed Source Current	$I_{SM}$	—	—	20	A
Diode Forward Voltage ( $T_C = 25^\circ C$ , $V_{GS} = 0V$ , $I_S = 5A$ )	$V_{SD}$	—	1.0	1.8	Volts
Reverse Recovery Time ( $I_S = 5A$ , $dI_S/dt = 100A/\mu sec$ , $T_C = 125^\circ C$ )	$t_{rr}$ $Q_{RR}$	— —	270 1.5	— —	ns $\mu C$

\*Pulse Test: Pulse width  $\leq 300 \mu s$ , duty cycle  $\leq 2\%$



**MAXIMUM SAFE OPERATING AREA**



**TYPICAL NORMALIZED  $R_{DS(ON)}$  AND  $V_{GS(TH)}$  VS. TEMP.**