

# DB201S THRU DB207S

## SINGLE-PHASE GLASS PASSIVATED SILICON SURFACE MOUNT BRIDGE RECTIFIER

Reverse Voltage - 50 to 1000 V

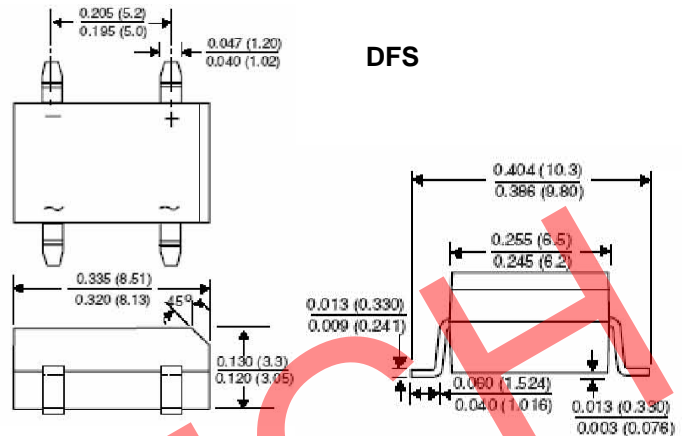
Forward Current - 2 A

### Features

- Ideal for automated placement
- Applicable for automotive insertion
- High surge current capability

### Mechanical Data

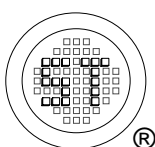
- Case: DFS
- Epoxy: UL 94V-0 rate flame retardant
- Terminals: Matte tin plated leads, solderable per J-STD-002B and JESD22-B102D
- Polarity: As marked on body



### Maximum Ratings and Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

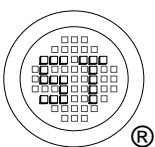
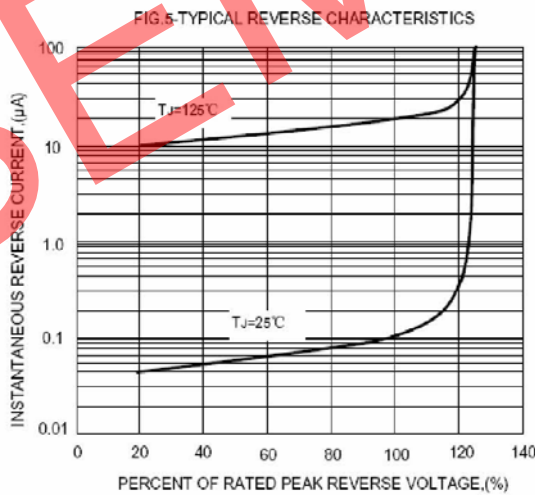
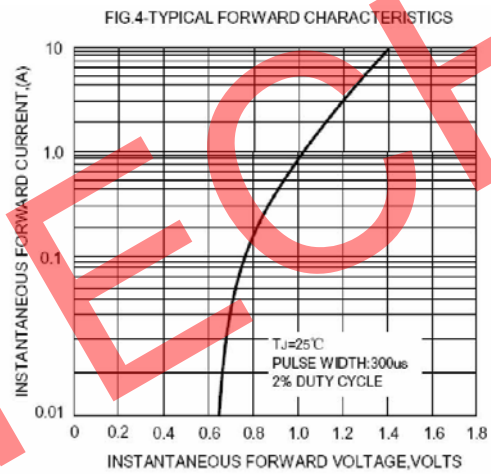
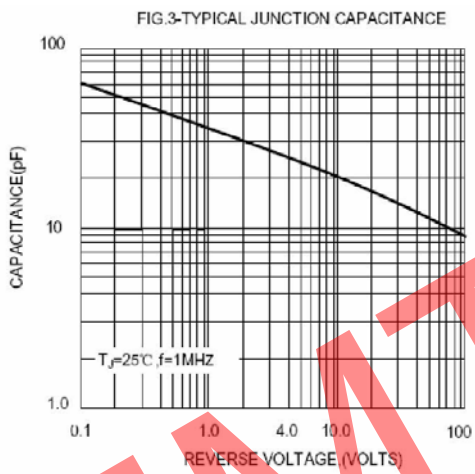
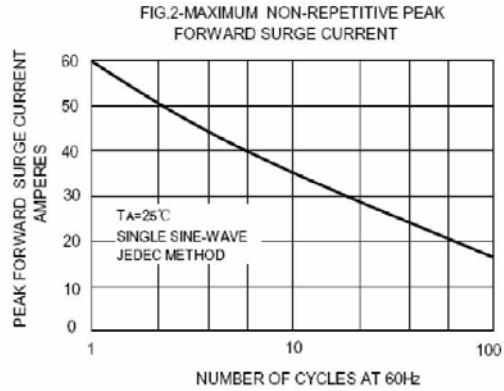
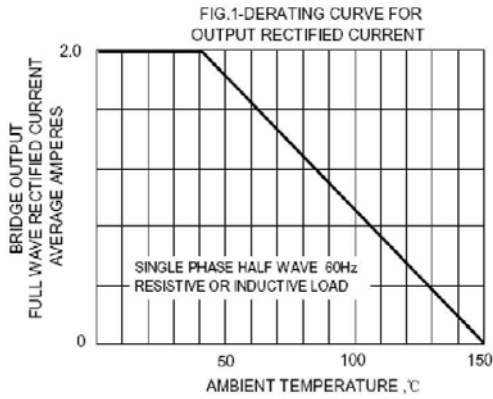
| Parameter   | Symbols        | DB201S        | DB202S | DB203S | DB204S | DB205S | DB206S | DB207S | Units            |
|---|----------------|---------------|--------|--------|--------|--------|--------|--------|------------------|
| Maximum repetitive Peak Reverse Voltage   | $V_{RRM}$      | 50            | 100    | 200    | 400    | 600    | 800    | 1000   | V                |
| Maximum RMS Voltage   | $V_{RMS}$      | 35            | 70     | 140    | 280    | 420    | 560    | 700    | V                |
| Maximum DC Blocking Voltage   | $V_{DC}$       | 50            | 100    | 200    | 400    | 600    | 800    | 1000   | V                |
| Maximum Average Forward output Rectified Current at $T_A = 40^\circ\text{C}$  | $I_{F(AV)}$    | 2             |        |        |        |        |        |        | A                |
| Peak Forward Surge Current Single sine -wave Superimposed on Rated Load (JEDEC Method)                                | $I_{FSM}$      | 60            |        |        |        |        |        |        | A                |
| Maximum Forward Voltage drop per leg at 2 A   | $V_F$          | 1.2           |        |        |        |        |        |        | V                |
| Maximum DC Reverse Current $T_A = 25^\circ\text{C}$<br>at Rated DC Blocking Voltage per leg $T_A = 125^\circ\text{C}$ | $I_R$          | 5<br>500      |        |        |        |        |        |        | $\mu\text{A}$    |
| Typical Junction Capacitance per element at 1 MHz, 4 V  | $C_J$          | 25            |        |        |        |        |        |        | pF               |
| Operating and Storage Temperature Range   | $T_j, T_{stg}$ | - 55 to + 150 |        |        |        |        |        |        | $^\circ\text{C}$ |



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