

DCR2150X14

Replaces DS6021-1

DS6021-2 June 2019 (LN38840)

Phase Control Thyristor

FEATURES

- Double Side Cooling
- High Surge Capability

High Power Drives

Static Switches

VOLTAGE RATINGS

Part and

Ordering

Number

DCR2150X14

DCR2150X12

DCR2150X10

DCR2150X08

DCR2150X06

High Voltage Power Supplies

Repetitive Peak

Voltages

V_{DRM} and V_{RRM}

1400

1200

1000

800

600

Conditions

 $T_{vi} = -40^{\circ}C$ to 125°C,

 $I_{DRM} = I_{RRM} = 150 \text{mA},$

 V_{DRM} , V_{RRM} t_{p} = 10ms,

V_{DSM} & V_{RSM} = V_{DRM} & V_{RRM} +100V

respectively

APPLICATIONS

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KEY PARAMETERS

1400 V
2150 A
29000 A
1000 V/µs
200 A/µs

* Higher dV/dt selections available

Image: Contract of the second seco

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DCR2150X14

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.



CURRENT RATINGS

 $T_{case} = 60^{\circ}C$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled			
I _{T(AV)}	Mean on-state current	Half wave resistive load	2150	А
I _{T(RMS)}	RMS value	-	3380	А
Ι _Τ	Continuous (direct) on-state current	-	3040	А

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{TSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 125^{\circ}C$	29.0	kA
l ² t	I ² t for fusing	$V_R = 0$	4.21	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	5	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.018	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Double side cooled	DC	-	0.005	°C/W
T _{vj}	Virtual junction temperature	Blocking V _{DRM} / _{VRRM}		-	125	°C
T _{stg}	Storage temperature range			-40	140	°C
Fm	Clamping force			26	34	kN

DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditio	ons	Min.	Max.	Units
I _{RRM} /I _{DRM}	Peak reverse and off-state current	At V _{RRM} /V _{DRM} , T _{case} = 125°C		-	150	mA
dV/dt	Max. linear rate of rise of off-state voltage	To 67% V _{DRM} , T _j = 125°C, ga	ate open	1000	-	V/µs
dl/dt	Rate of rise of on-state current	From 67% V _{DRM} to 2000A	Repetitive 50Hz	-	200	A/µs
		Gate source 30V, 10Ω ,	Non-repetitive	-	1000	A/µs
		t _r < 0.5μs, Τ _j = 125°C				
V _T	On-state voltage	I _T = 3000A, T _{case} = 125°C			1.23	V
V _{T(TO)}	Threshold voltage	T _{case} = 125°C		-	0.84	V
r _T	On-state slope resistance	T _{case} = 125°C		-	0.13	mΩ
t _{gd}	Delay time	$V_D = 67\% V_{DRM}$, gate source	30V, 10Ω	-	3.0	μs
		$t_r = 0.5 \mu s, T_j = 25^{\circ}C$				
ΙL	Latching current	$T_j = 25^{\circ}C,$		-	1	А
I _Η	Holding current	$T_j = 25^{\circ}C,$		-	200	mA

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V _{GT}	Gate trigger voltage	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	3	V
V _{GD}	Gate non-trigger voltage	At 40% V _{DRM} , T _{case} = 125°C	0.3	V
I _{GT}	Gate trigger current	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	300	mA
I _{GD}	Gate non-trigger current	At 40% V _{DRM} , T _{case} = 125°C	20	mA

CURVES

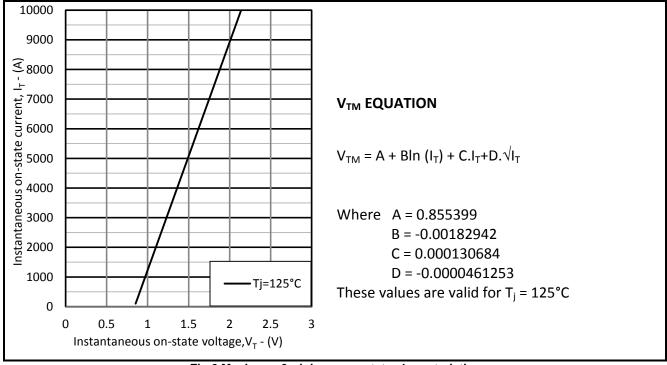


Fig.2 Maximum & minimum on-state characteristics

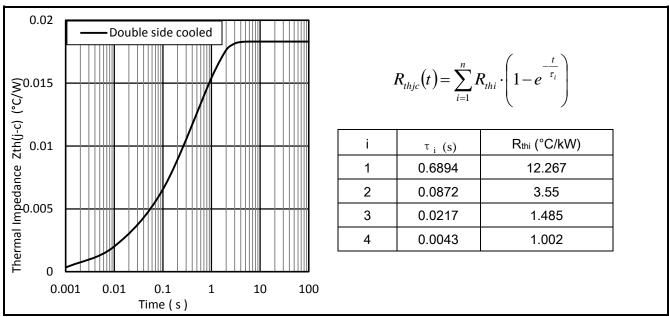


Fig.3 Maximum (limit) transient thermal impedance – junction to case (°C/W)

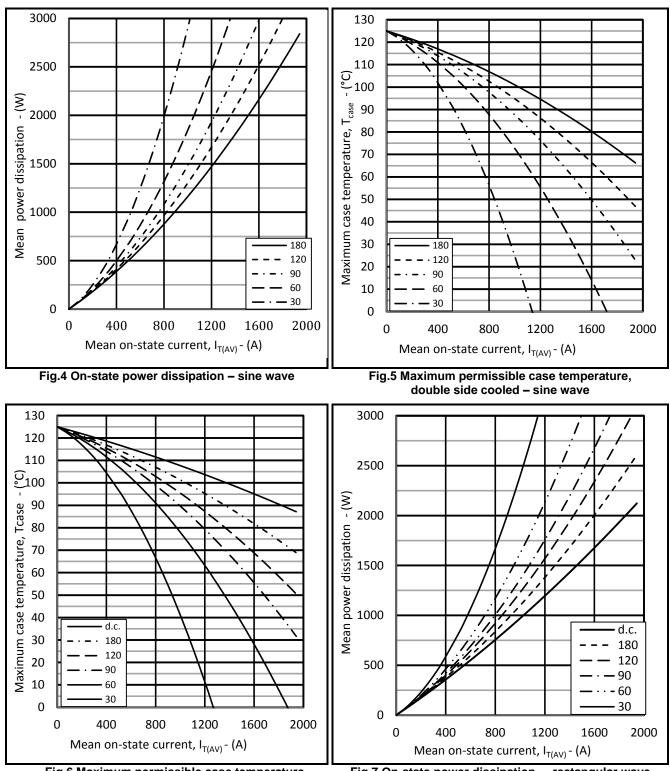
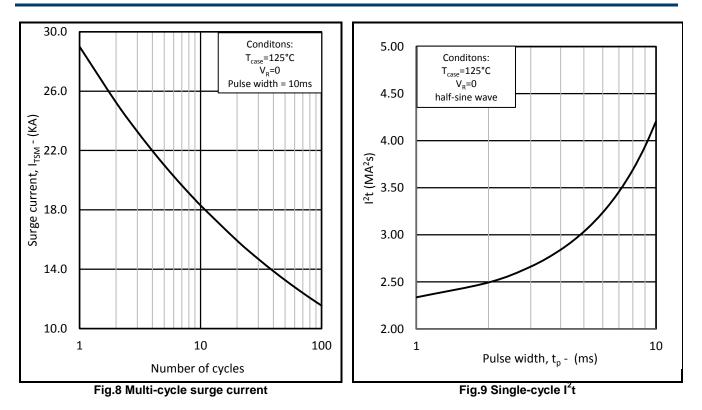


Fig.6 Maximum permissible case temperature, double side cooled – rectangular wave



DCR2150X14



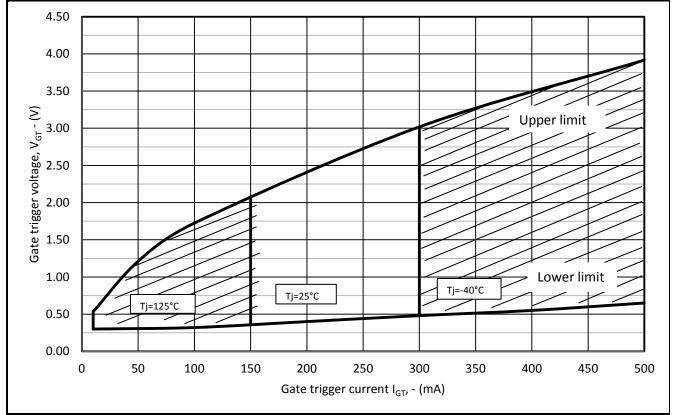
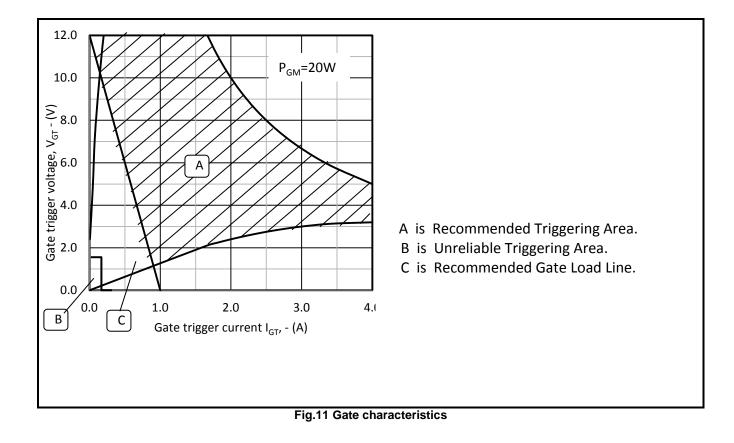


Fig.10 Gate characteristics



PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

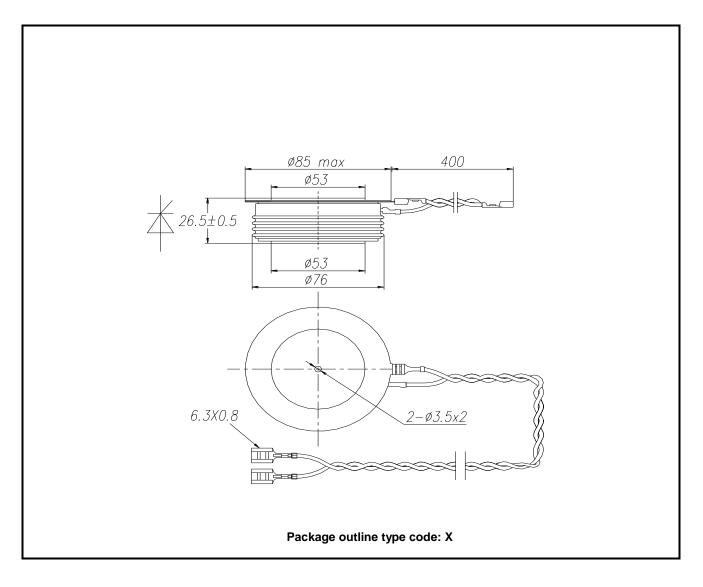


Fig.12 Package outline

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