

DE10S3L

Schottky Barrier Diodes
30V, 10A

Feature

- SMD
- High Recovery Speed
- Low V_F
- Pb free terminal
- RoHS:Yes

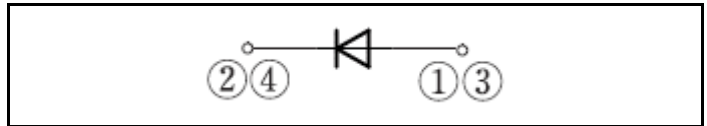
OUTLINE

Package (House Name): E-pack

Package (JEITA Code): SC-63



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : $T_c=25^\circ\text{C}$)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T_{stg}		-55 to 150	$^\circ\text{C}$
Junction temperature	T_j		150	$^\circ\text{C}$
Repetitive peak reverse voltage	V_{RRM}		30	V
Repetitive peak surge reverse voltage	V_{RRSM}	Pulse width 0.5ms, duty=1/40	35	V
Average forward current	$I_F(AV)$	50Hz sine wave, Resistance load, $T_c=124^\circ\text{C}$	10	A
Surge forward current	I_{FSM}	50Hz sine wave, Non-repetitive, 1 cycle, Peak value, $T_j=25^\circ\text{C}$	250	A
Repetitive peak surge reverse power	P_{RRSM}	Pulse width 10 μs , $T_j=25^\circ\text{C}$	660	W

※ : See the original Specifications

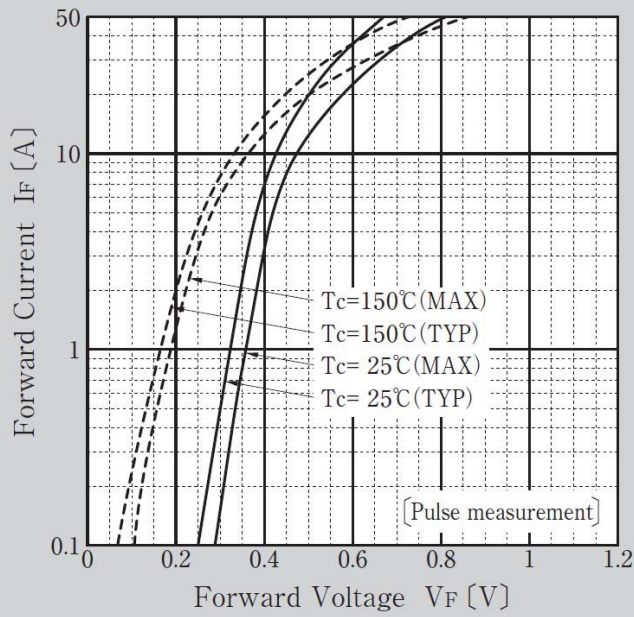
Electrical Characteristics (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Forward voltage	V _F	I _F =8A, Pulse measurement			0.45	V
Reverse current	I _R	V _R =30V, Pulse measurement			10	mA
Total capacitance	C _t	f=1MHz, V _R =10V		640		pF
Thermal resistance	R _{th(j-c)}	Junction to case			4	°C/W

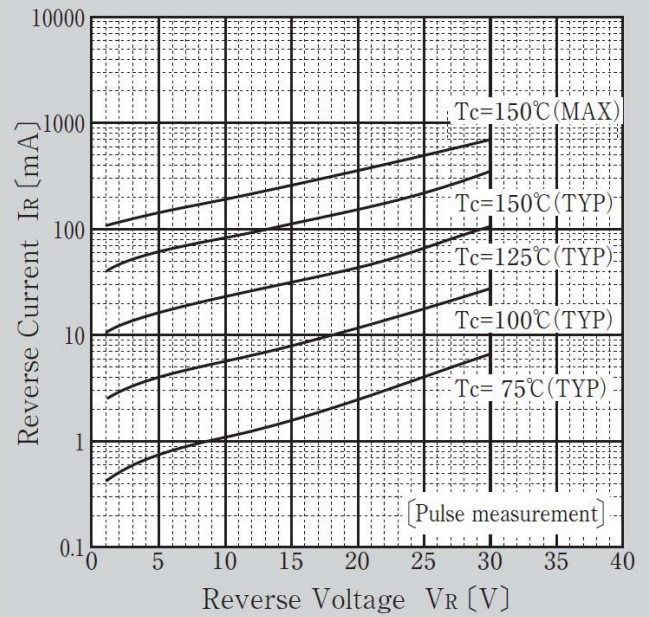
* : See the original Specifications

CHARACTERISTIC DIAGRAMS

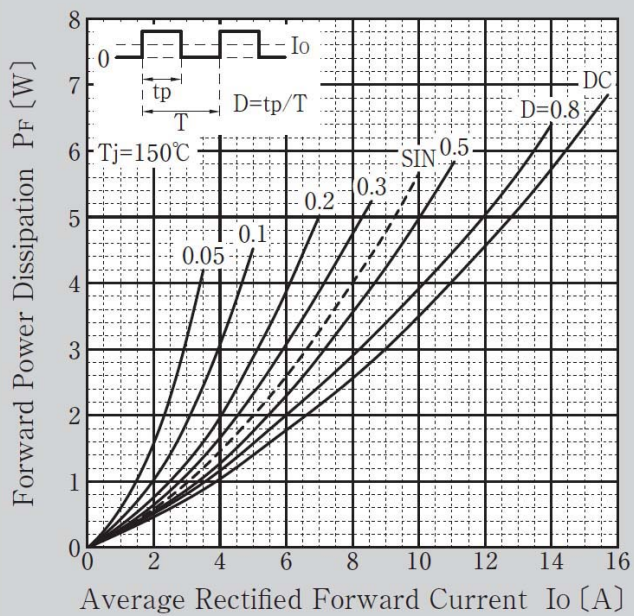
Forward Voltage



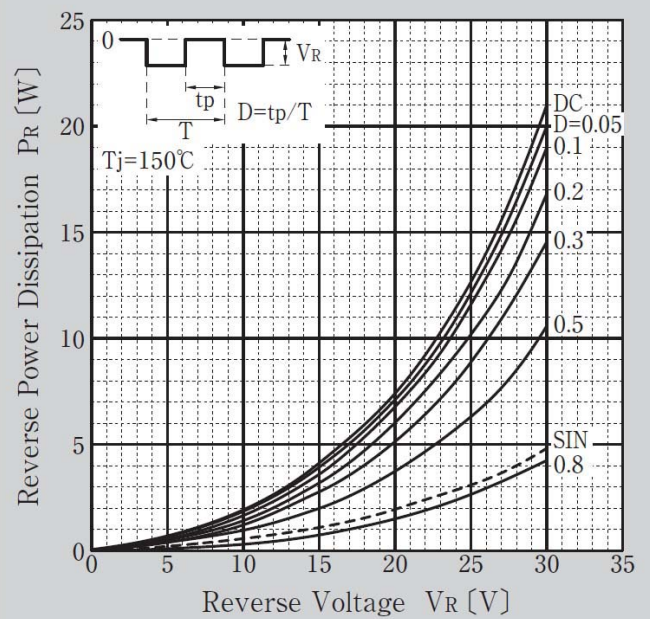
Reverse Current

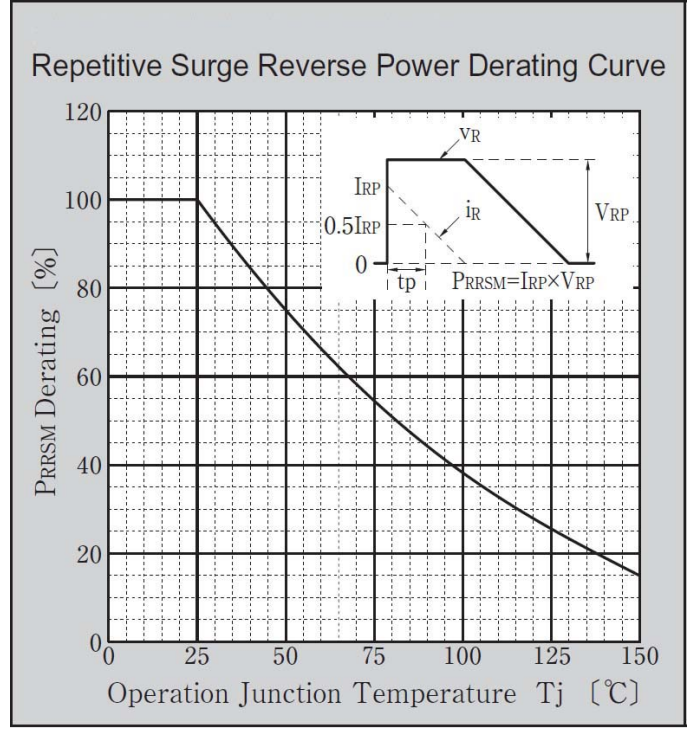
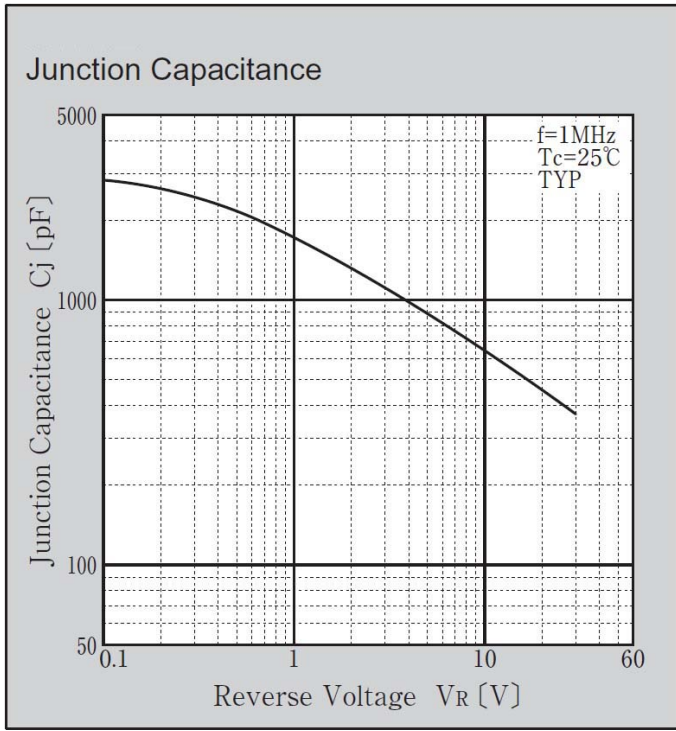
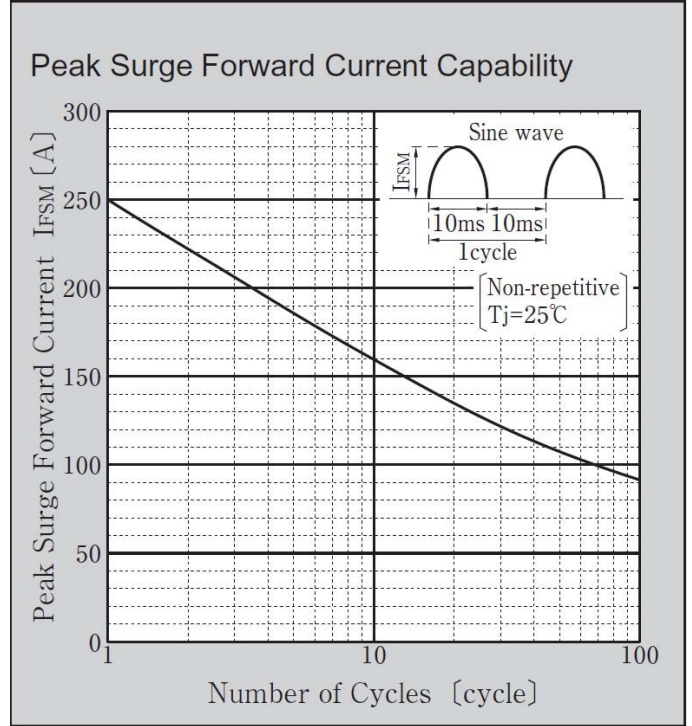
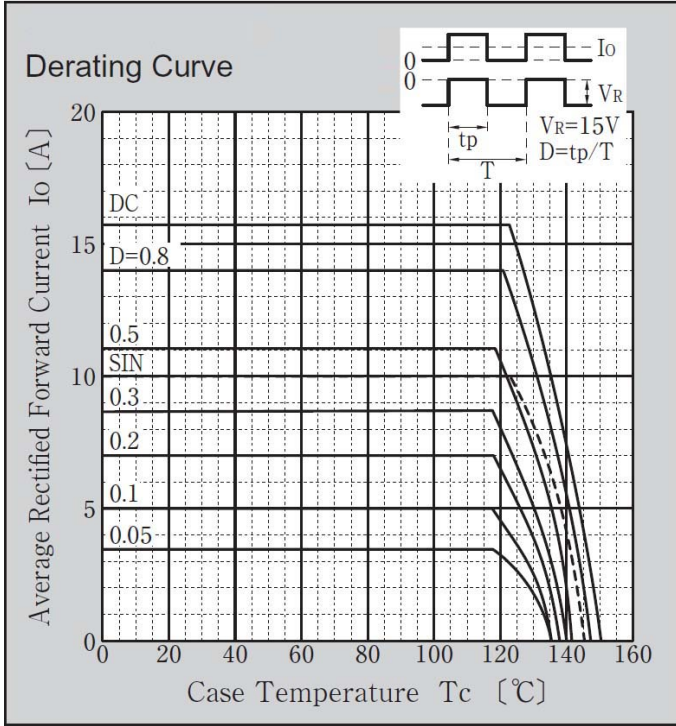


Forward Power Dissipation

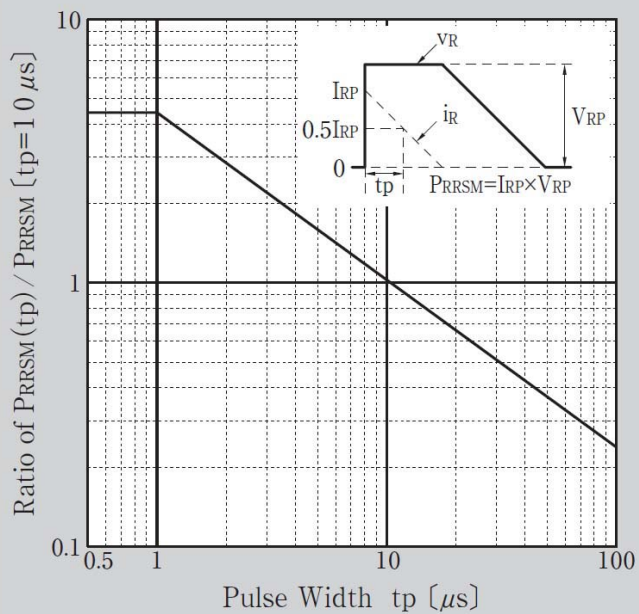


Reverse Power Dissipation



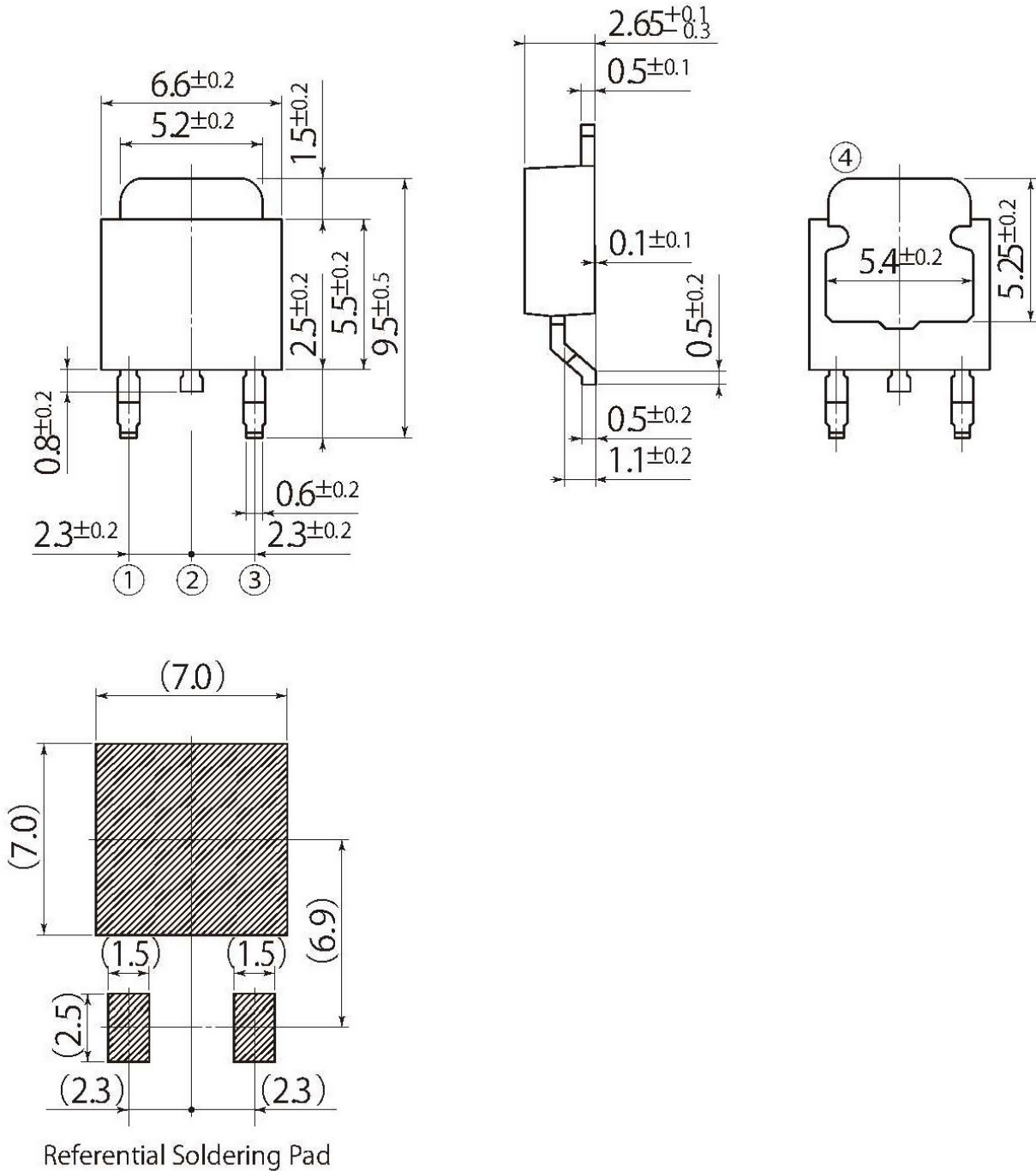


Repetitive Surge Reverse Power Capability



G1

JEDEC Code	-
JEITA Code	SC-63
House Name	E-pack



• Optimize soldering pad to the board design and soldering condition.

Notes

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