

Technical Data Data Sheet N1736, Rev. - **Green Products**

DF151 THRU DF157 SINGLE-PHASE GLASS PASSIVATED SILICON BRIDGE RECTIFIER

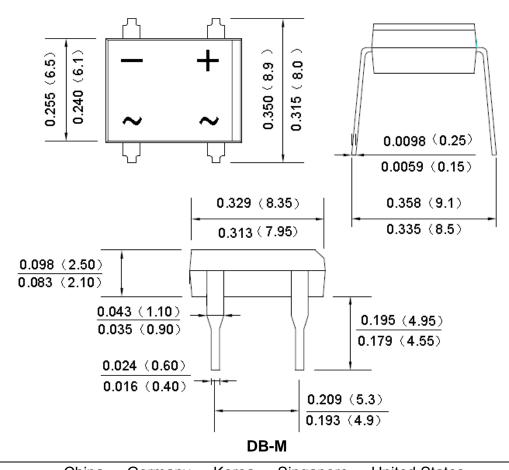
Features:

- Glass passivated die construction
- Low forward voltage drop
- High current capability
- High surge current capability
- Plastic material-UL flammability 94V-0

Mechanical Data:

- Case: DB-M, molded plastic
- Terminals: plated leads solderable per MIL-STD-202, Method 208
- · Polarity: as marked on case
- Mounting position: Any
- Marking: type number
- Lead Free: For RoHS / Lead Free Version,

Mechanical Dimensions: In Inches/ mm



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Technical Data Data Sheet N1736, Rev. - Green Products

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Type Number	Symbol	DF151	DF152	DF153	DF154	DF155	DF156	DF157	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	50	100	200	400	600	800	1000	V
RMS Reverse Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Average Forward Output Current (Note 1) @ T _A = 40°C	Io	1.5							Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	55							А
Forward Voltage Drop per Bridge Element @IF=1.5A	V _F	1.1							٧
Peak Reverse Current $@T_A = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_A = 125^{\circ}C$	I _R	5 500							μA
Typical Junction Capacitance per leg (Note 2)	CJ	25							pF
Typical Thermal Resistance per leg	R _{θJA}	40							°C/W
	$R_{\theta JL}$	15							
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55+150							°C

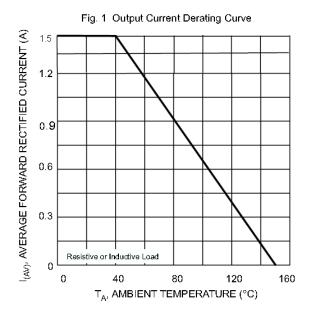
Note: 1. Mounted on glass epoxy PC board with 1.3mm² solder pad. 2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

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WSD 20

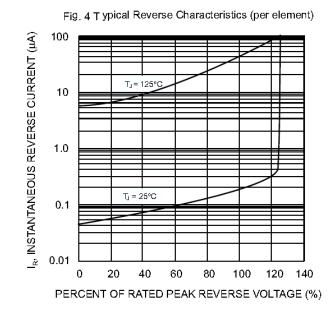
T_A = 25°C
Single Half Sine-Wave
Pulse Width =8.3ms
(JEDEC Method)

10

NUMBER OF CYCLES AT 60 Hz

1.0

Fig. 3 Maximum Peak Forward Surge Current (per leg)



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