

1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

PowerDI[®]123

DFLS120L

Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- High Current Capability and Low Forward Voltage Drop
- Lead Free Finish, RoHS Compliant (Note 4)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDl[®]123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.01 grams (approximate)



Top View

Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	20	V
RMS Reverse Voltage	V _{R(RMS)}	14	V
Average Forward Current	I _{F(AV)}	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	IFSM	50	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 1)	PD	1.67	W
Power Dissipation (Note 2)	PD	556	mW
Thermal Resistance Junction to Ambient (Note 1)	R _{0JA}	60	°C/W
Thermal Resistance Junction to Ambient (Note 2)	R _{0JA}	180	°C/W
Thermal Resistance Junction to Soldering (Note 3)	R _{θJS}	10	°C/W
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V _{(BR)R}	20	_	_	V	$I_R = 1.0 \text{mA}$
		_	0.20	-	V	$I_{F} = 0.1A$
Forward Voltage	VF		0.30	_		$I_{F} = 0.7A$
			0.32	0.36		I _F = 1.0A
Leakage Current (Note 5)	1-		0.26	_	mA	$V_R = 5V, T_A = 25^{\circ}C$ $V_R = 20V, T_A = 25^{\circ}C$
Leakage Current (Note 5)	IR	—	_	1.0		$V_{R} = 20V, T_{A} = 25^{\circ}C$
Total Capacitance	CT	_	75	_	pF	V _R = 10V, f = 1.0MHz

1. Part mounted on 50.8mm X 50.8mm GETEK board with 25.4mm X 25.4mm copper pad, 25% anode, 75% cathode. $T_A = 25^{\circ}$ C.

2. Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads. T_A = 25°C.

3. Theoretical R_{0JS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.

4. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.

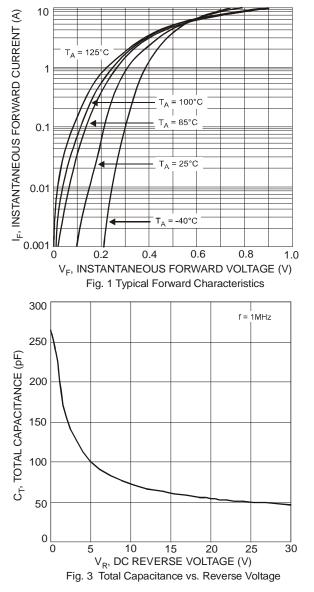
Short duration pulse test used to minimize self-heating effect.

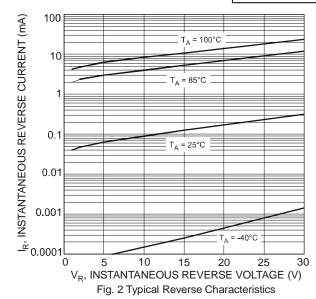
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Notes:







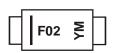


Ordering Information (Note 6)

Part Number	Case	Packaging
DFLS120L-7	PowerDI [®] 123	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



F02 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

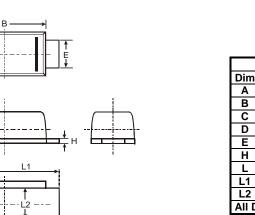
Date Code Key

Year	2004	20	05	2006	2007	20	08	2009	2010	20	11	2012
Code	R		6	Т	U	,	V	W	Х		(Z
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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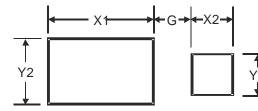
Package Outline Dimensions



PowerDI [®] 123						
Dim	Min	Max	Тур			
Α	3.50	3.90	3.70			
в	2.60	3.00	2.80			
C	1.63	1.93	1.78			
D	0.93	1.00	0.98			
ш	0.85	1.25	1.00			
H	0.15	0.25	0.20			
_	0.55	0.75	0.65			
L1	1.80	2.20	2.00			
L2	0.95	1.25	1.10			
All Dimensions in mm						

Suggested Pad Layout

Е



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4



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