



#### 1200V FIELD STOP IGBT IN TO-247

#### Description

The DGTD120T25S1PT is produced using advanced Field Stop Trench IGBT Technology, which provides low  $V_{\text{CE(sat)}}$ , excellent quality and high-switching performance.

#### **Features**

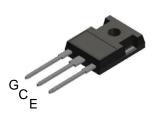
- High Speed Switching & Low V<sub>CE(sat)</sub> Loss
- $V_{CE(sat)} = 2.0V @ I_C = 25A$
- High Input Impedance
- $t_{rr} = 100$ ns (typ) @  $di_F/dt = 500$ A/ $\mu$ s
- Ultra-Soft, Fast Recovery Anti-parallel Diode
- Ultra Narrowed VF Distribution Control
- Positive Temperature Coefficient For Easy Parallelling
- Maximum Junction Temperature 175°C
- Lead-Free Finish & RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Applications**

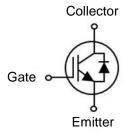
- Motor Drive
- UPS
- Welder
- Solar Inverter
- IH Cooker

#### **Mechanical Data**

- Case: TO-247 (Type MC)
- Case Material: Molded Plastic. "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Terminals: Finish Matte Tin Plated Leads.
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 5.6 grams (Approximate)



TO-247



Device Symbol

### **Ordering Information** (Note 4)

Ī	Product	Marking	Quantity		
	DGTD120T25S1PT	DGTD120T25S1	450 per Box in Tubes (Note 5)		

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.
- 5. 30 Devices per Tube.

#### **Marking Information**



);; = Manufacturer's Marking
DGTD120T25S1 = Product Type Marking Code
YY = Year (ex: 18 = 2018)
LLLLL = Lot Code
WW = Week (01 to 53)



## Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Emitter Voltage	VCE	1,200	V
T <sub>C</sub> = 25	°C ,	50	А
DC Collector Current, limited by $T_{vjmax}$ $T_C = 10^{-10}$	0°C	25	Α
Pulsed Collector Current, tp limited by Tvjmax	I <sub>Cpuls</sub>	100	Α
Turn Off Safe Operating Area V <sub>CE</sub> ≤ 1200V, T <sub>vj</sub> = 175°C	-	100	Α
Diada Farrand Compart limited by T	°C ,	25	Α
Diode Forward Current limited by $T_{vjmax}$ $T_C = 10^{-10}$	0°C	12.5	Α
Diode Pulsed Current, tp limited by Tvimax	I <sub>Fpuls</sub>	100	Α
Gate-Emitter Voltage	V <sub>GE</sub>	±20	V
Short Circuit Withstand Time			
$V_{CC} \le 600V$ , $V_{GE} = 15V$ , $T_{vj} = 175$ °C	tsc	10	μs
Allowed Number of Short Circuits < 1000	isc		
Time Between Short Circuits ≥ 1.0s			

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation Linear Derating Factor (Note 6)	P <sub>D</sub>	348	W	
T <sub>C</sub> = 100°C		174	VV	
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>0</sub> JA	40		
Thermal Resistance, Junction to Case for IBGT (Note 6)	$R_{ heta JC}$	0.43	°C/W	
Thermal Resistance, Junction to Case for Diode (Note 6)	R <sub>0</sub> JC	1.55		
Operating Temperature	T <sub>vi</sub>	-40 to +175	°C	
Storage Temperature Range	T <sub>STG</sub>	-55 to +150		

Note: 6. When mounted on a standard JEDEC 2-layer FR-4 board.



## Electrical Characteristics (@T<sub>vj</sub> = +25°C, unless otherwise specified.)

Parameter		Symbol	Min	Тур	Max	Unit	Condition	
STATIC CHARACTERISTICS	- Cymber		. , , ,	max	<b>O</b>	Containen		
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>	1200	_	_	V	I <sub>C</sub> = 500μA, V <sub>GE</sub> = 0V		
Collector-Ethilter Breakdown Voltage	T <sub>vj</sub> = 25°C	DACES	-	2.00	2.40	· ·	1C = 300μA, VGE = 0V	
Collector-Emitter Saturation Voltage	$T_{vi} = 150$ °C	V <sub>CE(sat)</sub>	_	2.40	_	V	$I_C = 25A$ , $V_{GE} = 15V$	
Composer Emilian Galaration Voltago	$T_{vi} = 175^{\circ}C$		_	2.50	_			
	$T_{vi} = 25^{\circ}C$		_	2.10	2.60	.,		
Diode Forward Voltage	$T_{vj} = 175^{\circ}C$	$V_{F}$	_	1.90	_	V	$V_{GE} = 0V, I_F = 12.5A$	
	$T_{vj} = 173 \text{ C}$		_	2.50	3.00		V <sub>GE</sub> = 0V, I <sub>F</sub> = 25A	
Diode Forward Voltage	$T_{vj} = 150$ °C	V <sub>F</sub>		2.55	_	V		
	$T_{vi} = 175^{\circ}C$	• -	_	2.45	_	•		
Gate-Emitter Threshold Voltage	110 - 170 0	V <sub>GE(th)</sub>	5.0	6.0	7.0	V	$V_{CE} = V_{GE}, I_{C} = 0.85 \text{mA}$	
	T <sub>vj</sub> = 25°C	- OE(ui)	_	_	250	_	102 102, 10 01001111	
Zero Gate Voltage Collector Current	T <sub>vi</sub> = 175°C	I <sub>CES</sub>	_	_	2500	μΑ	V <sub>CE</sub> = 1200V, V <sub>GE</sub> = 0V	
Gate-Emitter Leakage Current	1.49	I <sub>GES</sub>	_	_	±250	nA	$V_{GE} = 20V$ , $V_{CE} = 0V$	
Transconductance		9fs	_	16	_	S	$V_{CE} = 20V, I_{C} = 25A$	
DYNAMIC CHARACTERISTICS	'	0.0	l.	l	l.	I.	, , ,	
Total Gate Charge		Qg	_	204	_		V 000V I 05A	
Gate-Emitter Charge		Q <sub>ge</sub>	-	34	_	nC	$V_{CE} = 960V, I_{C} = 25A,$	
ate-Collector Charge		Q <sub>gc</sub>	_	94	_		V <sub>GE</sub> = 15V	
Input Capacitance		C <sub>ies</sub>	-	3942	_		.,	
Reverse Transfer Capacitance		Cres	_	72	_	pF	$V_{CE} = 25V, V_{GE} = 0V,$	
Output Capacitance		C <sub>oes</sub>	_	142	_		f = 1MHz	
Internal Emitter Inductance Measured 5mm (0.197")		L <sub>E</sub>	_	13	_	nH	_	
From Case  Short Circuit Collector Current Max. 1000 Short Circuits. Time Between Short Circuits ≥ 1.0s		I <sub>C(SC)</sub>	-	121	-	А	$V_{GE} = 15V, V_{CC} = 600V, \\ t_{SC} \le 10\mu s, T_{vj} = 175^{\circ}C$	
SWITCHING CHARACTERISTICS		4	_	73	_			
Turn-on Delay Time		t <sub>d(on)</sub>		41	_			
Rise time		t <sub>r</sub>		269		ns <sub>VGE</sub>	$V_{GE} = 15V, V_{CC} = 600V,$	
Turn-off Delay Time Fall Time	t <sub>d(off)</sub>	_	39	_		$I_C = 25A, R_G = 23\Omega,$		
		t <sub>f</sub> E <sub>on</sub>	_	1.44	_		Inductive Load,	
	n-on Switching Energy		_	0.55	_	m l	$T_{vj} = 25^{\circ}C$	
Turn-off Switching Energy Total Switching Energy		E <sub>off</sub>		1.99	_	mJ		
Reverse Recovery Time				100	_	ns		
Reverse Recovery Current		t <sub>rr</sub>	_	17	_	A	$I_F = 25A$ , $di_F/dt = 500A/\mu s$ ,	
,		Qrr	_	0.85	_	μC	V <sub>R</sub> = 600V,	
Reverse Recovery Charge				-376	_	μC A/μs	T <sub>vj</sub> = 25°C	
Rate Of Fall Of Reverse Current During t <sub>b</sub>		di <sub>rr</sub> /dt	_	65		Ανμδ		
Turn-on Delay Time		t <sub>d(on)</sub>	_	45	_	ns	V <sub>GE</sub> = 15V, V <sub>CC</sub> = 600V,	
Rise time		t <sub>r</sub>	_	292	_			
Turn-off Delay Time Fall Time		t <sub>d(off)</sub>	_	75	_		$I_C = 25A, R_G = 23\Omega,$	
		t <sub>f</sub>	_	2.43			Inductive Load,	
Turn-on Switching Energy		E <sub>on</sub>	_	1.09	_	mJ	T <sub>vj</sub> = 175°C	
Turn-off Switching Energy Total Switching Energy		E <sub>off</sub>		3.52		1110		
Reverse Recovery Time	E <sub>ts</sub>	_	150	_	ns			
Reverse Recovery Current		t <sub>rr</sub>	_	25	_	A	I <sub>F</sub> = 25A, di <sub>F</sub> /dt = 500A/μs,	
Reverse Recovery Current Reverse Recovery Charge		I <sub>rr</sub> Q <sub>rr</sub>	_	1.85	_	μC	V <sub>R</sub> = 600V, -T <sub>vj</sub> = 175°C	
Rate Of Fall Of Reverse Current During	1 th	di <sub>rr</sub> /dt	_	-374	_	μC A/μs		
Nate Of Fall Of Neverse Current Duffing	uirr/ut		5/4		Αμο			



## Typical Performance Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

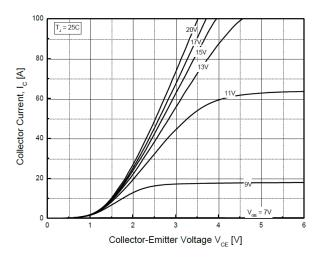


Fig.1 Typical Output Characteristic(T<sub>J</sub>=25°C)

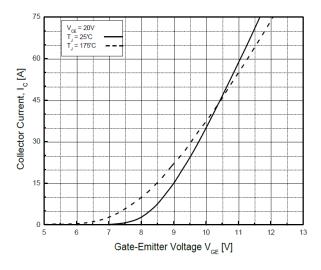


Fig.3 Typical Transfer Characteristic

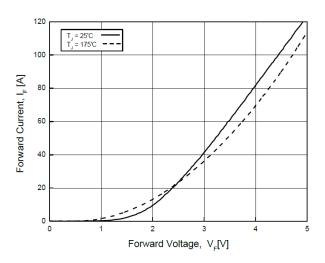


Fig.5 Diode Forward Characteristic

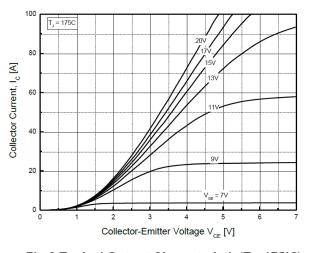


Fig.2 Typical Output Characteristic(T<sub>J</sub>=175°C)

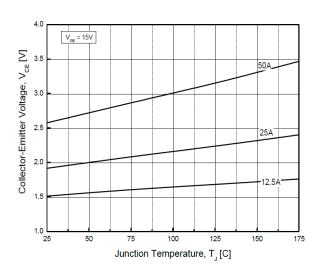


Fig.4 Typical Collector-Emitter Saturation Voltage
-Junction Temperature

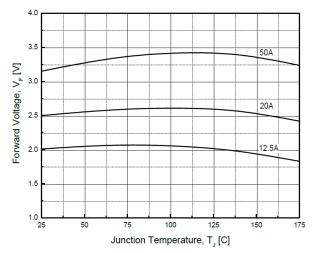


Fig.6 Diode Forward-Junction Temperature



## Typical Performance Characteristics (continued)

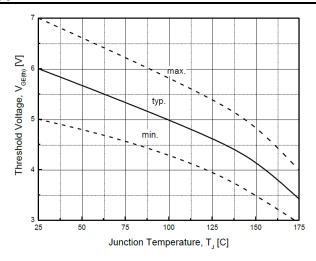


Fig.7 Threshold Voltage-Junction Temperature

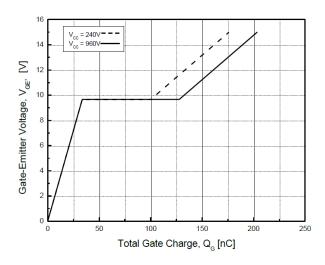


Fig.9 Typical Gate Charge

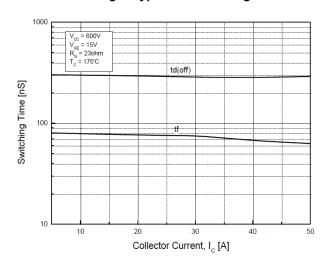


Fig.11 Typical Turn off-Collector Current

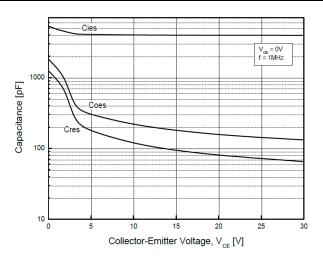


Fig.8 Typical Capacitance

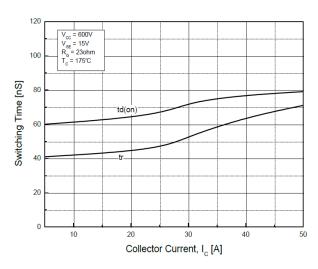


Fig.10 Typical Turn on-Collector Current

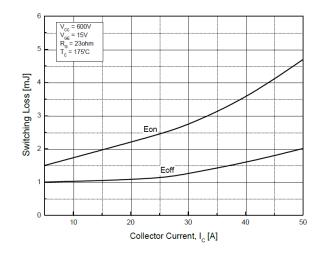
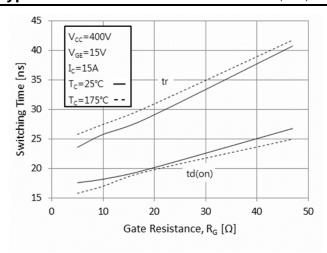


Fig.12 Switching Loss-Collector Current



### **Typical Performance Characteristics** (cont.)



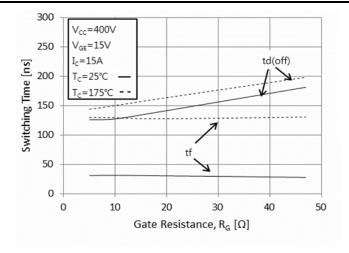


Fig.13 Turn on Characteristics-Gate Resistance

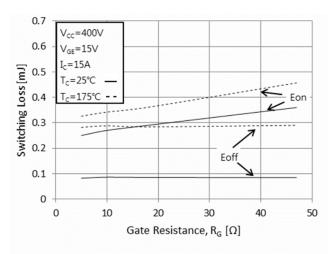


Fig.14 Turn off Characteristics-Gate Resistance

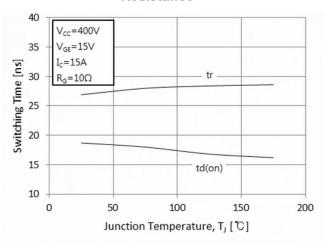


Fig.15 Switching Loss-Gate Resistance

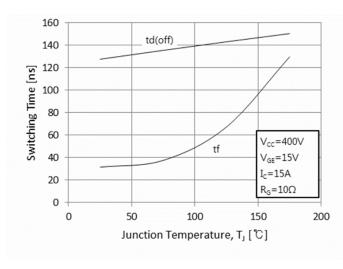


Fig.16 Turn on Characteristics-Junction Temperature

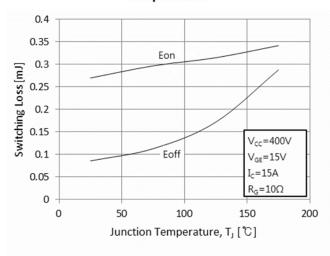


Fig.17 Turn off Characteristics-Junction Temperature

Fig.18 Switching Loss-Junction Temperature



## **Typical Performance Characteristics (cont.)**

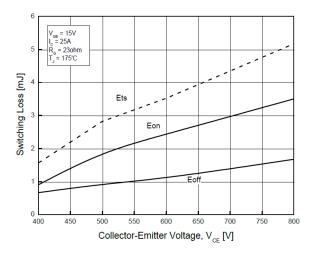


Fig.19 Switching Loss-Collector Emitter Voltage

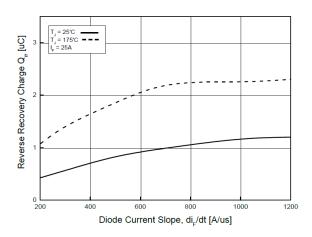


Fig.21 Reverse Recovery Charge -Diode Current Slope

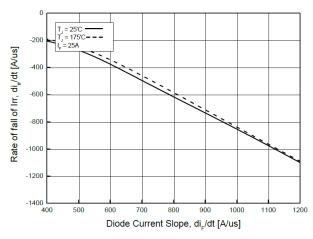


Fig.23 Rate of fall of reverse recovery current
-Diode Current Slope

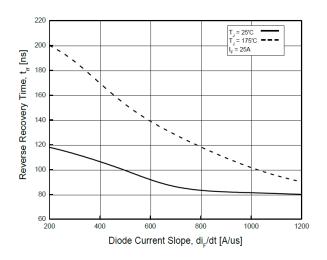


Fig.20 Reverse Recovery Time -Diode current slope

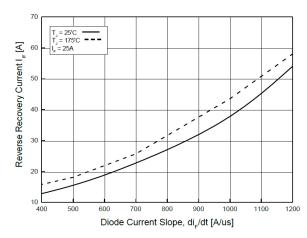


Fig.22 Reverse Recovery Current
-Diode current slope

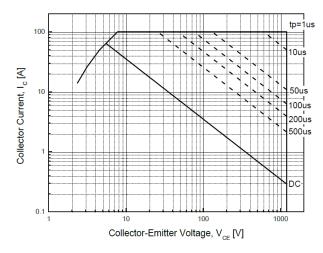
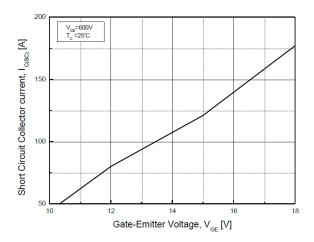


Fig.24 Forward Bias Safe Operating Area



## **Typical Performance Characteristics (cont.)**



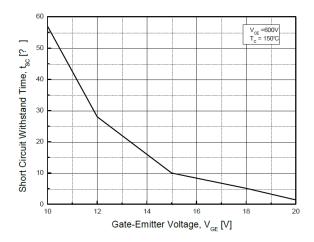
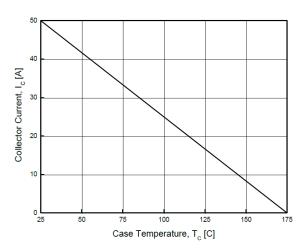


Fig.25 Typical Short Circuit Collector Current

Fig.26 Typical Short Circuit Withstand Time



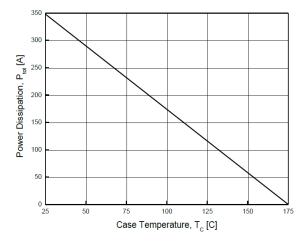
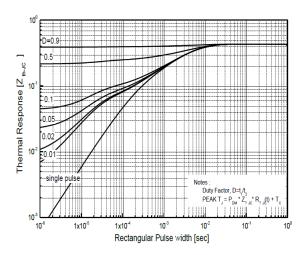


Fig.27 Case Temperature-Collector Current

Fig.28 Power Dissipation-Case Temperature



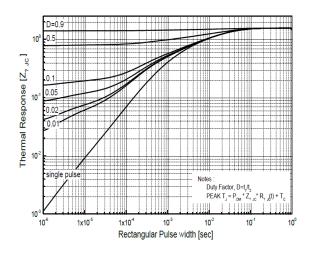


Fig.29 IGBT Transient Thermal Impedance

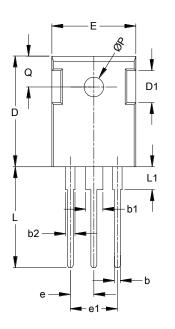
Fig.30 FRD Transient Thermal Impedance

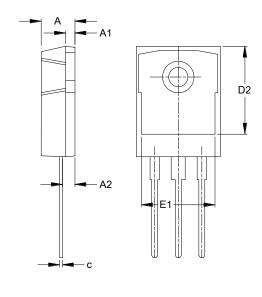


### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TO-247 (Type MC)





TO-247 (Type MC)						
Dim	Min	Max	Тур			
Α	4.700	5.310	-			
A1	1.500	2.490	-			
A2	2.200	2.600	-			
b	0.990 1.400 -					
b1	2.590 3.430 -					
b2	1.650 2.390 -					
С	0.380 0.890 -					
D	20.30					
D1	4.320	5.490	-			
D2	13.08	-	-			
Е	15.45	16.26	-			
E1	13.06 14.02 -					
е	5.450					
e1	10.90					
L	19.81 20.57 -					
L1	- 4.500 -					
Q	5.380 6.200 -					
øΡ	3.500 3.700 -					
All Dimensions in mm						

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.



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