



DIM250KHM33-TS001

Half Bridge IGBT Module

DS6391-6 April 2024 (LN43308) Replaces DS6391-5

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base with AIN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- **Traction Auxiliaries**
- Choppers

The Powerline range of high-power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM250KHM33-TS001 is a half bridge 3300V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA). This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM250KHM33-TS001

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		3300\
V _{CE(sat)}	* (typ)	2.2V
l _c	(max)	250A
I _{C(PK)}	(max)	500A

^{*} Measured at the auxiliary terminals

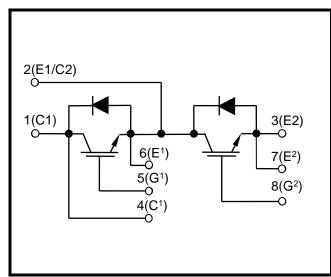


Fig. 1 Circuit configuration



Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Vces	Collector-emitter voltage	V _{GE} = 0V	3300	V
V _{GES}	Gate-emitter voltage		±20	V
Ic	Continuous collector current	T _{case} = 110°C	250	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 140°C	500	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	2.6	kW
l²t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C	20	kA ² s
Visol	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	7700	V
Q _{PD}	Partial discharge – per module	IEC1287, V ₁ = 4800V, V ₂ = 3500V, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

53mm

20mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	48	°C/kW
R _{th(j-c)}	Thermal resistance – Diode	Continuous dissipation - junction to case	-	-	96	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 6Nm (with mounting grease)	-	-	16	°C/kW
т.	Junction temperature	Transistor	-	-	150	°C
Tj		Diode	-	-	150	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	6	Nm
		Electrical connections – M6	-	-	6	Nm

ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Ices	Collector cut-off current	V _{GE} = 0V, V _{CE} = V _{CES}			1	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 125°C			15	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 150°C			25	mA
I _{GES}	Gate leakage current	V _{GE} = ± 20V, V _{CE} = 0V			1	μA
V _{GE(TH)}	Gate threshold voltage	Ic = 20mA, V _{GE} = V _{CE}		6.3		V
		V _{GE} = 15V, I _C = 250A		2.2		V
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 250A, T _j = 125°C		2.8		V
	- cararaman ramaga	V _{GE} = 15V, I _C = 250A, T _j = 150°C		3.0		V
lF	Diode forward current	DC		250		Α
I _{FM}	Diode maximum forward current	$t_p = 1 ms$		500		А
	Diode forward voltage	I _F = 250A		2.4		V
VF		I _F = 250A, T _j = 125°C		2.5		V
		I _F = 250A, T _j = 150°C		2.4		V
Cies	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		45		nF
Qg	Gate charge	±15V		5		μC
Cres	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		1		nF
L _M	Module inductance			40		nΗ
RINT	Internal transistor resistance			500		μΩ
SC _{Data}	Short circuit current, Isc	$T_{j} = 150^{\circ}\text{C}, V_{CC} = 2500\text{V}$ $t_{p} \le 10\mu\text{s}, V_{GE} \le 15\text{V}$ $V_{CE (max)} = V_{CES} - L^{*} x dI/dt$ $IEC 60747-9$		950		A

Note: * L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

 $T_{case} = 25$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 250A		2000		ns
t _f	Fall time	$V_{GE} = \pm 15V$		400		ns
Eoff	Turn-off energy loss	V _{CE} = 1800V		480		mJ
t _{d(on)}	Turn-on delay time	$R_{g(ON)} = 12\Omega$ $R_{g(OFF)} = 16.5\Omega$ $L_S \sim 250 nH$		240		ns
t _r	Rise time			160		ns
Eon	Turn-on energy loss			330		mJ
Qrr	Diode reverse recovery charge	I _F = 250A V _{CE} = 1800V		150		μC
Irr	Diode reverse recovery current			250		Α
Erec	Diode reverse recovery energy	dl _F /dt = 1500A/μs		165		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	L 350A		2050		ns
tf	Fall time	$I_{C} = 250A$ $V_{GE} = \pm 15V$		440		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 1800V		540		mJ
$t_{d(on)}$	Turn-on delay time	$R_{g(ON)} = 12\Omega$ $R_{g(OFF)} = 16.5\Omega$ $L_S \sim 250 nH$		260		ns
tr	Rise time			170		ns
Eon	Turn-on energy loss			420		mJ
Qrr	Diode reverse recovery charge	I _F = 250A V _{CE} = 1800V dI _F /dt = 1500A/μs		250		μC
Irr	Diode reverse recovery current			300		Α
Erec	Diode reverse recovery energy			350		mJ

T_{case} = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time	Ic = 250A		2100		ns
t _f	Fall time	$V_{GE} = \pm 15V$		450		ns
Eoff	Turn-off energy loss	$V_{CE} = 1800V$		580		mJ
t _{d(on)}	Turn-on delay time	$R_{g(ON)} = 12\Omega$ $R_{g(OFF)} = 16.5\Omega$ $L_{S} \sim 250 \text{nH}$		260		ns
t _r	Rise time			180		ns
Eon	Turn-on energy loss			470		mJ
Qrr	Diode reverse recovery charge	I _F = 250A		300		μC
Irr	Diode reverse recovery current	V _{CE} = 1800V dI _F /dt = 1500A/μs		310		Α
Erec	Diode reverse recovery energy			400		mJ

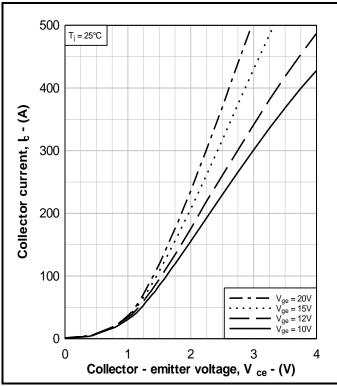


Fig. 3 Typical output characteristics

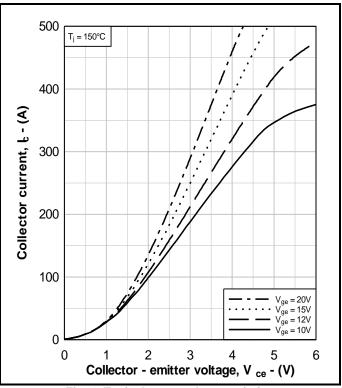


Fig. 4 Typical output characteristics

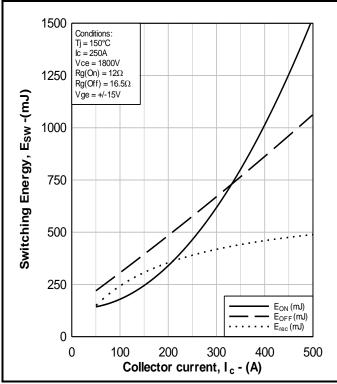


Fig. 5 Typical switching energy vs collector current

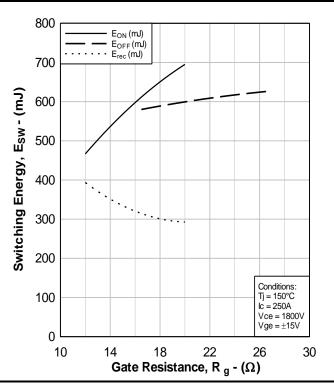


Fig. 6 Typical switching energy vs gate resistance

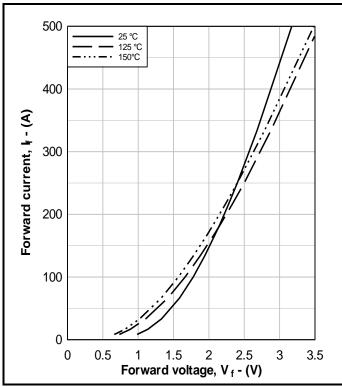


Fig. 7 Diode typical forward characteristics

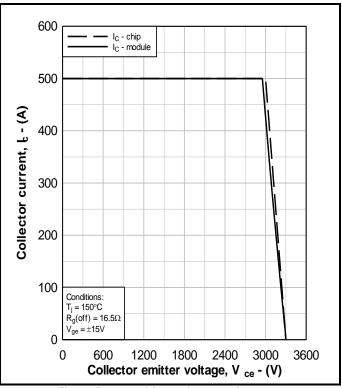


Fig. 8 Reverse bias safe operating area

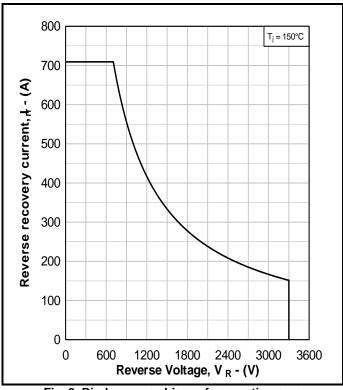


Fig. 9 Diode reverse bias safe operating area

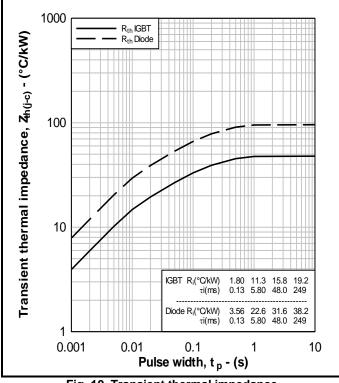


Fig. 10 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

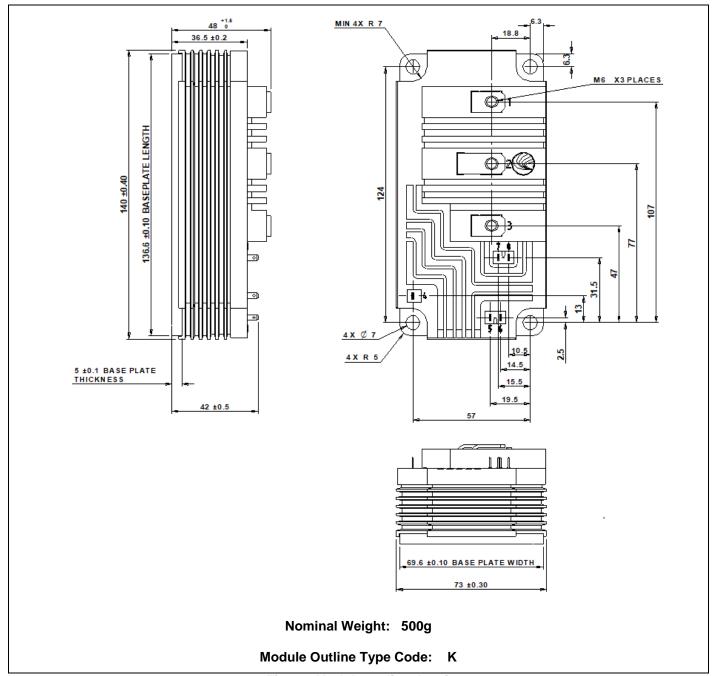


Fig. 11 Module outline drawing

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