

DIM600DDM17-A000

Dual Switch IGBT Module

DS5596-1.1 September 2005 (LN24180)

FEATURES

- 10µs Short Circuit Withstand
- Non Punch Through Silicon
- Isolated AlSiC Baseplate with AlN Substrates
- High Thermal Cycling Capability

APPLICATIONS

- High Power Inverters
- Motor Controllers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bidirectional switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The DIM600DDM17-A000 is a dual switch 1700V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full $10\mu s$ short circuit withstand. This module is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM600DDM17-A000

Note: When ordering, please use the whole part number.

KEY PARAMETERS

V _{CES}		1700V
V _{CE (sat)} *	(typ)	2.7V
Ic	(max)	600A
I _{C(PK)}	(max)	1200 <i>A</i>

*(measured at the power busbars and not the auxiliary terminals)

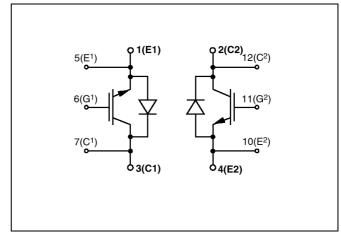


Fig. 1 Dual switch circuit diagram

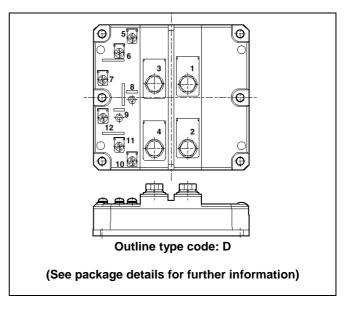


Fig. 2 Electrical connections (not to scale)

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.



ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

Tcase = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Units
V _{CES}	Collector-emitter voltage	$V_{GE} = 0V$	1700	V
V _{GES}	Gate-emitter voltage		±20	V
Ic	Continuous collector current	T _{case} = 75°C	600	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} =105°C	1200	Α
P _{max}	Max. transistor power dissipation	T _{case} = 25°C, T _j = 150°C	5200	W
l ² t	Diode I ² t value (IGBT arm)	$V_R = 0$, $t_P = 10$ ms, $T_{vj} = 125$ °C	120	kA 2S
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V
Q_{PD}	Partial discharge per module	IEC1287, V ₁ = 1800V, V ₂ = 1300V, 50 Hz RSM	10	рС

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THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Critical Tracking Index):

AIN

AISiC

20mm

10mm

175

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
R _{th(j-c)}	Thermal resistance - transistor	Continuous dissipation – junction to case	'	-	24	°C/kW
R _{th(j-c)}	Thermal resistance - diode	Continuous dissipation – junction to case	-	-	40	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
Tj	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
-	Screw torque	Mounting – M6	-	-	5	Nm
	·	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Units
I _{ces}	Collector cut-off current	V _{GE} = 0V, V _{CE} = V _{CES}		-	-	1	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 125°	С	-	-	20	mA
I _{ces}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$		-	-	4	μA
V _{GE(TH)}	Gate threshold voltage	$I_C = 30$ mA, $V_{GE} = V_{CE}$		4.5	5.5	6.5	V
$V_{CE(sat)^{\dagger}}$	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 600A		-	2.7	3.2	V
		V _{GE} = 15V, I _C = 600A, T _{case} = 125°	С	-	3.4	4.0	V
I _F	Diode forward current	DC		-	-	600	Α
I _{FM}	Diode maximum forward current	t _p = 1ms		-	-	1200	Α
V_{F^\dagger}	Diode forward voltage	I _F = 600A		-	2.0	2.3	V
		I _F = 600A, T _{case} = 125°C		-	2.1	2.4	V
Cies	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		-	45	-	nF
L_M	Module inductance -per arm	-		-	3.8	-	nH
R _{INT}	Internal resistance –per arm	-		-	0.27	-	mΩ
SC _{Data}	Short circuit. I _{sc}	$T_j = 125^{\circ}C, V_{cc} = 1000V,$	I ₁	-	2780	-	Α
		$\begin{aligned} t_p & \leq 10 \mu s, \\ V_{CE(max)} &= V_{CES} - L^* \times di/dt \\ IEC 60747-9 \end{aligned}$	l ₂	-	2400	-	A

Note:

 $^{^{\}scriptscriptstyle\dagger}$ Measured at the power busbars and not the auxiliary terminals

 $^{^{*}}$ L is the circuit inductance + L_{M}



ELECTRICAL CHARACTERISTICS

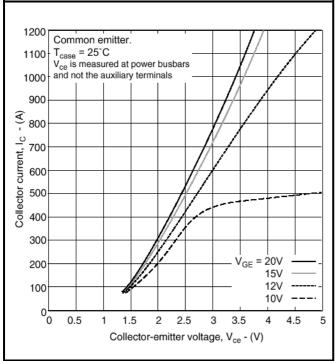
 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 600A	-	1200	-	ns
t _f	Fall time	$V_{GE} = \pm 15V$	-	140	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 900V	-	190	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 3.3\Omega$	-	250	-	ns
t _r	Rise time	L ~ 100nH	-	250	-	ns
Eo	Turn-on energy loss		-	220	-	mJ
Qg	Gate charge		-	6.8	-	μC
Qrr	Diode reverse recovery charge	$I_F = 600A$, $V_R = 900V$,	-	150	-	μC
Irr	Diode reverse current	dl _F /dt = 3000A/µs	-	350	-	А
E _{REC}	Diode reverse recovery energy		-	100	-	mJ

T_{case} = 125°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 600A	-	1500	-	ns
t _f	Fall time	$V_{GE} = \pm 15V$	-	170	-	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 900V	-	270	-	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 3.3\Omega$	-	400	-	ns
t _r	Rise time	L ~ 100nH	-	250	-	ns
E _{ON}	Turn-on energy loss		-	350	-	mJ
Qrr	Diode reverse recovery charge	I _F = 600A, V _R = 900V,	-	250	-	μC
I _{rr}	Diode reverse current	$dI_F/dt = 3000A/\mu s$	-	400	-	А
E _{REC}	Diode reverse recovery energy		-	150	-	mJ

DIM600DDM17-A000



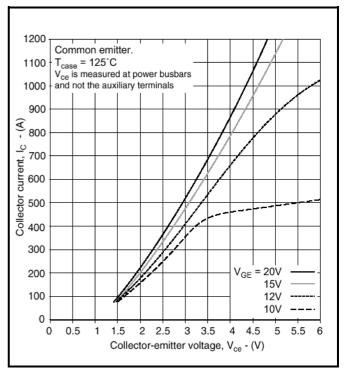
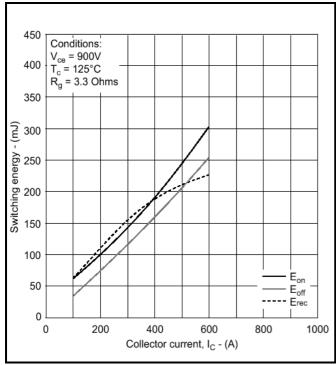
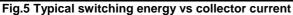


Fig.3 Typical output characteristics







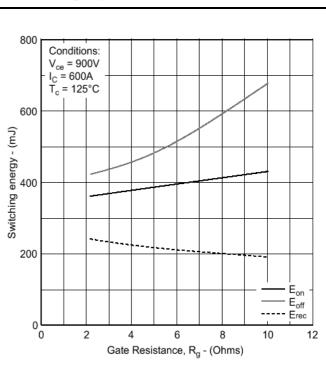
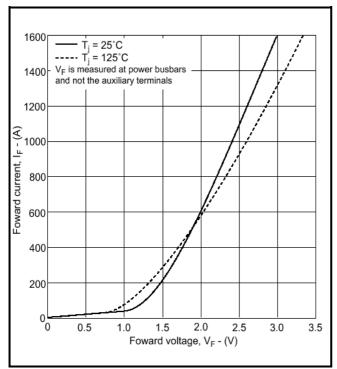


Fig.6 Typical switching energy vs gate resistance

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures



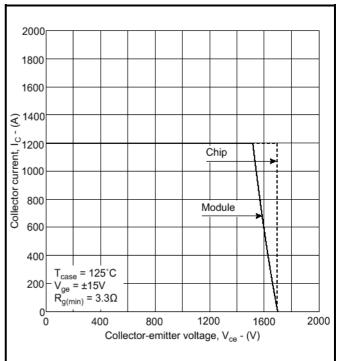
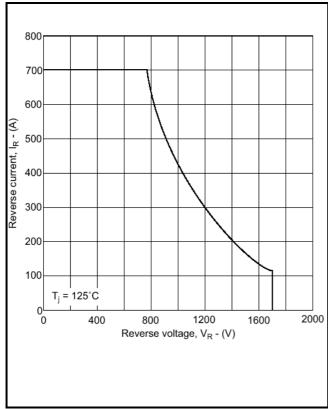


Fig.7 Diode typical forward characteristics





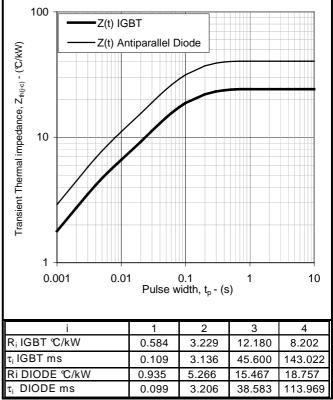


Fig.9 Diode reverse bias safe operating area

Fig.10 Transient thermal impedance

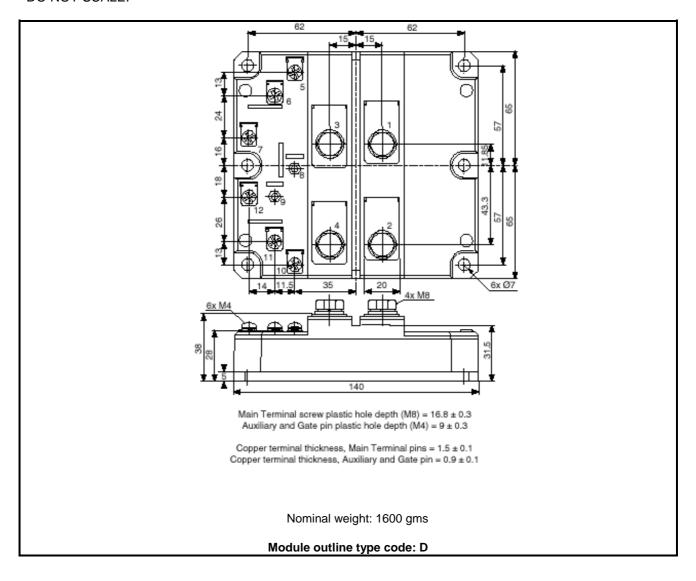
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PACKAGE DETAILS

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DO NOT SCALE.



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The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



http://www.dynexsemi.com

e-mail: power solutions@dynexsemi.com

HEADQUARTERS OPERATIONS
DYNEX SEMICONDUCTOR LTD

Doddington Road, Lincoln Lincolnshire, LN6 3LF. United Kingdom.

Tel: +44(0)1522 500500 Fax: +44(0)1522 500550 **CUSTOMER SERVICE**

Tel: +44(0)1522 502753 / 502901. Fax: +44(0)1522 500020

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Advance Information: The product design is complete and final characterisation for volume production is well in hand.

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