DIM800ACM45-TS000

IGBT Chopper Module

Replaces DS6187-3

DS6187-4 August 2023 (LN42740)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AlSiC Base With AlN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM800ACM45-TS000 is a 4500V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM800ACM45-TS000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}		**4500V**
$V_{CE(sat)}$ *	**(typ)**	**2.5V**
I_C	**(max)**	**800A**
$I_{C(PK)}$	**(max)**	**1600A**

* Measured at the auxiliary terminals

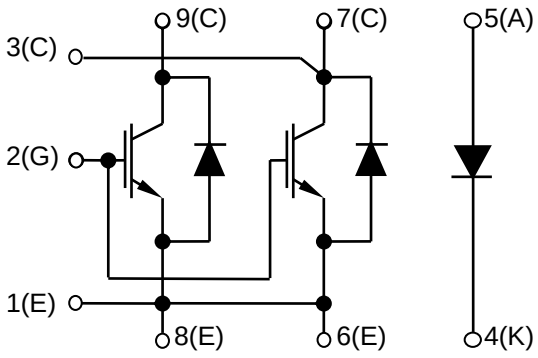


Fig. 1 Circuit configuration

Outline type code: A

(See Fig. 11 for further information)

Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V_{CES}	Collector-emitter voltage	V_{GE} = 0V	4500	V
V_{GES}	Gate-emitter voltage		±20	V
I_C	Continuous collector current	T_{case} = 90°C	800	A
$I_{C(PK)}$	Peak collector current	1ms, T_{case} = 115°C	1600	A
P_{max}	Max. transistor power dissipation	T_{case} = 25°C, T_j = 125°C	8.3	kW
I^2t	Diode I^2t value	V_R = 0, t_p = 10ms, T_j = 125°C	205	kA^2s
V_{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	7.4	kV
Q_{PD}	Partial discharge – per module	IEC1287, V_1 = 4800V, V_2 = 3500V, 50Hz RMS	10	pC

THERMAL AND MECHANICAL RATINGS

Internal insulation material: AlN

Baseplate material: AlSiC

Creepage distance: 56mm

Clearance: 26mm

CTI (Comparative Tracking Index): >600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$R_{th(j-c)}$	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	12	°C/kW
$R_{th(j-c)}$	Thermal resistance – diode (IGBT arm)	Continuous dissipation - junction to case	-	-	24	°C/kW
	Thermal resistance – diode (Diode arm)		-	-	24	°C/kW
$R_{th(c-h)}$	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	°C/kW
T_j	Junction temperature	Transistor	-	-	125	°C
		Diode	-	-	125	°C
T_{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I_{CES}	Collector cut-off current	V_{GE} = 0V, V_{CE} = V_{CES}			1	mA
		V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125°C			40	mA
I_{GES}	Gate leakage current	V_{GE} = ± 20V, V_{CE} = 0V			1	µA
$V_{GE(TH)}$	Gate threshold voltage	I_C = 80mA, V_{GE} = V_{CE}		5.8		V
$V_{CE(sat)}$	Collector-emitter saturation voltage	V_{GE} = 15V, I_C = 800A		2.5		V
		V_{GE} = 15V, I_C = 800A, T_j = 125°C		3.2		V
I_F	Diode forward current	DC		800		A
I_{FM}	Diode maximum forward current	t_p = 1ms		1600		A
V_F	Diode forward voltage	I_F = 800A		2.9		V
		I_F = 800A, T_j = 125°C		3.1		V
C_{ies}	Input capacitance	V_{CE} = 25V, V_{GE} = 0V, f = 1MHz		100		nF
Q_g	Gate charge	±15V		15		µC
C_{res}	Reverse transfer capacitance	V_{CE} = 25V, V_{GE} = 0V, f = 1MHz		8		nF
L_M	Module inductance			15		nH
R_{INT}	Internal transistor resistance (IGBT arms)			135		µΩ
	Internal transistor resistance (Diode arm)			270		µΩ
SC_{Data}	Short circuit current, I_{SC}	T_j = 125°C, V_{CC} = 3400V t_p ≤ 10µs, V_{GE} ≤ 15V $V_{CE\ (max)}$ = V_{CES} – L^* x dI/dt IEC 60747-9		3200		A

Note:
* L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	I_C = 800A V_{GE} = ±15V V_{CE} = 2800V $R_{G(ON)}$ = 3.9Ω $R_{G(OFF)}$ = 3.9Ω C_{ge} = 150nF L_S ~ 165nH		2800		ns
t_f	Fall time			530		ns
E_{OFF}	Turn-off energy loss			2500		mJ
$t_{d(on)}$	Turn-on delay time			1000		ns
t_r	Rise time			160		ns
E_{ON}	Turn-on energy loss			3500		mJ
Q_{rr}	Diode reverse recovery charge	I_F = 800A V_{CE} = 2800V dI_F/dt = 1900A/µs		880		µC
I_{rr}	Diode reverse recovery current			680		A
E_{rec}	Diode reverse recovery energy			1100		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	I_C = 800A V_{GE} = ±15V V_{CE} = 2800V $R_{G(ON)}$ = 3.9Ω $R_{G(OFF)}$ = 3.9Ω C_{ge} = 150nF L_S ~ 165nH		2900		ns
t_f	Fall time			490		ns
E_{OFF}	Turn-off energy loss			2600		mJ
$t_{d(on)}$	Turn-on delay time			1000		ns
t_r	Rise time			170		ns
E_{ON}	Turn-on energy loss			4800		mJ
Q_{rr}	Diode reverse recovery charge	I_F = 800A V_{CE} = 2800V dI_F/dt = 2000A/µs		1450		µC
I_{rr}	Diode reverse recovery current			750		A
E_{rec}	Diode reverse recovery energy			2000		mJ

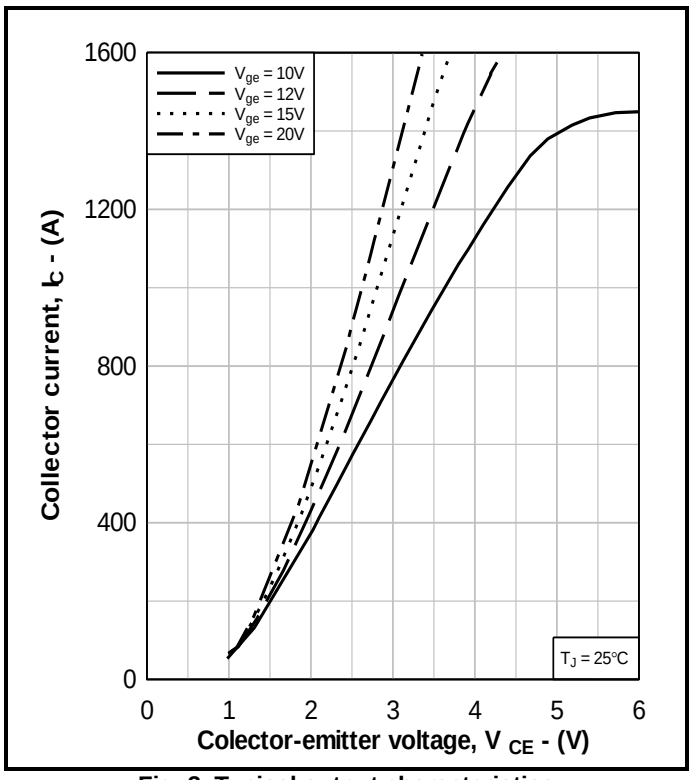


Fig. 3 Typical output characteristics

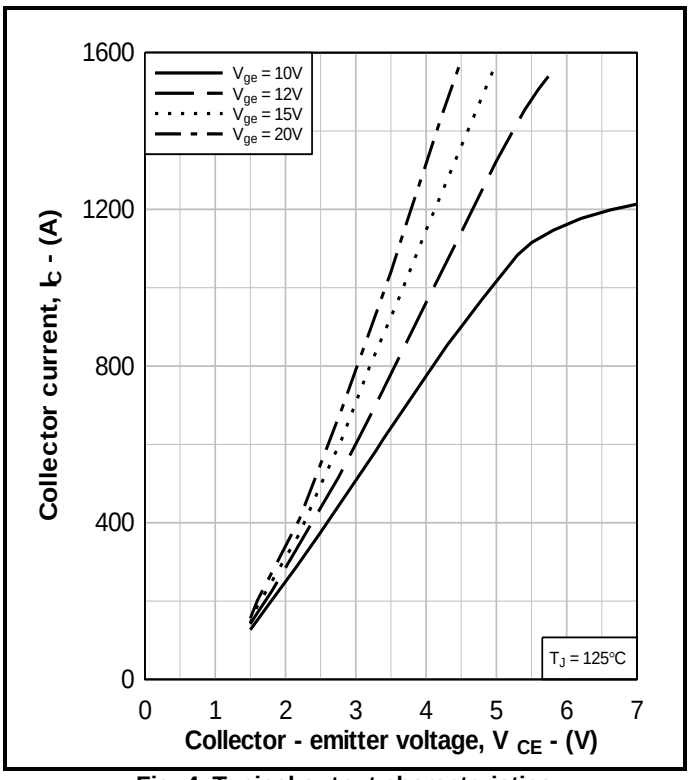


Fig. 4 Typical output characteristics

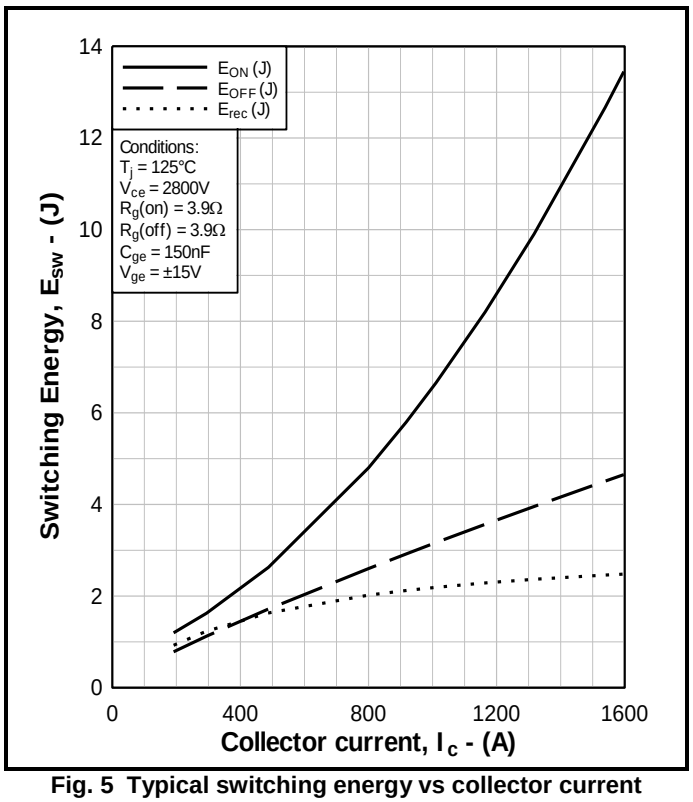


Fig. 5 Typical switching energy vs collector current

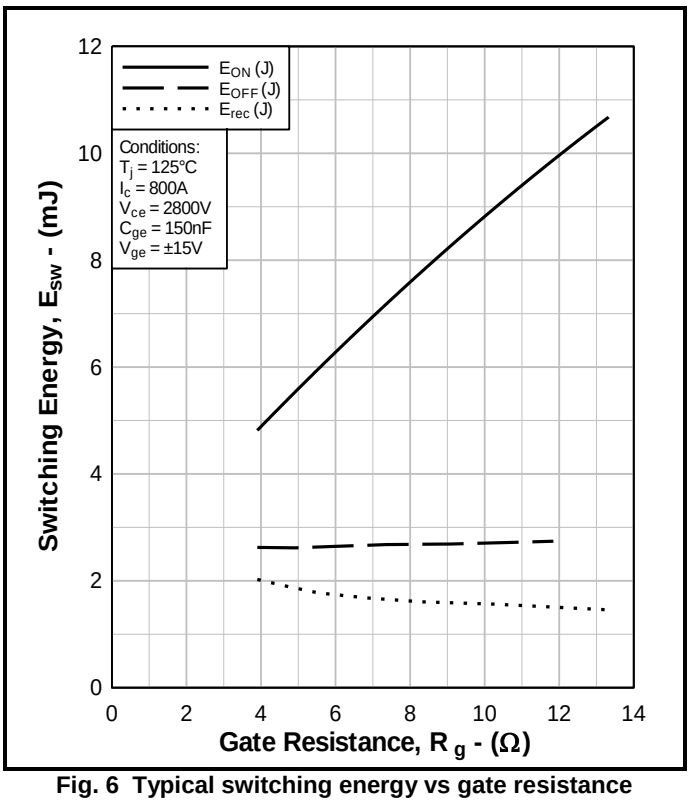


Fig. 6 Typical switching energy vs gate resistance

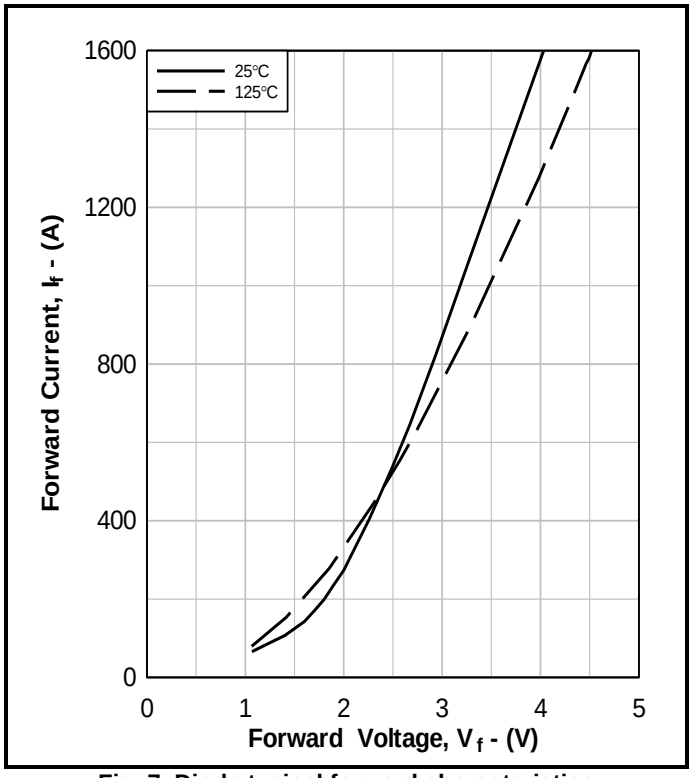


Fig. 7 Diode typical forward characteristics

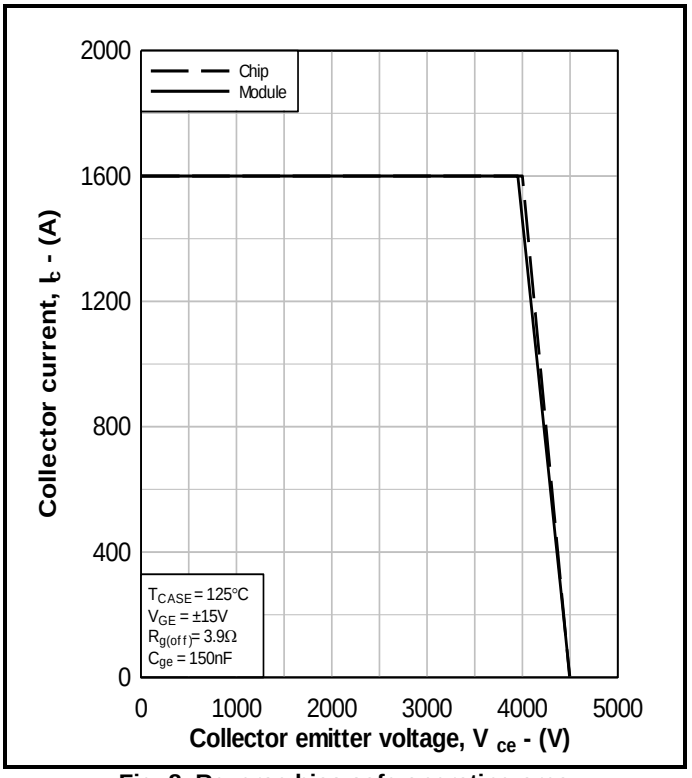


Fig. 8 Reverse bias safe operating area

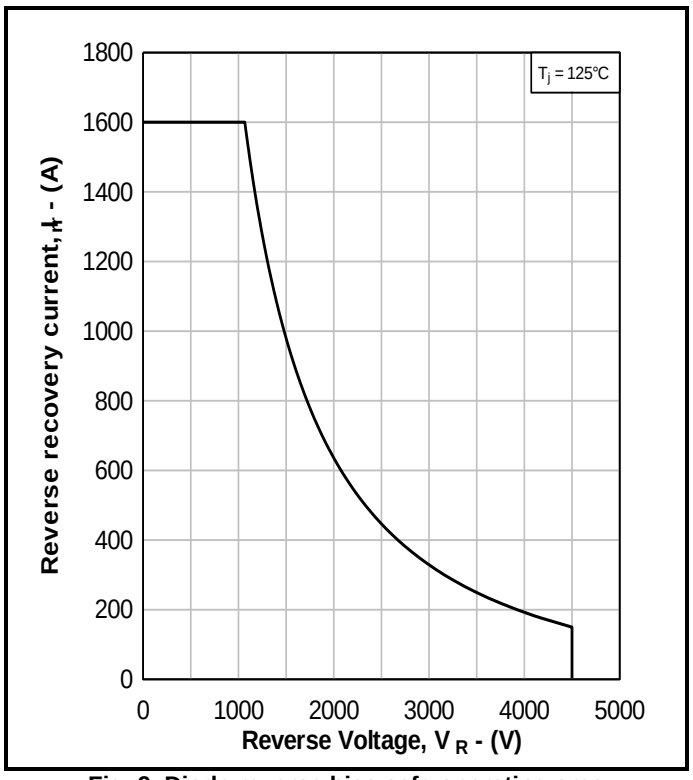


Fig. 9 Diode reverse bias safe operating area

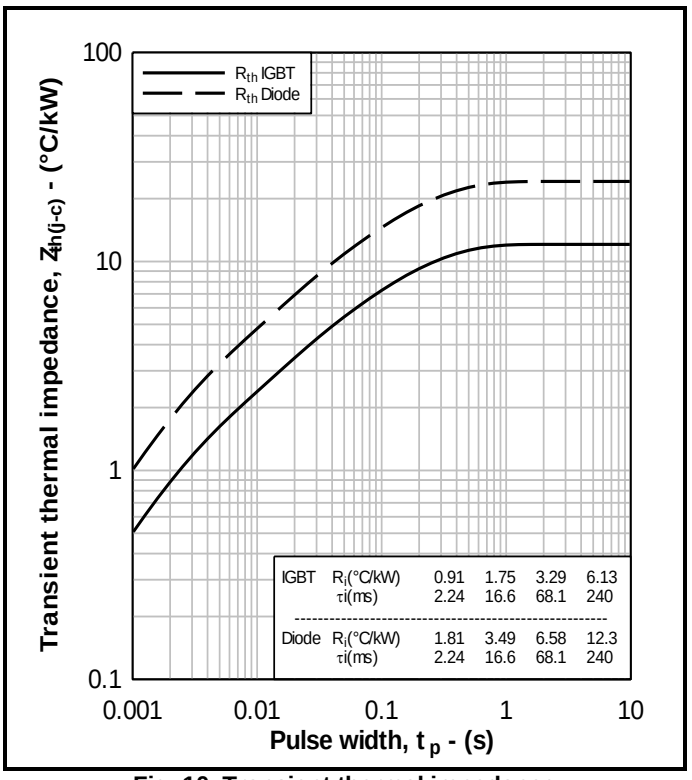
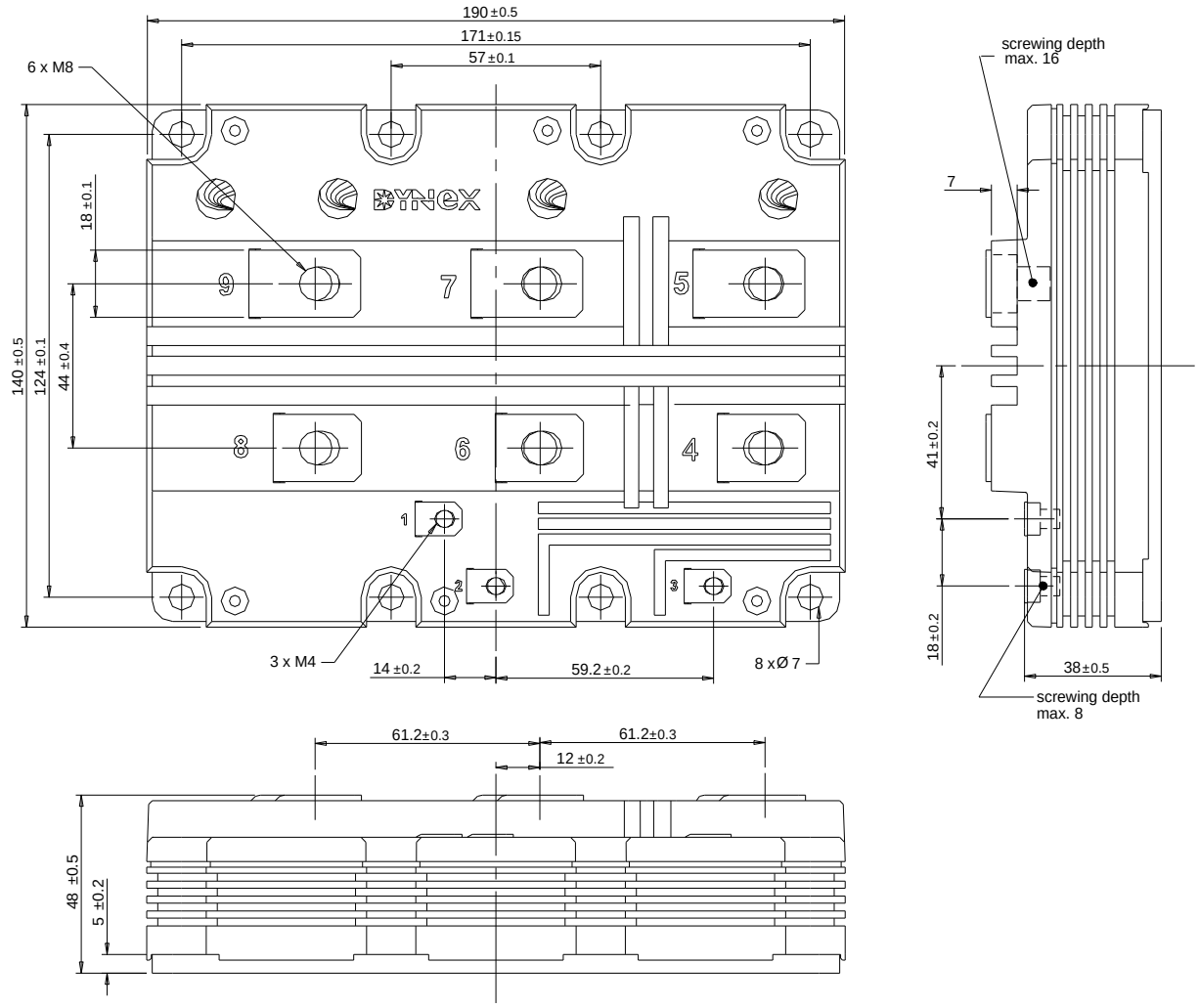


Fig. 10 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services.
All dimensions in mm, unless stated otherwise.
DO NOT SCALE.



Nominal Weight: 1700g

Module Outline Type Code: A

Fig. 11 Module outline drawing

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HEADQUARTERS OPERATIONS

DYNEX SEMICONDUCTOR LTD
Doddington Road, Lincoln, Lincolnshire, LN6 3LF,
United Kingdom
Fax: +44(0)1522 500550
Tel: +44(0)1522 500500
Web: http://www.dynexsemi.com

CUSTOMER SERVICE

DYNEX SEMICONDUCTOR LTD
Doddington Road, Lincoln, Lincolnshire, LN6 3LF,
United Kingdom

Tel: +44(0)1522 502753 / 502901
Email: powersolutions@dynexsemi.com