



DIM800FSM17-A000

Single Switch IGBT Module

Preliminary Information

Replaces May 2001, version DS5461-1.0

DS5461-1.0 May 2001

FEATURES

- 10 μ s Short Circuit Withstand
- High Thermal Cycling Capability
- Non Punch Through Silicon
- Isolated MMC Base with AlN Substrates

APPLICATIONS

- Inverters
- Motor Controllers
- Traction Drives

The Powerline range of modules includes half bridge, chopper, dual and single switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The DIM800FSM17-A000 is a single switch 1700V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus full 10 μ s short circuit withstand. This module is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM800FSM17-A000

Note: When ordering, please use the whole part number.

KEY PARAMETERS

| | | |
|-----------------|--------------|--------------|
| V_{CES} | | 1700V |
| $V_{CE(sat)}$ * | (typ) | 2.7V |
| I_C | (max) | 800A |
| $I_{C(PK)}$ | (max) | 1600A |

*(measured at the power busbars and not the auxiliary terminals)

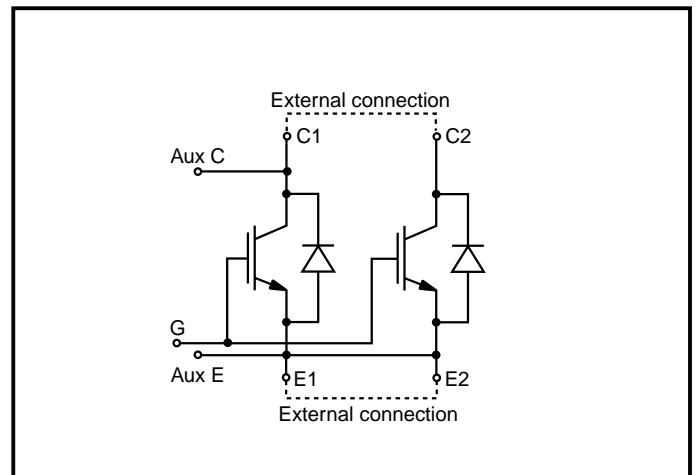


Fig. 1 Single switch circuit diagram

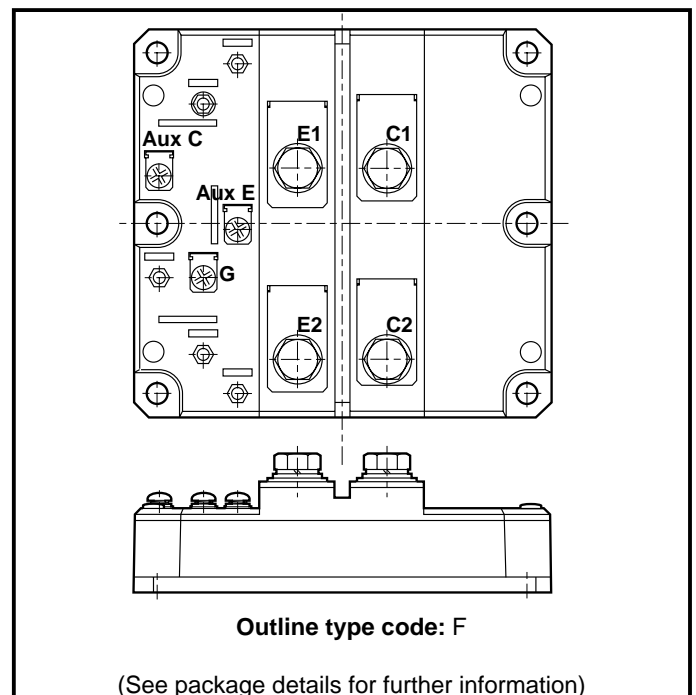


Fig. 2 Electrical connections - (not to scale)

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

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ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise

| Symbol | Parameter | Test Conditions | Max. | Units |
|-------------|-----------------------------------|------------------------------------------------------------------|----------|-----------------------|
| V_{CES} | Collector-emitter voltage | $V_{GE} = 0\text{V}$ | 1700 | V |
| V_{GES} | Gate-emitter voltage | - | ± 20 | V |
| I_C | Continuous collector current | $T_{case} = 75^{\circ}\text{C}$ | 800 | A |
| $I_{C(PK)}$ | Peak collector current | 1ms, $T_{case} = 105^{\circ}\text{C}$ | 1600 | A |
| P_{max} | Max. transistor power dissipation | $T_{case} = 25^{\circ}\text{C}$, $T_j = 150^{\circ}\text{C}$ | 6940 | W |
| I^2t | Diode I^2t value | $V_R = 0$, $t_p = 10\text{ms}$, $T_{vj} = 125^{\circ}\text{C}$ | 120 | kA^2s |
| V_{isol} | Isolation voltage - per module | Commoned terminals to base plate. AC RMS, 1 min, 50Hz | 4000 | V |
| Q_{PD} | Partial discharge - per module | IEC1287. $V_1 = 1500\text{V}$, $V_2 = 1100\text{V}$, 50Hz RMS | 10 | pC |

THERMAL AND MECHANICAL RATINGS

Internal insulation material: AlN
 Baseplate material: AlSiC
 Creepage distance: 20mm
 Clearance: 10mm
 CTI (Critical Tracking Index): 175

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|---------------|-------------------------------------------------------|-----------------------------------------------|------|------|------|-------|
| $R_{th(j-c)}$ | Thermal resistance - transistor (per arm) | Continuous dissipation - junction to case | - | - | 18 | °C/kW |
| $R_{th(j-c)}$ | Thermal resistance - diode (per arm) | Continuous dissipation - junction to case | - | - | 40 | °C/kW |
| $R_{th(c-h)}$ | Thermal resistance - case to heatsink (per module) | Mounting torque 5Nm (with mounting grease) | - | - | 8 | °C/kW |
| T_j | Junction temperature | Transistor | - | - | 150 | °C |
| | | Diode | - | - | 125 | °C |
| T_{stg} | Storage temperature range | - | -40 | - | 125 | °C |
| - | Screw torque | Mounting - M6 | - | - | 5 | Nm |
| | | Electrical connections - M4 | - | - | 2 | Nm |
| | | Electrical connections - M8 | - | - | 10 | Nm |

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ELECTRICAL CHARACTERISTICS

 $T_{\text{case}} = 25^{\circ}\text{C}$ unless stated otherwise.

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units | |
|--------------------------------|--------------------------------------|---------------------------------------------------------------------------------------------------------|-------|------|------|---------------|---|
| I_{CES} | Collector cut-off current | $V_{\text{GE}} = 0\text{V}, V_{\text{CE}} = V_{\text{CES}}$ | - | - | 1 | mA | |
| | | $V_{\text{GE}} = 0\text{V}, V_{\text{CE}} = V_{\text{CES}}, T_{\text{case}} = 125^{\circ}\text{C}$ | - | - | 25 | mA | |
| I_{GES} | Gate leakage current | $V_{\text{GE}} = \pm 20\text{V}, V_{\text{CE}} = 0\text{V}$ | - | - | 4 | μA | |
| $V_{\text{GE(TH)}}$ | Gate threshold voltage | $I_{\text{C}} = 40\text{mA}, V_{\text{GE}} = V_{\text{CE}}$ | 4.5 | 5.5 | 6.5 | V | |
| $V_{\text{CE(sat)}}^{\dagger}$ | Collector-emitter saturation voltage | $V_{\text{GE}} = 15\text{V}, I_{\text{C}} = 800\text{A}$ | - | 2.7 | 3.2 | V | |
| | | $V_{\text{GE}} = 15\text{V}, I_{\text{C}} = 800\text{A}, T_{\text{case}} = 125^{\circ}\text{C}$ | - | 3.4 | 4.0 | V | |
| I_{F} | Diode forward current | DC | - | - | 800 | A | |
| I_{FM} | Diode maximum forward current | $t_{\text{p}} = 1\text{ms}$ | - | - | 1600 | A | |
| V_{F}^{\dagger} | Diode forward voltage | $I_{\text{F}} = 800\text{A}$ | - | 2.2 | 2.5 | V | |
| | | $I_{\text{F}} = 800\text{A}, T_{\text{case}} = 125^{\circ}\text{C}$ | - | 2.3 | 2.6 | V | |
| C_{ies} | Input capacitance | $V_{\text{CE}} = 25\text{V}, V_{\text{GE}} = 0\text{V}, f = 1\text{MHz}$ | - | 60 | - | nF | |
| L_{M} | Module inductance | - | - | 20 | - | nH | |
| R_{INT} | Internal transistor resistance | - | - | 0.27 | - | m Ω | |
| SC_{Data} | Short circuit. I_{SC} | $T_{\text{j}} = 125^{\circ}\text{C}, V_{\text{CC}} = 1000\text{V},$ | I_1 | - | 3700 | - | A |
| | | $t_{\text{p}} \leq 10\mu\text{s}, V_{\text{CE(max)}} = V_{\text{CES}} - L^* \cdot di/dt$ IEC 60747-9 | I_2 | - | 3200 | - | A |

Note:
 \dagger Measured at the power busbars and not the auxiliary terminals)

 $*$ L is the circuit inductance + L_{M}

ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|--------------|-------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------|------|------|------|---------------|
| $t_{d(off)}$ | Turn-off delay time | $I_C = 800\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 900\text{V}$ $R_{G(ON)} = R_{G(OFF)} = 2.2\Omega$ $L \sim 100\text{nH}$ | - | 1250 | - | ns |
| t_f | Fall time | | - | 170 | - | ns |
| E_{OFF} | Turn-off energy loss | | - | 230 | - | mJ |
| $t_{d(on)}$ | Turn-on delay time | | - | 250 | - | ns |
| t_r | Rise time | | - | 250 | - | ns |
| E_{ON} | Turn-on energy loss | | - | 220 | - | mJ |
| Q_g | Gate charge | | - | 9.0 | - | μC |
| Q_{rr} | Diode reverse recovery charge | $I_F = 800\text{A}, V_R = 900\text{V},$ $dI_F/dt = 4000\text{A}/\mu\text{s}$ | - | 200 | - | μC |
| I_{rr} | Diode reverse current | | - | 460 | - | A |
| E_{REC} | Diode reverse recovery energy | | - | 130 | - | mJ |

$T_{case} = 125^{\circ}\text{C}$ unless stated otherwise

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|--------------|-------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------|------|------|-------|
| $t_{d(off)}$ | Turn-off delay time | $I_C = 800\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 900\text{V}$ $R_{G(ON)} = R_{G(OFF)} = 2.2\Omega$ $L \sim 100\text{nH}$ | - | 1500 | - | ns |
| t_f | Fall time | | - | 200 | - | ns |
| E_{OFF} | Turn-off energy loss | | - | 360 | - | mJ |
| $t_{d(on)}$ | Turn-on delay time | | - | 400 | - | ns |
| t_r | Rise time | | - | 250 | - | ns |
| E_{ON} | Turn-on energy loss | | - | 340 | - | mJ |
| Q_{rr} | Diode reverse recovery charge | | $I_F = 800\text{A}, V_R = 900\text{V},$ $dI_F/dt = 4000\text{A}/\mu\text{s}$ | - | 330 | - |
| I_{rr} | Diode reverse current | - | | 530 | - | A |
| E_{REC} | Diode reverse recovery energy | - | | 200 | - | mJ |

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TYPICAL CHARACTERISTICS

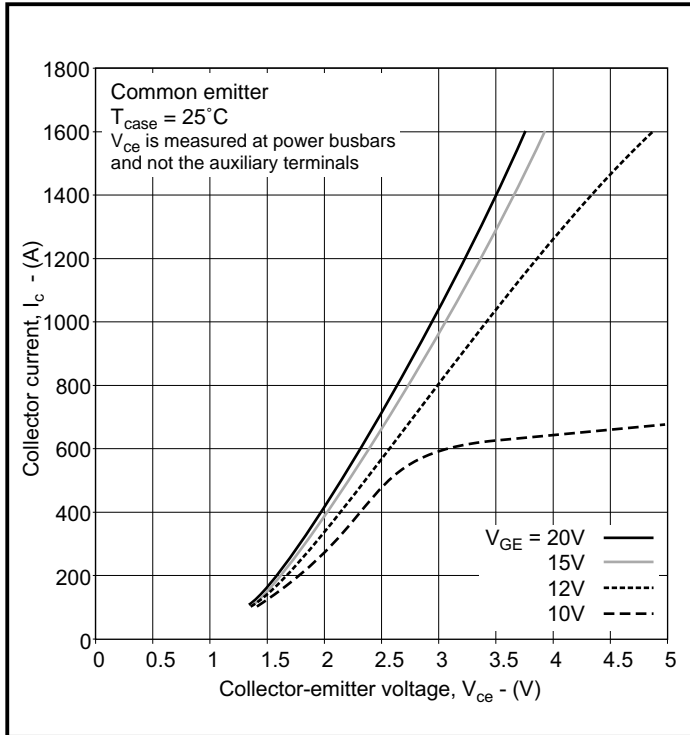


Fig. 3 Typical output characteristics

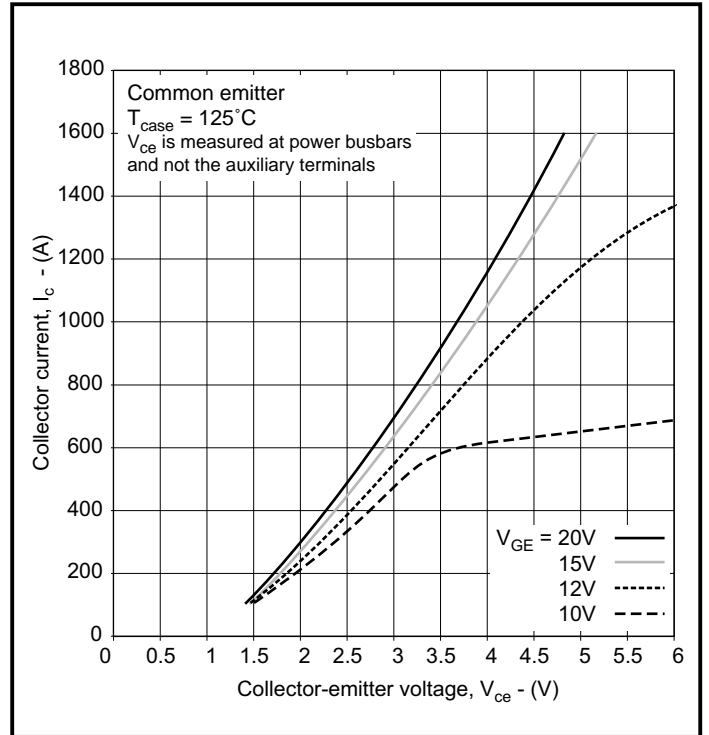


Fig. 4 Typical output characteristics

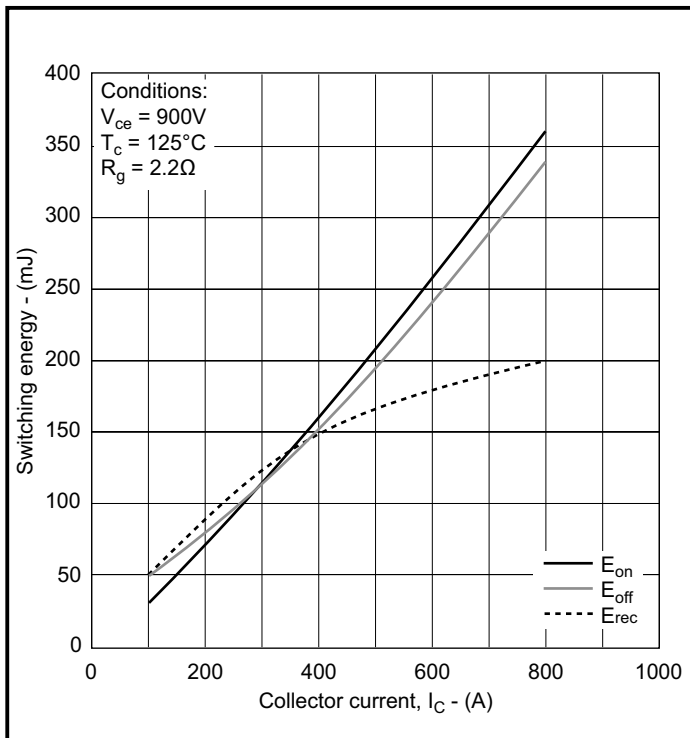


Fig. 5 Typical switching energy vs collector current

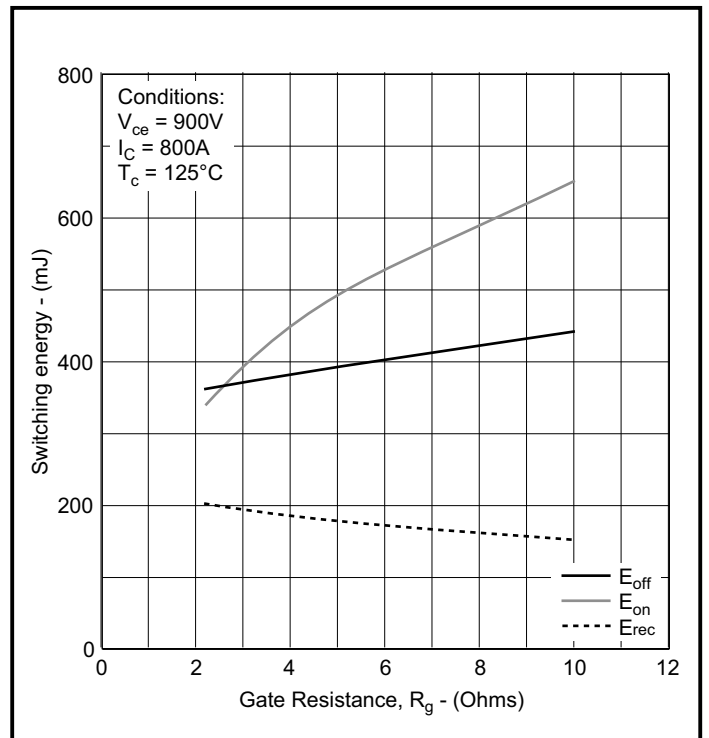


Fig. 6 Typical switching energy vs gate resistance

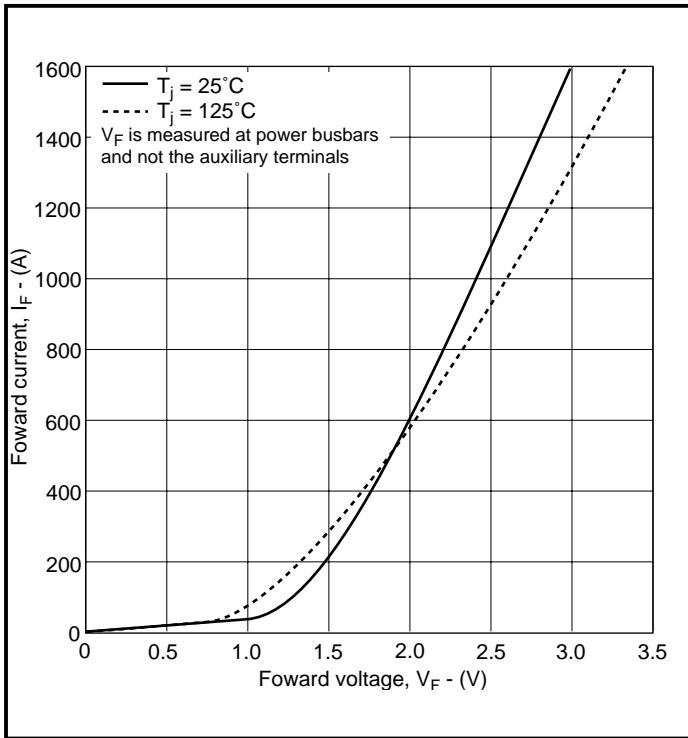


Fig. 7 Diode typical forward characteristics

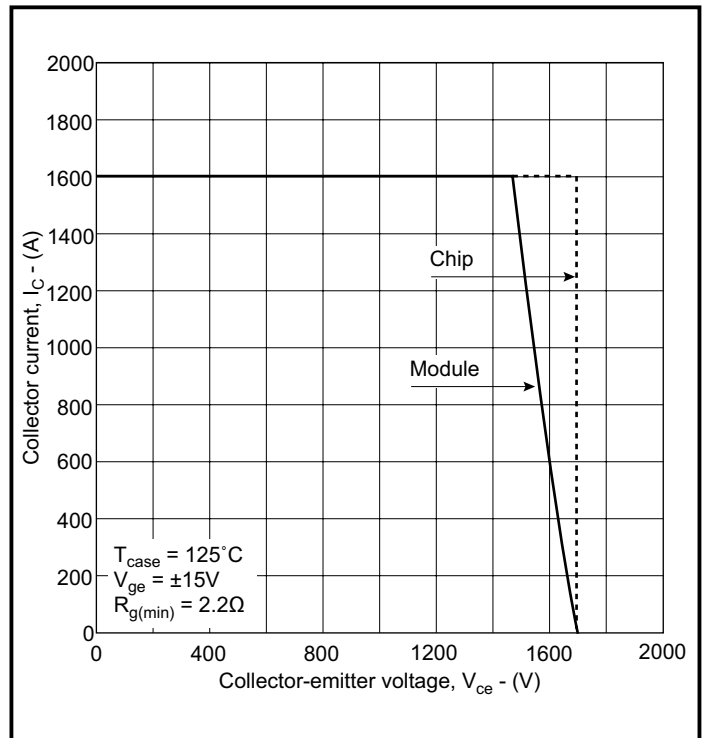


Fig. 8 Reverse bias safe operating area

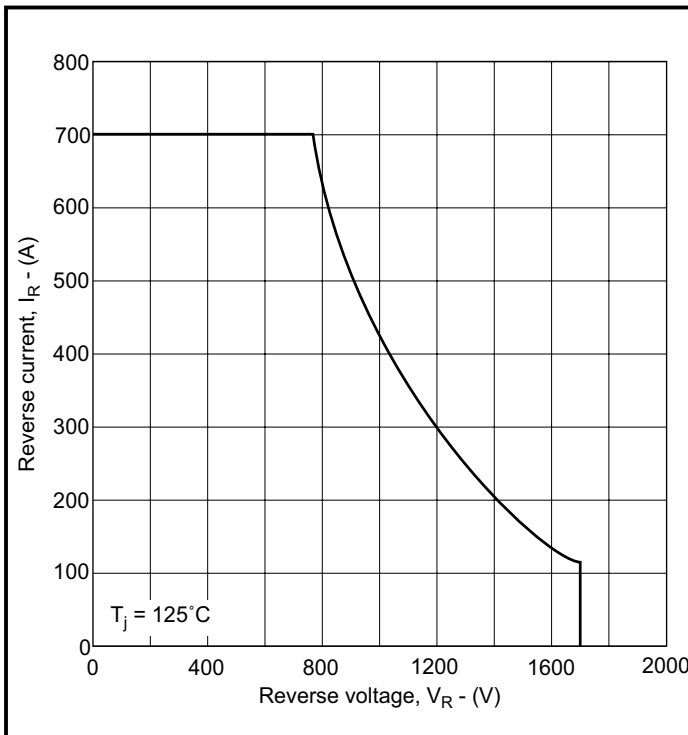


Fig. 9 Diode reverse bias safe operating area

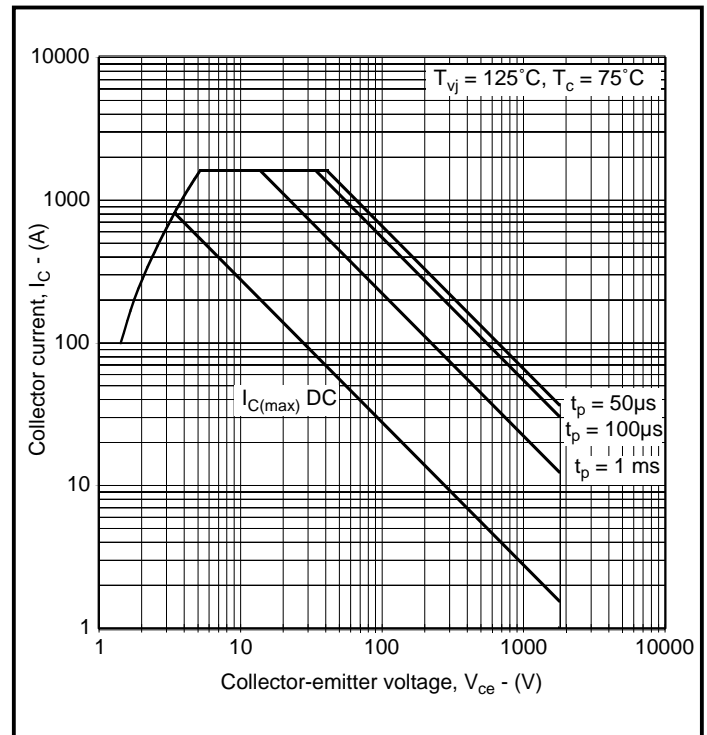


Fig. 10 Forward bias safe operating area

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

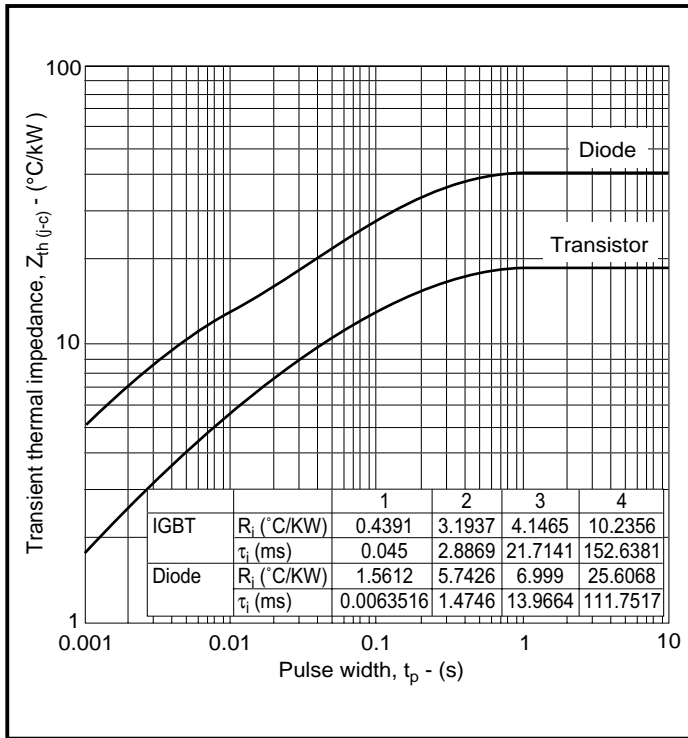


Fig. 11 Transient thermal impedance

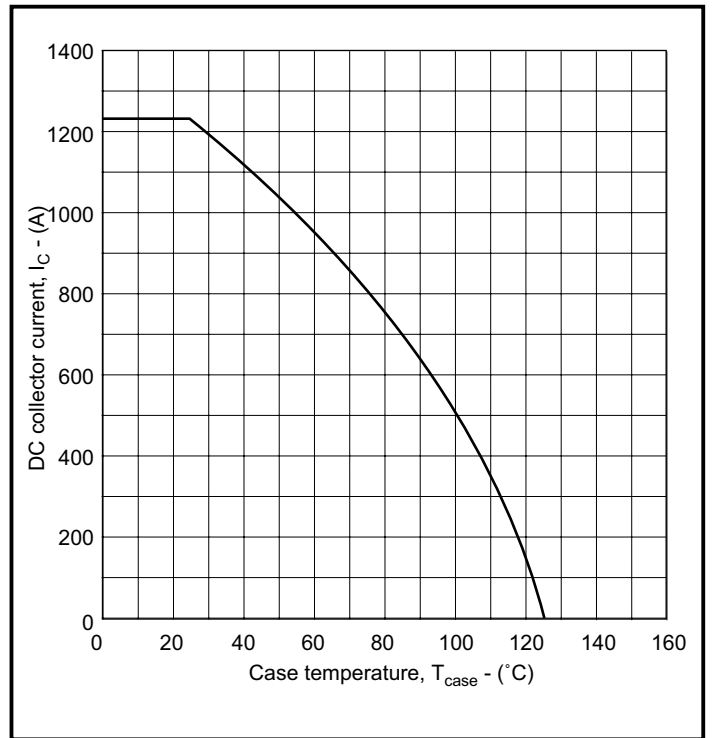
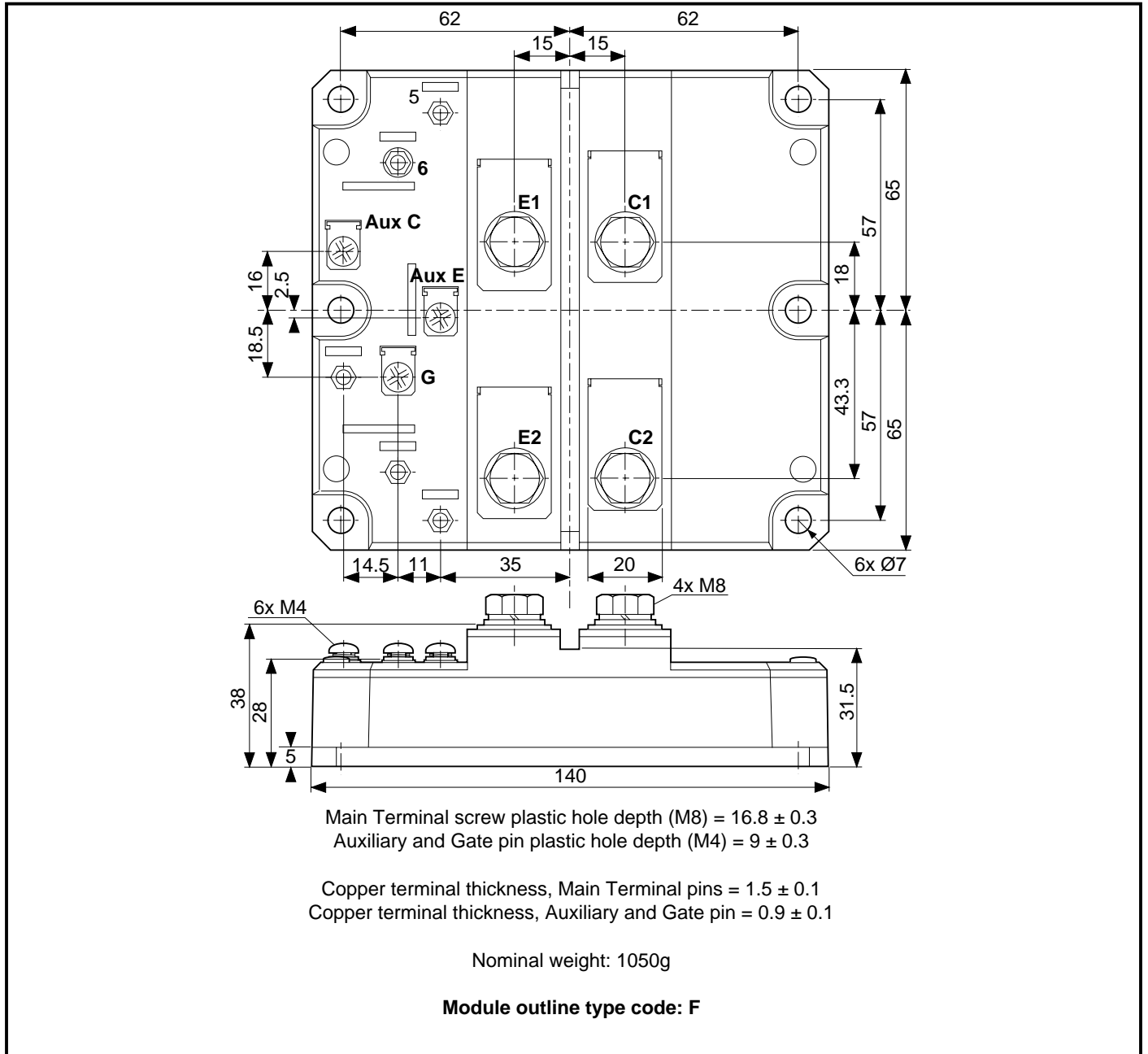


Fig. 12 DC current rating vs case temperature

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



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POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or customer service office.



<http://www.dynexsemi.com>

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Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand.

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