

High Precision CC/CV Primary-Side Converter

Description

The DK935 is a high performance CC/CV primary-side AC-DC converter chip. With 850V high voltage switch power transistor integrated, it can detect primary side peak current. It operates in primary-side sensing and regulation, so opto-coupler and TL431 could be eliminated. It also integrated primary-side feedback constant current, constant voltage, self-power supply circuit and output cable compensation circuit. It is designed with highly integrated CMOS circuit, with few external components and simple transformer design. The isolated output circuit transformer needs only two windings.

Main Features

- Global voltage input 85V-265V
- Built-in 850V high voltage power BJT
- Internal integrated high voltage constant current start-up circuit, no need of additional start-up resistor
- Patented self-power supply design without auxiliary winding
- Support applications of two-winding and three-winding

• Output line loss compensation technology can effectively compensate the loss voltage drop of the output current on the output line

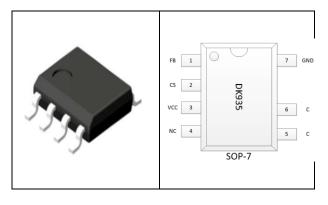
Applications

- AC-DC power adapters for shaver, mobile phone, PDA, MP3 and other portable devices
- Standby or auxiliary power supply for household appliances and personal computers
- LED power supply
- Replace linear power supply or RCC switching mode power supply

• Built-in PWM oscillating circuit and frequency jittering function for improved EMI performance

- RCD absorption can be omitted on the primary side
- ±5% CV accuracy, ±8% CC accuracy
- Over temperature, over voltage, over current, short circuit Protection
- 4KV ESD
- Package: SOP-7

PINS Array



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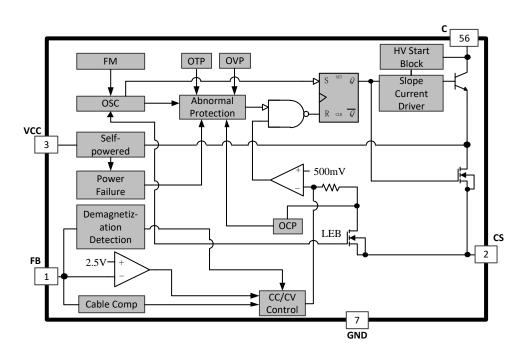
Ordering Information

Product Model	Package Type	Package Information	Silk Screen
DK935	SOP-7	1000 pieces per plata	DK935
		4000 pieces per plate	ХХХХ

PIN Function

Pin No.	Pin Name	Function
1	FB	Primary feedback pin
2	CS	Max peak current setting pin, connected with resistance to ground externally
3	VCC	Power supply pin, connected with a capacitor of 10uF-47uF to ground externally
4	NC	Floating
5,6	С	Collector of Power transistor
7	GND	Ground pin

Function Structure Diagram



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Typical Power

Product Model	Input Voltage	Typical Power
DK935	85-265VAC	5W

Remark: Test typical power in closed environment @45 $^\circ C$ ambient temperature.

Absolute Maximum Rating

Parameter	Symbol	Min	Тур	Max	Unit
Power supply voltage VCC	Us	-0.3		7	V
Power supply current VCC	I _S		100		mA
Pin Voltage	U _{PV}	-0.3		VCC+0.3	V
Breakdown Voltage	U _{PP}	-0.3		850	V
Peak Current	I _{PEAK}			400	mA
Total Dissipation Power	P _{TOT}		450		mW
Operating Temperature	T _R	-25		135	°C
Storage Temperature	T _{STG}	-55		150	°C
Soldering Temperature	Tw		280/55		°C
ESD	ESD		4000		V

Electrical Characteristics (T_A=25 °C, Special circumstances shall be specified separately)

Parameter	Test Condition	Min	Тур	Max	Unit
VCC operating voltage	AC input 85V-265V	4.5	5.0	5.5	V
VCC start-up threshold voltage	AC input 85V-265V	4.5	5.0	5.5	V
VCC restart voltage	AC input 85V-265V	3.0	3.3	3.6	V
VCC protect voltage	AC input 85V-265V	5.8	6.2	6.6	V
VCC operating current	VDD=5V, FB=2V			40	mA
HV Start-up current	AC input 265V			0.5	mA
Start-up time	AC input 85V			500	ms
BJT Voc breakdown voltage	I _{oc} =1mA	850			V
IS max turn-on voltage	VDD=5V	450	500	550	mV
CV reference voltage	VDD=5V	2.45	2.5	2.55	V
Max BJT current	VDD=5V			450	mA
Operating frequency	VDD=5V	0.5		80	KHz
Short circuit protection threshold	VDD=5V, Measure FB voltage		1.2		V
Open circuit protection voltage	VDD=5V, Measure FB voltage		3.7		V
Temperature protection	VDD=5V	130	135	140	°C
Min turn-on time	VCC=5V		500		ns

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Operation Principle

1. Start up

With HV start-up current source integrated in the chip, external VCC capacitor would be charged when power on. When VCC voltage reaches 5V, the start-up procedure ends and the chip enters the soft start stage.

2. CC mode

The chip sets primary maximum peak current (Ip = 500 mV/Ris). When the load exceeds maximum output power, the chip operates in CC mode.

Output current: Io = $\frac{1}{4} * Ip * \frac{Np}{Ns}$

3. CV mode

During the fly-back stage, the chip detects output voltage (Vo) indirectly by detecting FB voltage. FB voltage and internal 2.5V reference are used for error amplification. The output of the error amplifier controls the PWM switching frequency to stabilize the output voltage (Vo) according to the load condition. The output voltage (Vo) of the three-winding application is expressed as follows:

Vo = $2.5V * \frac{Ns}{Na} * \left(1 + \frac{Rfb2}{Rfb1}\right) - Vd$. Vd is the secondary rectifier diode voltage.

The output voltage (Vo) of the two-winding application is expressed as follows:

 $Vo = 2.5V * \frac{Ns}{Np} * \left(1 + \frac{Rfb2}{Rfb1}\right) - Vd.$ Vd is the secondary rectifier diode voltage, other parameters can

refer to the typical application.

4. Cable compensation

The cable compensation technology can compensate the loss voltage drop of the output line. When the load decreases from full load to no load, the line loss compensation voltage also decreases linearly. The compensation voltage can be set through the FB divider resistor, the specific formula is: Three-winding application: the line compensation voltage at the output is:

 $V_{COMP} = I_{COMP} * RFB2 * \frac{N_S}{N_A}$, Line loss compensation current ICOMP I_{COMP(max)}=16uA;

Two-winding application: the line compensation voltage at the output is:

$$V_{COMP} = I_{COMP} * RFB2 * \frac{N_S}{N_P} .$$

5. Protection

DK935 has various self-protection functions, such as output open-circuit protection, output short-circuit protection, VCC under-voltage protection, VCC over-voltage protection, over-temperature protection, etc.

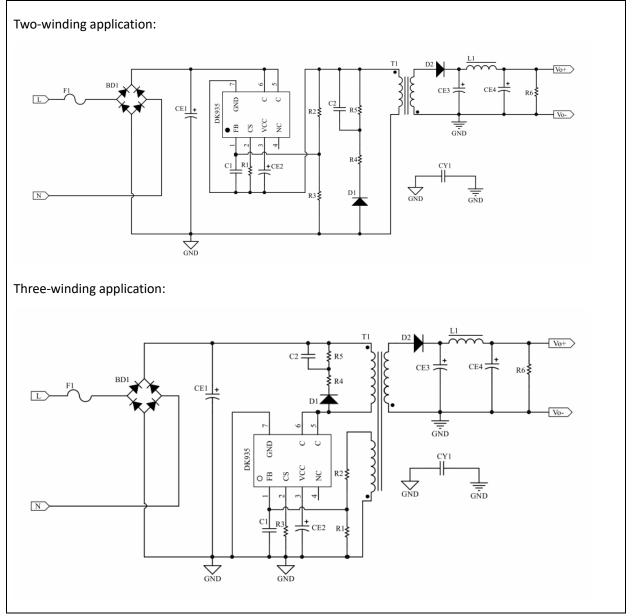
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DK935

Typical Application Schematic



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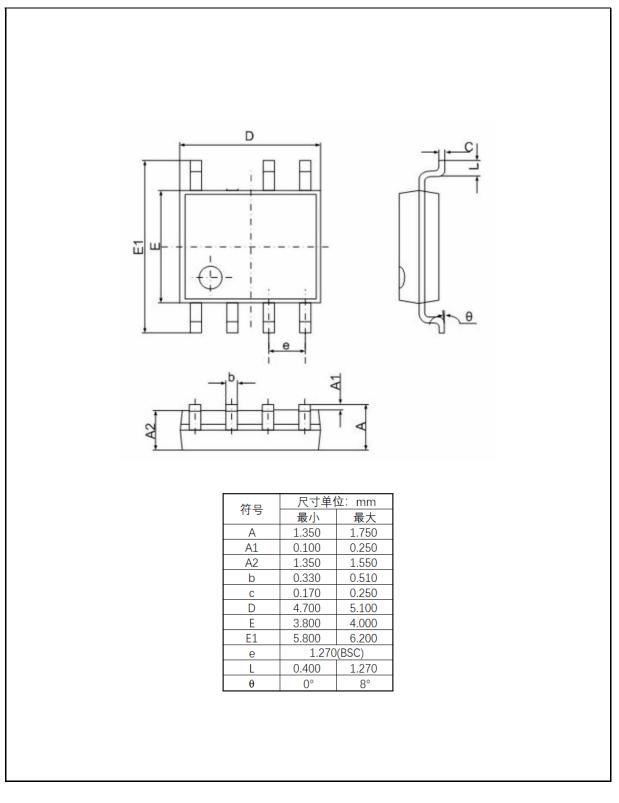
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Package Information and Dimensions

1. SOP-7 Package Information



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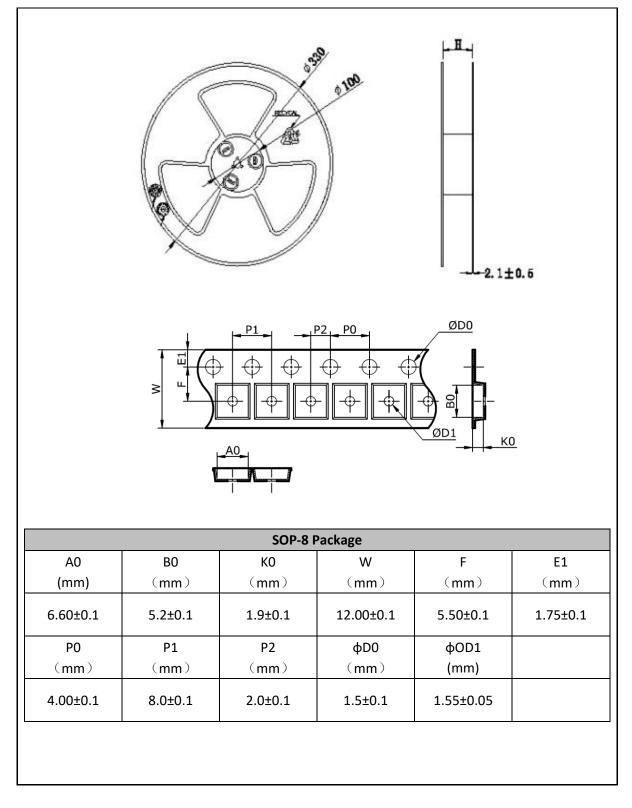
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2. Tape and Reel Information



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