

DM54155/DM74155 Dual 2-Line to 4-Line Decoders/Demultiplexers

General Description

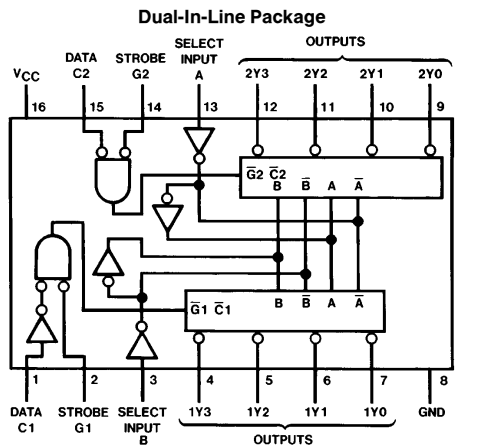
These TTL circuits feature dual 1-line-to-4-line demultiplexers with individual strobes and common binary-address inputs in a single 16-pin package. When both sections are enabled by the strobes, the common address inputs sequentially select and route associated input data to the appropriate output of each section. The individual strobes permit activating or inhibiting each of the 4-bit sections as desired. Data applied to input C1 is inverted at its outputs and data applied at C2 is true through its outputs. The inverter following the C1 data input permits use as a 3-to-8-line decoder, or 1-to-8-line demultiplexer, without external gating.

Input clamping diodes are provided on these circuits to minimize transmission-line effects and simplify system design.

Features

- Applications:
 - Dual 2-to-4-line decoder
 - Dual 1-to-4-line demultiplexer
 - 3-to-8-line decoder
 - 1-to-8-line demultiplexer
- Individual strobes simplify cascading for decoding or demultiplexing larger words
- Input clamping diodes simplify system design

Connection Diagram and Function Tables



Order Number DM54155J, DM54155W or DM74155N
See NS Package Number J16A, N16A or W16A

2-Line-to-4-Line Decoder or 1-Line-to-4-Line Demultiplexer			
Inputs			Outputs
Select	Strobe	Data	
B A	G1	C1	1Y0 1Y1 1Y2 1Y3
X X	H	X	H H H H
L L	L	H	L H H H
L H	L	H	H L H H
H L	L	H	H H L H
H H	L	H	H H H L
X X	X	L	H H H H

2-Line-to-4-Line Decoder or 1-Line-to-4-Line Demultiplexer			
Inputs			Outputs
Select	Strobe	Data	
B A	G2	C2	2Y0 2Y1 2Y2 2Y3
X X	H	X	H H H H
L L	L	L	L H H H
L H	L	L	H L H H
H L	L	L	H H L H
H H	L	L	H H H L
X X	X	H	H H H H

3-Line-to-8-Line Decoder or 1-Line-to-8-Line Demultiplexer			
Inputs			Outputs
Select	Strobe	Data	
C† B A	G‡	C†	(0) (1) (2) (3) (4) (5) (6) (7)
C† B A	G‡	C†	2Y0 2Y1 2Y2 2Y3 1Y0 1Y1 1Y2 1Y3
X X X	H	H	H H H H H H H H
L L L	L	L	L H H H H H H H
L L H	L	H	H L H H H H H H
L H L	L	H	H H L H H H H H
L H H	L	H	H H H L H H H H
H L L	L	H	H H H H L H H H
H L H	L	H	H H H H H L H H
H H L	L	H	H H H H H H L H
H H H	L	H	H H H H H H H L

†C = inputs C1 and C2 connected together
‡G = inputs G1 and G2 connected together
H = high level, L = low level, X = don't care

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
DM54	−55°C to +125°C
DM74	0°C to +70°C
Storage Temperature Range	−65°C to +150°C

Note: The “Absolute Maximum Ratings” are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the “Electrical Characteristics” table are not guaranteed at the absolute maximum ratings. The “Recommended Operating Conditions” table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54155			DM74155			Units
		Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.8			0.8	V
I _{OH}	High Level Output Current			−0.8			−0.8	mA
I _{OL}	Low Level Output Current			16			16	mA
T _A	Free Air Operating Temperature	−55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = −12 mA			−1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max V _{IL} = Max, V _{IH} = Min	2.4			V
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max V _{IH} = Min, V _{IL} = Max			0.4	V
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 5.5V			1	mA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.4V			40	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V			−1.6	mA
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)	DM54 −20 DM74 −18		−55 −55	mA
I _{CC}	Supply Current	V _{CC} = Max (Note 3)	DM54 DM74	25 25	35 40	mA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

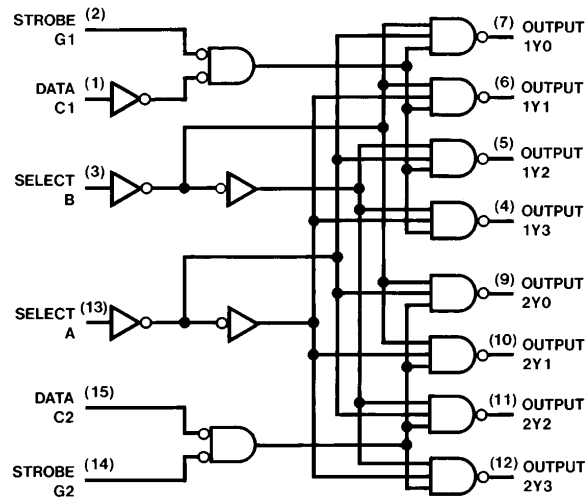
Note 2: Not more than one output should be shorted at a time.

Note 3: I_{CC} is measured with the outputs open, A, B, and C1 inputs at 4.5V, and C2, G1, and G2 inputs grounded.

Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^\circ C$ (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter	From (Input) To (Output)	$R_L = 400\Omega, C_L = 15\text{ pF}$		Units
			Min	Max	
t_{PLH}	Propagation Delay Time Low to High Level Output	A, B, C2, G1 or G2 to Y		20	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	A, B, C2, G1 or G2 to Y		27	ns
t_{PLH}	Propagation Delay Time Low to High Level Output	A or B to Y		32	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	A or B to Y		32	ns
t_{PLH}	Propagation Delay Time Low to High Level Output	C1 to Y		24	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	C1 to Y		27	ns

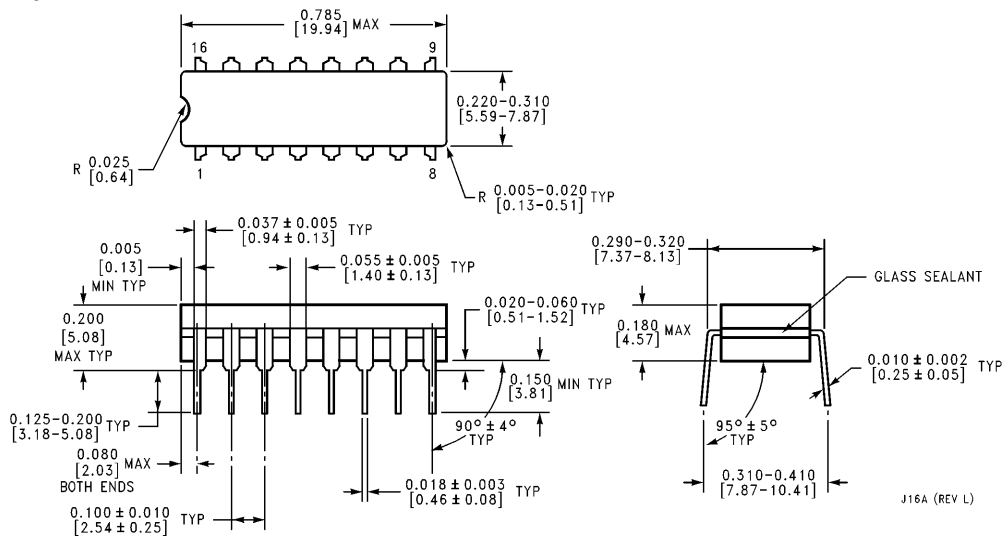
Logic Diagram



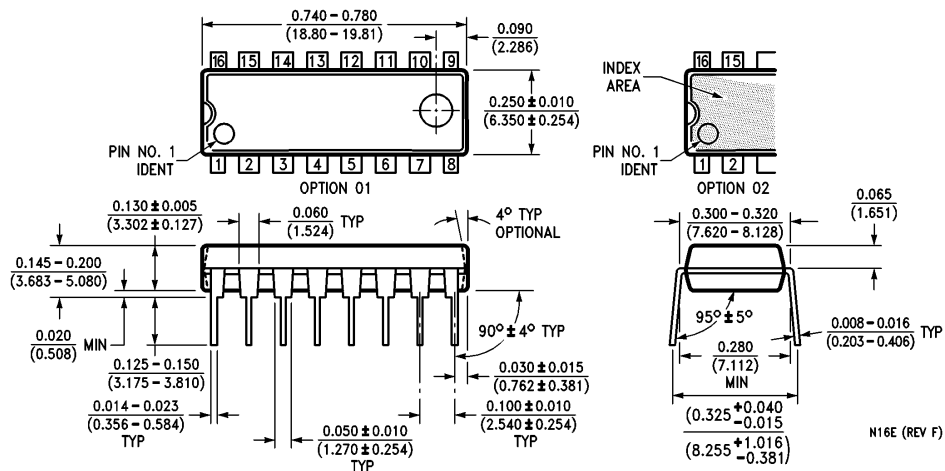
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Physical Dimensions inches (millimeters)

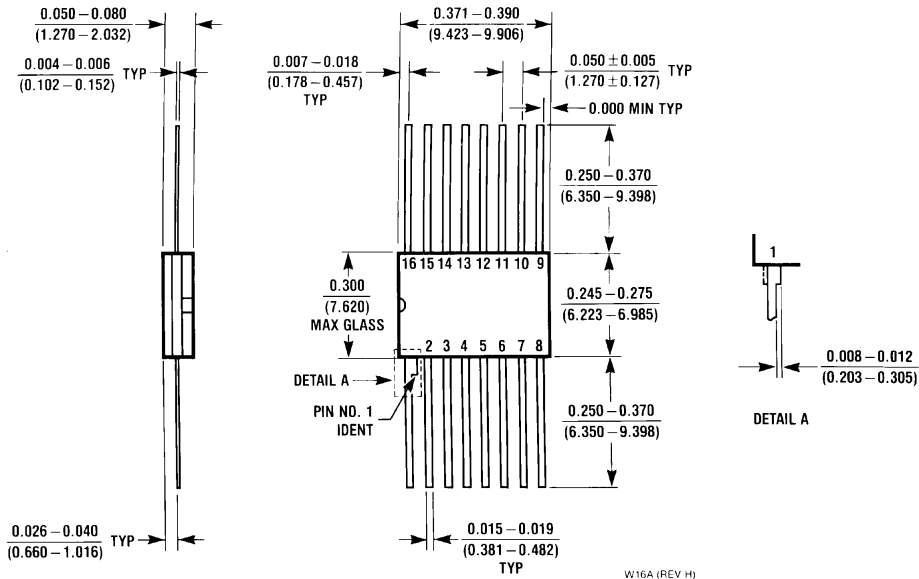


16-Lead Ceramic Dual-In-Line Package (J)
Order Number DM54155J
NS Package Number J16A



16-Lead Molded Dual-In-Line Package (N)
Order Number DM74155N
NS Package Number N16E

Physical Dimensions inches (millimeters) (Continued)



16-Lead Ceramic Flat Package (W)
Order Number DM54155W
NS Package Number W16A

W16A (REV H)

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National Semiconductor Corporation
1111 West Bardin Road
Arlington, TX 76017
Tel: 1(800) 272-9959
Fax: 1(800) 737-7018

National Semiconductor Europe
Fax: (+49) 0-180-530 85 86
Email: cnjwge@tevm2.nsc.com
Deutsch Tel: (+49) 0-180-530 85 85
English Tel: (+49) 0-180-532 78 32
Français Tel: (+49) 0-180-532 93 58
Italiano Tel: (+49) 0-180-534 16 80

National Semiconductor Hong Kong Ltd.
19th Floor, Straight Block,
Ocean Centre, 5 Canton Rd.
Tsimshatsui, Kowloon
Hong Kong
Tel: (852) 2737-1600
Fax: (852) 2736-9960

National Semiconductor Japan Ltd.
Tel: 81-043-299-2309
Fax: 81-043-299-2408

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