

|   |                  |                |             |                         |
|---|------------------|----------------|-------------|-------------------------|
| <div style="border: 1px solid black; border-radius: 10px; padding: 10px; text-align: center;"> <h1>LCD Module Specification</h1> </div> |                  | First Edition  | Approved by | Production Div.         |
|   |                  | June 18, 1998  | Checked by  | Quality Assurance Div.  |
|   |                  | Final Revision | Checked by  | Design Engineering Div. |
|   |                  | *****          | Prepared by | Production Div.         |
| Type No.  | DMF-50316NF-FW-1 |                |             |                         |

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## 1. General Specifications

|                    |   |  |
|--------------------|---|--|
| Operating Temp.    | : | min. 0°C ~ max. 50°C   |
| Storage Temp.      | : | min. -20°C ~ max. 60°C   |
| Dot Pixels         | : | 240 (W) × 64 (H) dots  |
| Dot Size           | : | 0.50 (W) × 0.50 (H) mm   |
| Dot Pitch          | : | 0.53 (W) × 0.53 (H) mm   |
| Viewing Area       | : | 132.0 (W) × 39.0 (H) mm  |
| Outline Dimensions | : | 180.0 (W) × 70.0 (H) × 14.0 * max. (D) mm<br>* Without CFL Cable                       |
| Weight             | : | 180g max.  |
| LCD Type           | : | NTD-12222<br>( F-STN / Black & White -mode / Transmissive )                            |
| Viewing Angle      | : | 6:00   |
| Control LSI        | : | T6963C-0101 (Produced by TOSHIBA)  |
| Data Transfer      | : | 8-bit data transfer  |
| Backlight          | : | Cold Cathode Fluorescent Lamp (CFL) × 1  |
| Additional Spec.   | : | (1) Nega/Posi Reverse Circuit<br>(2) Holder and PWB ground are connected by soldering. |
| Drawings           | : | Dimensional Outline    UE-35433  |

## 2. Electrical Specifications

### 2.1. Absolute Maximum Ratings

$V_{SS}=0V$

| Parameter                     | Symbol          | Conditions | Min. | Max.         | Units |
|-------------------------------|-----------------|------------|------|--------------|-------|
| Supply Voltage<br>(Logic)     | $V_{CC}-V_{SS}$ | —          | -0.3 | 7.0          | V     |
| Supply Voltage<br>(LCD Drive) | $V_{CC}-V_{EE}$ | —          | 0    | 28.0         | V     |
| Input Voltage                 | $V_I$           | —          | -0.3 | $V_{CC}+0.3$ | V     |

### 2.2. DC Characteristics

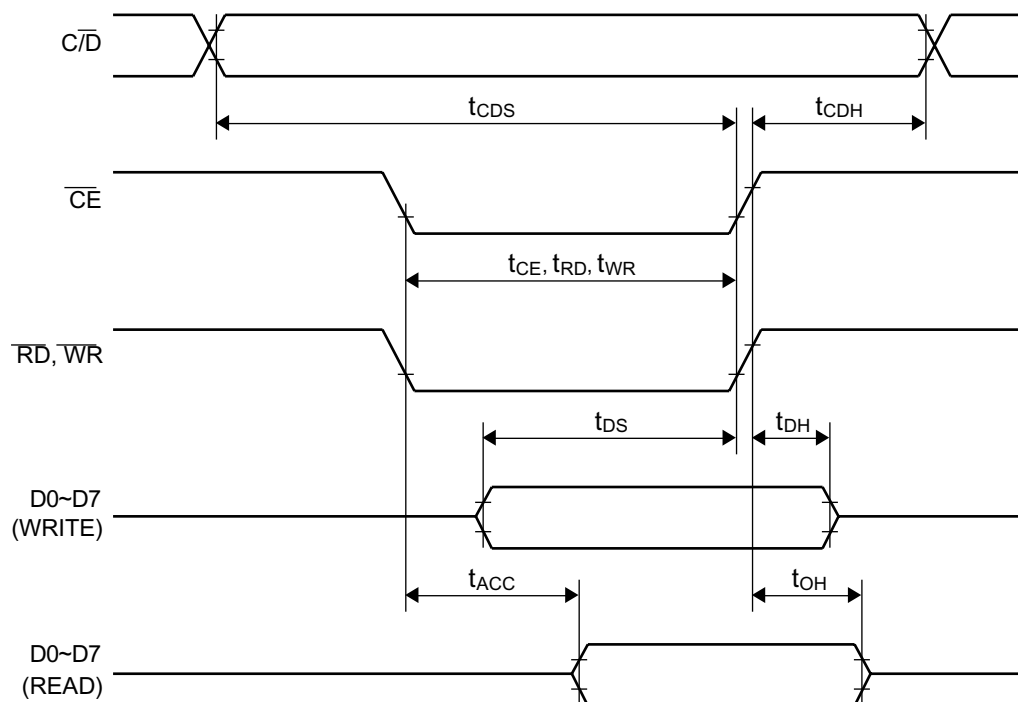
$T_a=25^{\circ}C$ ,  $V_{SS}=0V$

| Parameter                     | Symbol          | Conditions            | Min.         | Typ. | Max.     | Units |
|-------------------------------|-----------------|-----------------------|--------------|------|----------|-------|
| Supply Voltage<br>(Logic)     | $V_{CC}-V_{SS}$ | —                     | 4.5          | —    | 5.5      | V     |
| Supply Voltage<br>(LCD Drive) | $V_{CC}-V_{EE}$ | Shown in 3.1          |              |      |          | V     |
| High Level<br>Input Voltage   | $V_{IH}$        | $V_{CC}=5.0V\pm 10\%$ | $V_{CC}-2.2$ | —    | $V_{CC}$ | V     |
| Low Level<br>Input Voltage    | $V_{IL}$        | $V_{CC}=5.0V\pm 10\%$ | 0            | —    | 0.8      | V     |
| High Level<br>Output Voltage  | $V_{OH}$        | $I_{OH}=-0.75mA$      | $V_{CC}-0.3$ | —    | $V_{CC}$ | V     |
| Low Level<br>Output Voltage   | $V_{OL}$        | $I_{OL}=0.75mA$       | 0            | —    | 0.3      | V     |
| Supply Current                | $I_{CC}$        | $V_{CC}-V_{SS}=5.0V$  | —            | —    | 20.0     | mA    |
|                               | $I_{EE}$        | $V_{CC}-V_{EE}=13.3V$ | —            | —    | 10.0     | mA    |

## 2.3.AC Characteristics

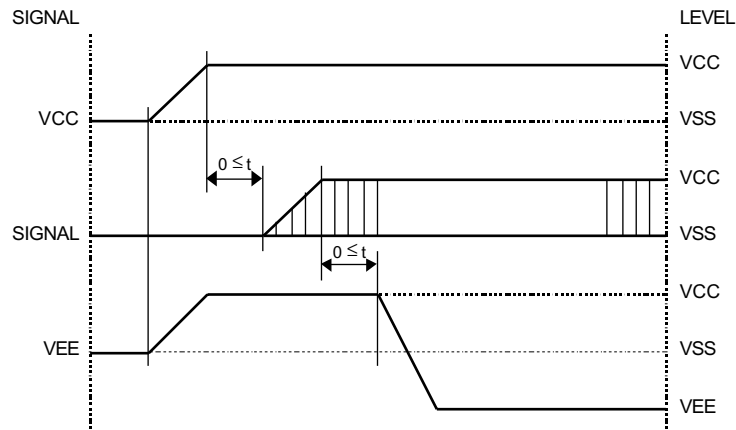
$V_{CC}=5.0V\pm10\%$

| Parameter   | Symbol                         | Min. | Max. | Units |
|---|--------------------------------|------|------|-------|
| $\overline{C/D}$ Setup Time                                     | $t_{CDS}$                      | 100  | —    | ns    |
| $\overline{C/D}$ Hold Time                                      | $t_{CDH}$                      | 10   | —    | ns    |
| $\overline{CE}$ , $\overline{RD}$ , $\overline{WR}$ Pulse Width | $t_{CE}$ , $t_{RD}$ , $t_{WR}$ | 80   | —    | ns    |
| Data Setup Time   | $t_{DS}$                       | 80   | —    | ns    |
| Data Hold Time  | $t_{DH}$                       | 40   | —    | ns    |
| Access Time   | $t_{ACC}$                      | —    | 150  | ns    |
| Output Hold Time  | $t_{OH}$                       | 10   | 50   | ns    |

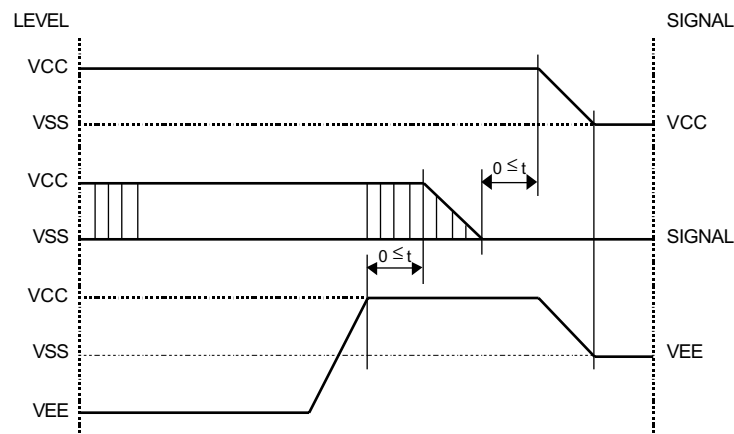


## 2.4.Power Supply ON/OFF Sequence

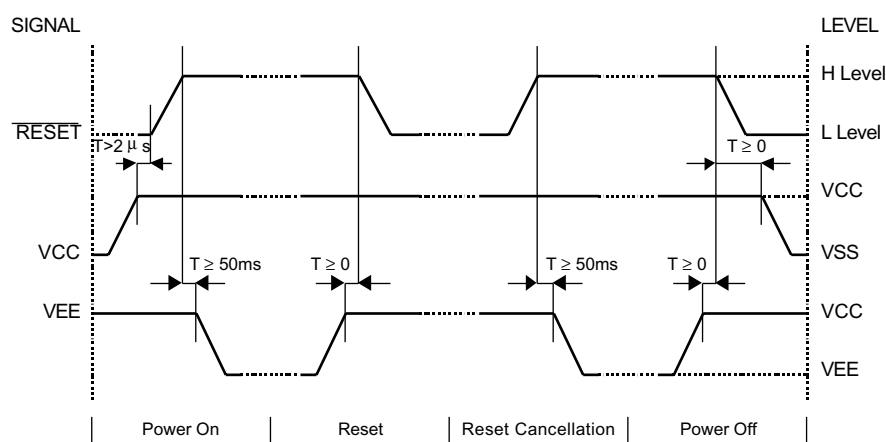
### 2.4.1.ON Sequence



### 2.4.2.OFF Sequence



### 2.4.3.Reset Sequence



Please maintain the above sequence when turning on and off the power supply of the module.  
If VEE is supplied to the module while internal alternate signal for LCD driving (M) is unstable or RESET is active, DC component will be supplied to the LCD panel. This may cause damage to the LCD module.

## 2.5. Lighting Specifications

Ta=25°C

| Parameter         | Symbol          | Conditions             | Min. | Typ.  | Max. | Units             | Notes |
|-------------------|-----------------|------------------------|------|-------|------|-------------------|-------|
| Lamp Voltage      | V <sub>L</sub>  | —                      | —    | 310   | —    | V <sub>rms</sub>  | 1     |
| Lamp Current      | I <sub>L</sub>  | —                      | 4.0  | 5.0   | 6.0  | mA <sub>rms</sub> | 2     |
| Starting Voltage  | V <sub>S</sub>  | —                      | —    | —     | 800  | V <sub>rms</sub>  | 3     |
| Surface Luminance | L               | I <sub>L</sub> = 5.0mA | 70   | 130   | —    | cd/m <sup>2</sup> | 4     |
| Average Life      | T <sub>AL</sub> | I <sub>L</sub> = 5.0mA | 8000 | 10000 | —    | hrs               | 5     |

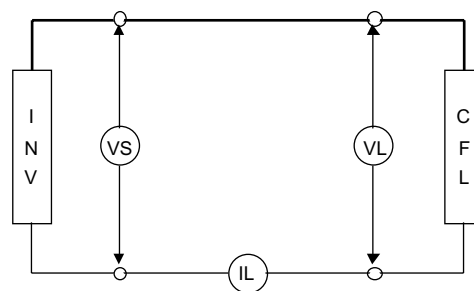
Note 1 : The voltage ( r.m.s. ) to maintain the electric discharge of the lamp. It is measured after lighting for 3 minutes .

Note 2 : The current ( r.m.s. ) to flow through the lamp with the electric discharge. It is measured after lighting for 3 minutes.

Note 3 : The voltage at starting the electric discharge when the voltage is increased gradually from 0V.

Note 4 : Surface Luminance is specified by the initial data of luminance measured at the center of display surface after 20 minutes power on. ( All ON pattern )

Note 5 : CFL life is defined as the time for which the initial luminance is attenuated by 50% of the luminance value. Average Life represents the time elapsed at the point of time when the residual ratio becomes below 50% when plural lamps are lighted in comparison with the definition of life mentioned above.



CFL Testing Circuit

### 3. Optical Specifications

#### 3.1. LCD Driving Voltage

| Parameter                                    | Symbol          | Conditions               | Min. | Typ. | Max. | Units |
|--|-----------------|--------------------------|------|------|------|-------|
| Recommended<br>LCD Driving Voltage<br>Note 1 | $V_{CC}-V_{EE}$ | $T_a=0^{\circ}\text{C}$  | —    | —    | 16.1 | V     |
|  |                 | $T_a=25^{\circ}\text{C}$ | 12.4 | 13.3 | 14.2 | V     |
|  |                 | $T_a=50^{\circ}\text{C}$ | 11.3 | —    | —    | V     |

Note 1 : Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

#### 3.2. Optical Characteristics

$T_a=25^{\circ}\text{C}$ , 1/64 Duty, 1/9 Bias,  $V_D=13.3\text{V}$  (Note 4),  $\theta=0^{\circ}$ ,  $\phi=-^{\circ}$

| Parameter             | Symbol       | Conditions                            | Min. | Typ. | Max. | Units |
|-----------------------|--------------|---------------------------------------|------|------|------|-------|
| Contrast Ratio Note 1 | CR           | $\theta=0^{\circ}$ , $\phi=-^{\circ}$ | —    | 50   | —    |       |
| Viewing Angle         |              | Shown in 3.3                          |      |      |      |       |
| Response Time         | Rise Note 2  | $T_{ON}$                              | —    | 220  | 330  | ms    |
|                       | Decay Note 3 | $T_{OFF}$                             | —    | 80   | 160  | ms    |

Note 1 : Contrast ratio is defined as follows.

$$CR = L_{OFF} / L_{ON}$$

$L_{ON}$  : Luminance of the ON segments

$L_{OFF}$  : Luminance of the OFF segments

Note 2 : The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.

Note 3 : The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.

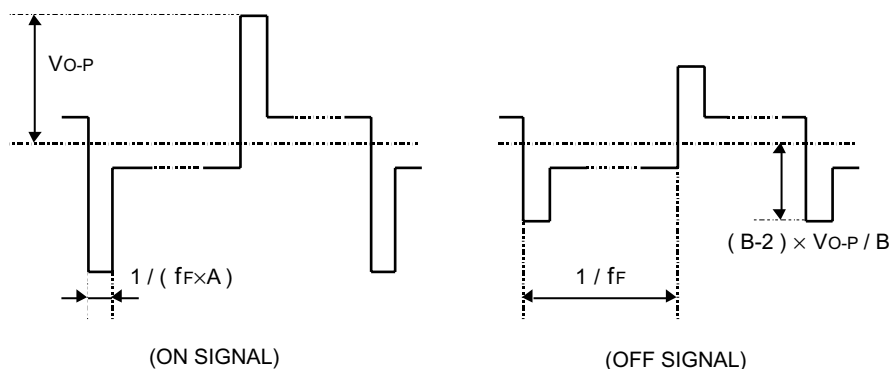
Note 4 : Definition of Driving Voltage  $V_D$

Assuming that the typical driving waveforms shown below are applied to the LCD Panel at 1/A Duty - 1/B Bias ( A : Duty Number, B : Bias Number ). Driving voltage  $V_D$  is defined as follows.

$$V_D = (V_{th1} + V_{th2}) / 2$$

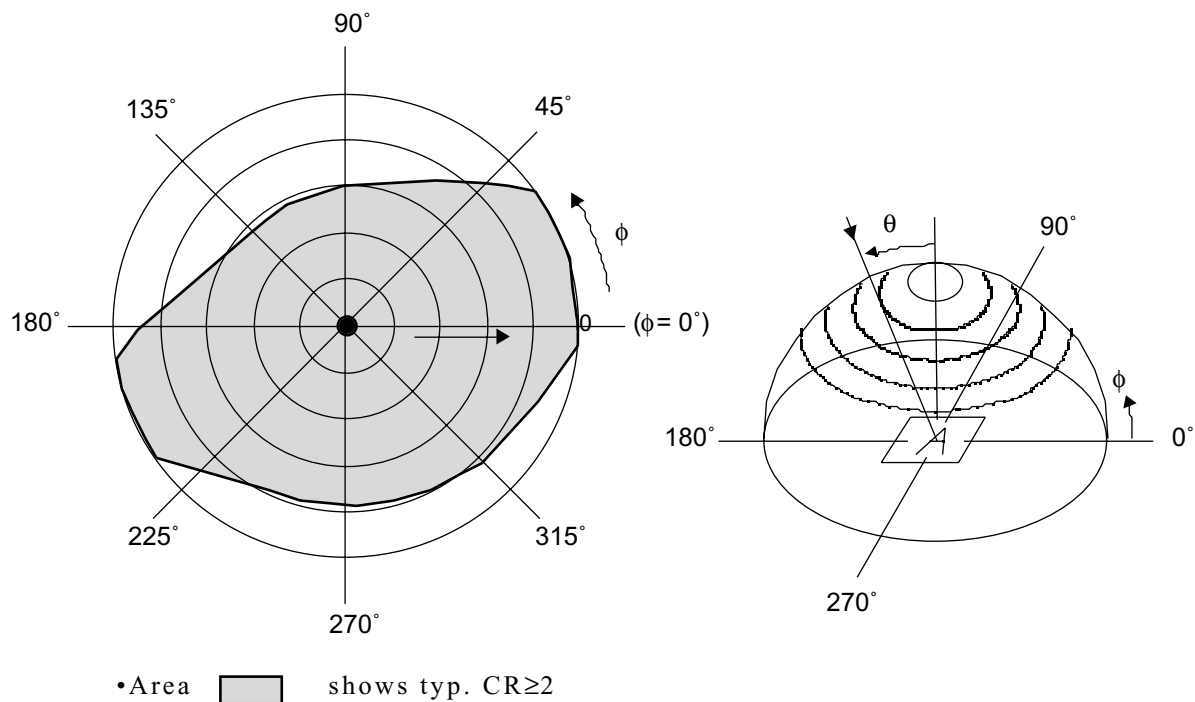
$V_{th1}$  : The voltage  $V_{O-P}$  that should provide 50% of the saturation level in the luminance at the segment which the ON signal is applied to.

$V_{th2}$  : The voltage  $V_{O-P}$  that should provide 50% of the saturation level in the luminance at the segment which the OFF signal is applied to.

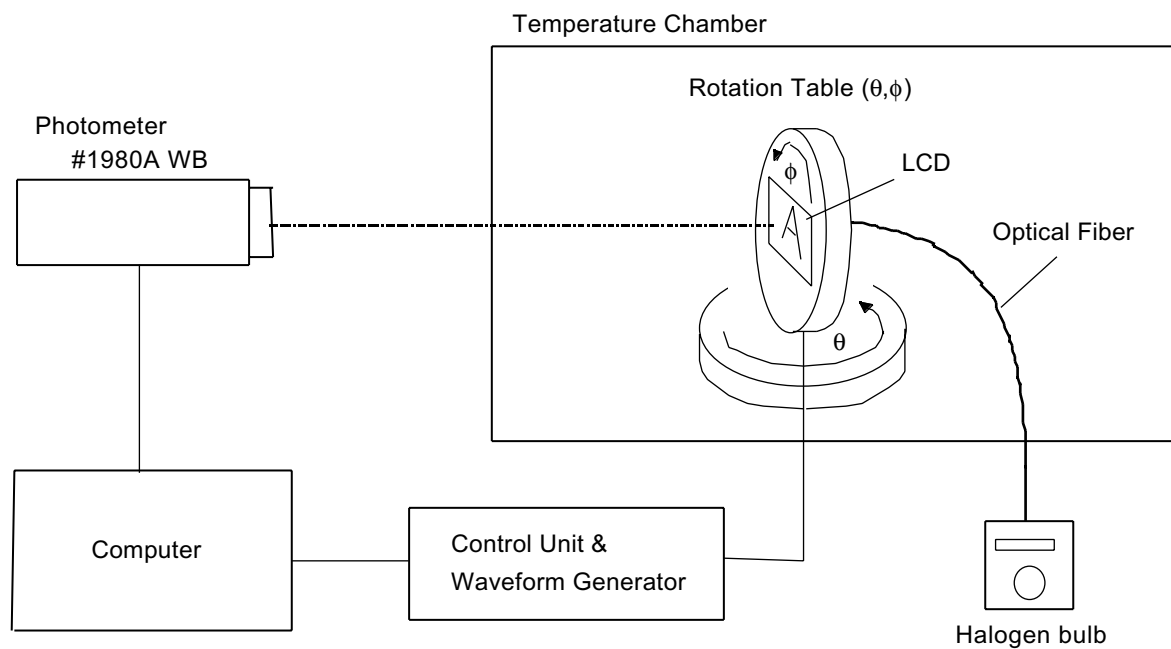


### 3.3. Definition of Viewing Angle and Optimum Viewing Area

- Point ● shows the point where contrast ratio is measured. :  $\theta = 0^\circ$ ,  $\phi = -^\circ$
- Driving condition : 1/64 Duty, 1/9 Bias,  $V_D = 13.3V$ ,  $f_F = 75Hz$



### 3.4. System Block Diagram





## 4.I/O Terminal

### 4.1.Pin Assignment

#### CN1

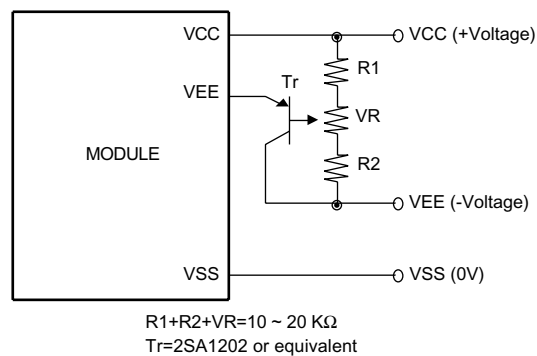
| No. | Symbol                     | Level | Function   |
|-----|----------------------------|-------|--|
| 1   | FG                         | —     | Frame Ground   |
| 2   | V <sub>SS</sub>            | —     | Power Supply (0V, GND)   |
| 3   | V <sub>CC</sub>            | —     | Power Supply for Logic   |
| 4   | V <sub>EE</sub>            | —     | Power Supply for LCD Drive   |
| 5   | $\overline{\text{WR}}$     | H / L | Write Signal L : Read  |
| 6   | $\overline{\text{RD}}$     | H / L | Read Signal L : Write  |
| 7   | $\overline{\text{CE}}$     | H / L | Chip Enable Signal L : Enable  |
| 8   | C/D                        | H / L | Write Mode H : Command Write L : Data Write<br>Read Mode H : Status Read L : Data Read |
| 9   | NC                         | —     | Non-connection   |
| 10  | $\overline{\text{RESET}}$  | H / L | Reset Signal L : Reset   |
| 11  | D0                         | H / L | Data Bus Line  |
| 12  | D1                         | H / L | Data Bus Line  |
| 13  | D2                         | H / L | Data Bus Line  |
| 14  | D3                         | H / L | Data Bus Line  |
| 15  | D4                         | H / L | Data Bus Line  |
| 16  | D5                         | H / L | Data Bus Line  |
| 17  | D6                         | H / L | Data Bus Line  |
| 18  | D7                         | H / L | Data Bus Line  |
| 19  | FS                         | H / L | H:6 × 8dots L:8 × 8dots (Pulled up by 10 k Ω)  |
|     | $\overline{\text{INVERT}}$ | H / L | Display data Reverse signal (Pulled up by 22 k Ω)<br>H:Positive Image L:Negative Image |

#### CN2

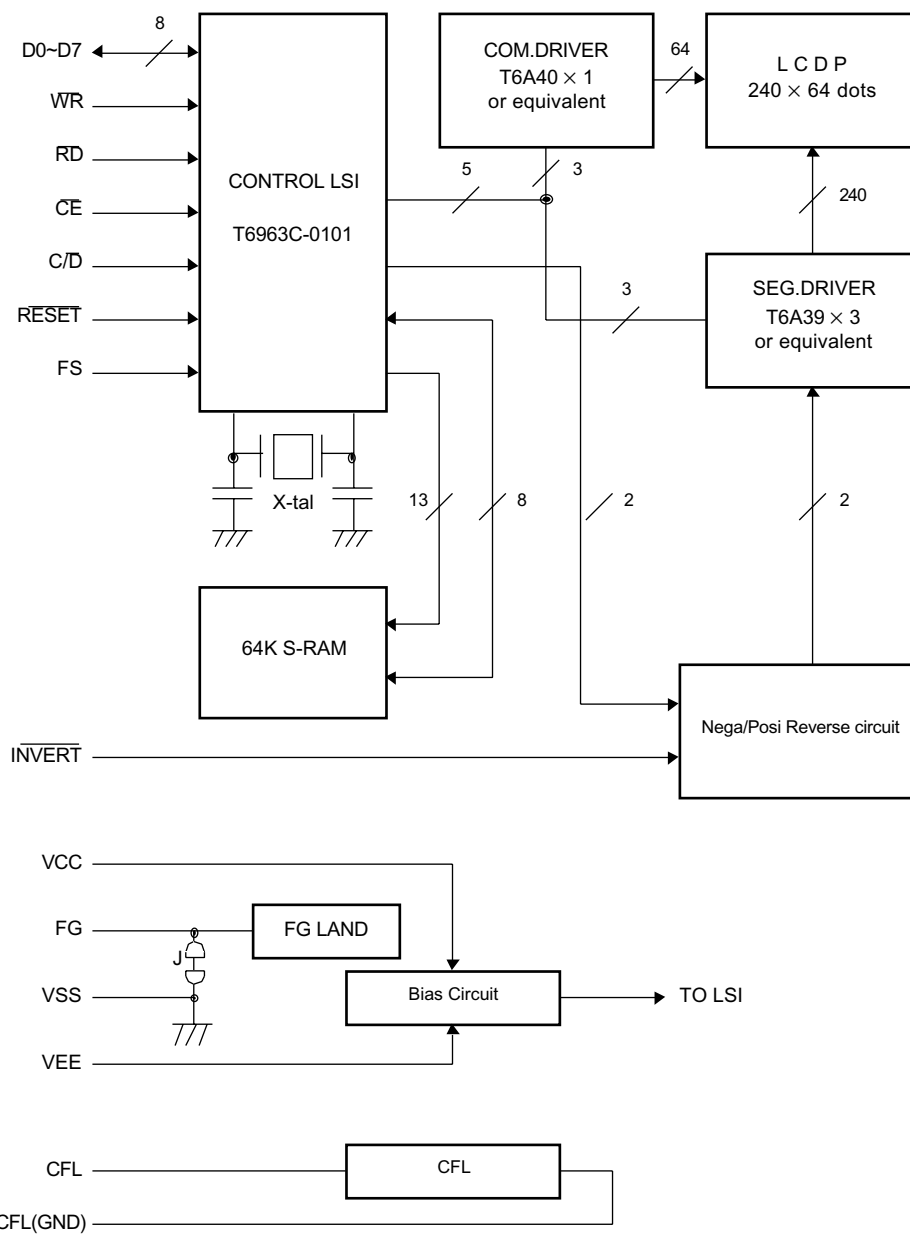
| No. | Symbol    | Level | Function                   |
|-----|-----------|-------|----------------------------|
| 1   | CFL       | —     | Power Supply for CFL (HOT) |
| 2   | NC        | —     | Non-connection             |
| 3   | NC        | —     | Non-connection             |
| 4   | CFL (GND) | —     | Power Supply for CFL (GND) |

#### 4.2.Example of Power Supply

It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.

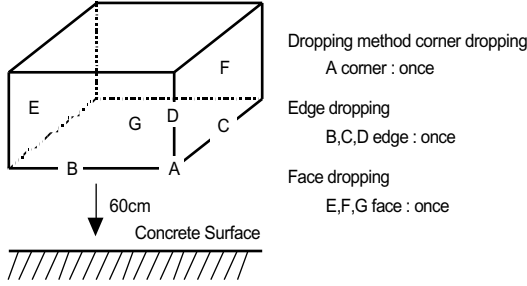


### 4.3. Block Diagram



## 5. Test

No change on display and in operation under the following test condition.

| No. | Parameter                  | Conditions  | Notes |
|-----|----------------------------|---|-------|
| 1   | High Temperature Operating | 50°C±2°C, 96hrs (operation state)   |       |
| 2   | Low Temperature Operating  | 0°C±2°C, 96hrs (operation state)  | 3     |
| 3   | High Temperature Storage   | 60°C±2°C, 96hrs   | 4     |
| 4   | Low Temperature Storage    | -20°C±2°C, 96hrs  | 3, 4  |
| 5   | Damp Proof Test            | 40°C±2°C, 90~95%RH, 96hrs   | 3, 4  |
| 6   | Vibration Test             | Total fixed amplitude : 1.5mm<br>Vibration Frequency : 10~55Hz<br>One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes   | 5     |
| 7   | Shock Test                 | To be measured after dropping from 60cm high on the concrete surface in packing state.<br> |       |

Note 1 : Unless otherwise specified, tests will be conducted under the following condition.

Temperature : 20±5°C

Humidity : 65±5%

Note 2 : Unless otherwise specified, tests will be not conducted under functioning state.

Note 3 : No dew condensation to be observed.

Note 4 : The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.

Note 5 : Vibration test will be conducted to the product itself without putting it in a container.

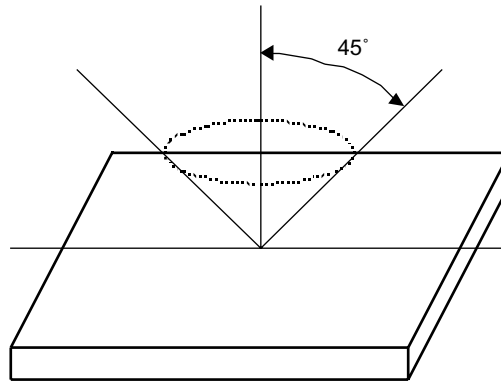
## 6.Appearance Standards

### 6.1.Inspection conditions

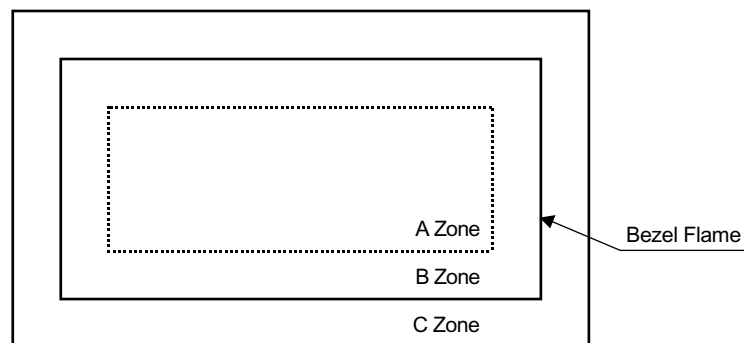
The LCD shall be inspected under 40W white fluorescent light.

The distance between the eyes and the sample shall be more than 30cm.

All directions for inspecting the sample should be within 45° against perpendicular line.



### 6.2.Definition of applicable Zones



A Zone : Active display area

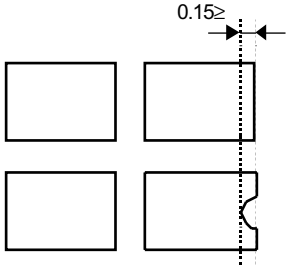
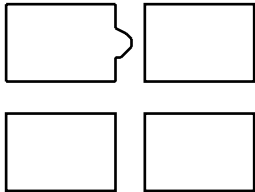
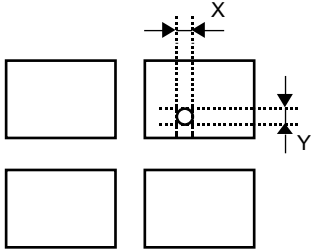
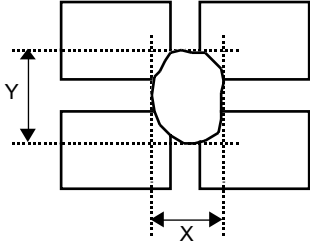
B Zone : Area from outside of "A Zone" to validity viewing area

C Zone : Rest parts

A Zone + B Zone = Validity viewing area

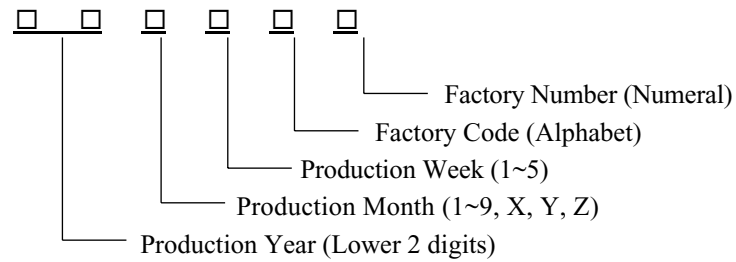
### 6.3. Standards

| No.  |   | <div><div></div><div>(2) Line Shape</div></div> <table><tr><th colspan="2" rowspan="2"><div><div></div><div>Zone</div></div><div>X (mm) Y (mm)</div></th><th colspan="3">Acceptable Number</th></tr><tr><th>A</th><th>B</th><th>C</th></tr><tr><td>—</td><td><math>0.03 \geq W</math></td><td>*</td><td>*</td><td>*</td></tr><tr><td><math>2.0 \geq L</math></td><td><math>0.05 \geq W</math></td><td>3</td><td>3</td><td>*</td></tr><tr><td><math>1.0 \geq L</math></td><td><math>0.1 \geq W</math></td><td>3</td><td>3</td><td>*</td></tr><tr><td>—</td><td><math>0.1 &lt; W</math></td><td colspan="3">In the same way (1)</td></tr></table> <p>X : Length Y : Width * : Disregard</p> <p>Total defects shall not exceed 5.</p> | <div><div></div><div>Zone</div></div> <div>X (mm) Y (mm)</div> |                   | Acceptable Number |  |   | A | B | C            | — | $0.03 \geq W$ | * | *                  | * | $2.0 \geq L$ | $0.05 \geq W$ | 3                  | 3 | * | $1.0 \geq L$ | $0.1 \geq W$ | 3 | 3 | * | — | $0.1 < W$ | In the same way (1) |  |  |
|--|---|--|--|-------------------|-------------------|--|---|---|---|--------------|---|---------------|---|--------------------|---|--------------|---------------|--------------------|---|---|--------------|--------------|---|---|---|---|-----------|---------------------|--|--|
| <div><div></div><div>Zone</div></div> <div>X (mm) Y (mm)</div> |   | Acceptable Number  |  |                   |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
|  |   | A  | B  | C                 |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
| —  | $0.03 \geq W$                                 | *  | *  | *                 |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
| $2.0 \geq L$   | $0.05 \geq W$                                 | 3  | 3  | *                 |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
| $1.0 \geq L$   | $0.1 \geq W$                                  | 3  | 3  | *                 |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
| —  | $0.1 < W$                                     | In the same way (1)  |  |                   |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
| 2  | Air Bubbles<br>(between glass<br>& polarizer) | <div><div></div><div>Zone</div></div> <table><tr><th rowspan="2">Dimension (mm)</th><th colspan="3">Acceptable Number</th></tr><tr><th>A</th><th>B</th><th>C</th></tr><tr><td><math>D \leq 0.3</math></td><td>*</td><td>*</td><td>*</td></tr><tr><td><math>0.3 &lt; D \leq 0.4</math></td><td>3</td><td>*</td><td>*</td></tr><tr><td><math>0.4 &lt; D \leq 0.6</math></td><td>2</td><td>3</td><td>*</td></tr><tr><td><math>0.6 &lt; D</math></td><td>0</td><td>0</td><td>*</td></tr></table> <p>* : Disregard</p> <p>Total defects shall not exceed 3.</p>   | Dimension (mm)   | Acceptable Number |                   |  | A | B | C | $D \leq 0.3$ | * | *             | * | $0.3 < D \leq 0.4$ | 3 | *            | *             | $0.4 < D \leq 0.6$ | 2 | 3 | *            | $0.6 < D$    | 0 | 0 | * |   |           |                     |  |  |
| Dimension (mm)   | Acceptable Number                             |  |  |                   |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
|  | A   | B  | C  |                   |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
| $D \leq 0.3$   | *   | *  | *  |                   |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
| $0.3 < D \leq 0.4$   | 3   | *  | *  |                   |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
| $0.4 < D \leq 0.6$   | 2   | 3  | *  |                   |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |
| $0.6 < D$  | 0   | 0  | *  |                   |                   |  |   |   |   |              |   |               |   |                    |   |              |               |                    |   |   |              |              |   |   |   |   |           |                     |  |  |

| No. | Parameter  | Criteria   |
|-----|--|--|
| 3   | The Shape of Dot                                     | <p>(1) Dot Shape (with Dent)</p>  <p>As per the sketch of left hand.</p>                                      |
|     |  | <p>(2) Dot Shape (with Projection)</p>  <p>Should not be connected to next dot.</p>                           |
|     |  | <p>(3) Pin Hole</p>  <p> <math>(X+Y) / 2 \leq 0.2\text{mm}</math><br/> (Less than 0.1mm is no counted.) </p> |
|     |  | <p>(4) Deformation</p>  <p> <math>(X+Y) / 2 \leq 0.2\text{mm}</math> </p>                                   |
|     |  | <p>Total acceptable number : 1/dot, 5/cell<br/>(Defect number of (4) : 1pc.)</p>   |
| 4   | Polarizer Scratches                                  | Not to be conspicuous defects.   |
| 5   | Polarizer Dirts                                      | If the stains are removed easily from LCDP surface, the module is not defective.   |
| 6   | Complex Foreign Substance Defects                    | Black spots, line shaped foreign substances or air bubbles between glass & polarizer should be 5pcs maximum in total.  |
| 7   | Distance between Different Foreign Substance Defects | $D \leq 0.2$ : 20mm or more<br>$0.2 < D$ : 40mm or more  |

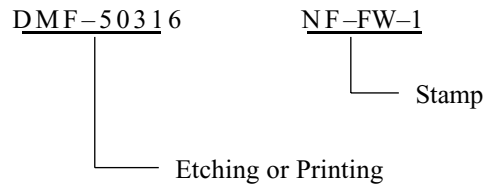
## 7.Code System of Production Lot

The production lot of module is specified as follows.



## 8.Type Number

The type number of module is specified on the back of module as follows.



## 9.Applying Precautions

Please contact us when questions and/or new problems not specified in this specifications arise.

## 10. Handling Precautions

Optrex Products are designed for use in ordinary electronic devices such as business machines, telecommunications equipment, measurement devices and etc..

Optrex Products are not designed, intended, or authorized for use in any application in which the failure of the product could result in a situation where personal injury or death may occur. These applications include, but are not limited to, life-sustaining equipment, nuclear control devices, aerospace equipment, devices related to hazardous or flammable materials, etc. (If Buyer intends to purchase or use the Optrex Products for such unintended or unauthorized applications, Buyer must secure prior written consent to such use by a responsible officer of Optrex Corporation.) Should Buyer purchase or use Optrex Products for any such unintended or unauthorized application (without such consent), Buyer shall indemnify and hold Optrex and its officers, employees, subsidiaries, affiliates and distributors harmless against all claims, costs, damages and expenses, and reasonable attorney's fees, arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Optrex was negligent regarding the design or manufacture of the part.

- 1) LCD may be broken because it is made of glass.
- 2) Polarizer is a soft material and can easily be scratched.
- 3) Please avoid static electricity.
  - ① Please be sure to ground human body and electric appliances during work.
  - ② It is preferable to use conductive mat on table and wear cotton clothes or conduction processed fiber. Synthetic fiber is not recommended.
  - ③ Please slowly peel off protective film, because static electricity may be charged.
- 4) If it is necessary to store LCD modules for a long time, please comply with the following procedures. If storage condition is not satisfactory, display (especially polarizer) may be deteriorated or soldering I/O terminals may become difficult (some oxide is generated at I/O terminals plating).
  - ① Store as delivered by Optrex
  - ② If you store as unpacked, put in anti-static bag, seal its opening and store where it is not subjected to direct sunshine nor fluorescent lamp.
  - ③ Store at temperature 0 to +35°C and at low humidity. Please refer to our specification sheets for storage temperature range and humidity condition.
- 5) The module does not contain excess current limiter.  
Please design the limiter to cut excess current in your power supply circuit.
- 6) Liquid crystal may be leaked when display is broken. Never taste it. If your hands or clothes touch it, please immediately wash using soap.
- 8) A high voltage over 1000V is applied at the connector of CFL cable when the CFL inverter is connected and energized. Please do not touch there incidentally or accidentally to avoid a skin burn. And please set the cable properly in the housing to avoid a worn-out of isolated cover of cable wire.

Optrex shall not be responsible for any infringement of industrial property rights of third parties in any country arising out of the application or use of Optrex Products, except which directly concern the structure or production of such products.