			First Edition	Approved by	Production Div.
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L	CD Module Sp	ecification	Final Revision		
			*****	Checked by	Design Engineering Div.
Type No.	DMF5001N	IYL-EB	<u>JL</u>	Prepared by	Production Div.
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1. General Specifications

Operating Temp.	: min. 0 ~ max. 50
Storage Temp.	: min20 ~ max. 60
Dot Pixels	: 160 (W) × 128 (H) dots
Dot Size	: 0.54 (W) × 0.54 (H) mm
Dot Pitch	: 0.58 (W) × 0.58 (H) mm
Viewing Area	: 101.0 (W) × 82.0 (H) mm
Outline Dimensions	: 129.0 (W) × 102.0 (H) × 12.8 (D) mm
Weight	: 180g max.
LCD Type	: NSD-7353 (STN / Yellow-mode / Transflective)
Viewing Angle	: 9:00
Control LSI	: T6963C-0101 (Produced by TOSHIBA)
Data Transfer	: 8-bit parallel data transfer
Backlight	: Electro Luminescence (EL) / Blue-green
Drawings	: Dimensional Outline UE-31324A

2. Electrical Specifications

2.1. Absolute Maximum Ratings

		0			Vss=0V
Parameter	Symbol	Conditions	Min.	Max.	Units
Supply Voltage	VDD-VSS	-	-0.3	7.0	v
(Logic)					
Supply Voltage	VDD-VEE	-	-0.3	28.0	v
(LCD Drive)					
Input Voltage	VI	-	-0.3	VDD+0.3	V

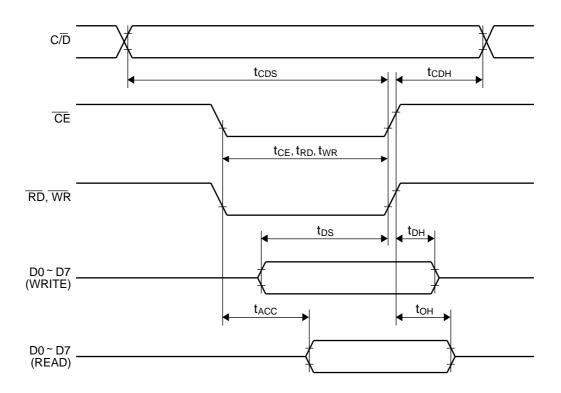
2.2.DC Characteristics

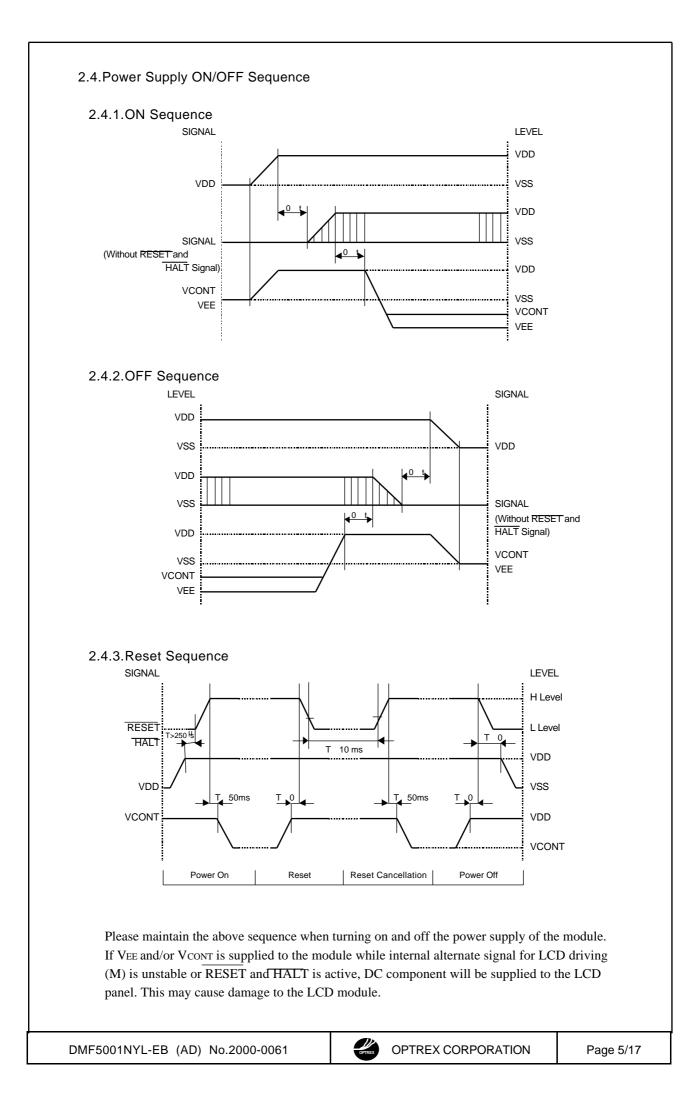
					Ta=25 ,	Vss=0V
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Supply Voltage	VDD-VSS	-	4.5	-	5.5	v
(Logic)						
	VDD-VEE	-	17.3	-	24.3	v
Supply Voltage						
(LCD Drive)	VDD-VCONT		Shown in 3	.1		v
High Level	Vih	VDD= $5.0V \pm 10\%$	VDD-2.2	-	Vdd	v
Input Voltage						
Low Level	VIL	$V_{DD}=5.0V \pm 10\%$	0	-	0.8	v
Input Voltage						
High Level	Voh	Іон=-0.75mА	VDD-0.3	-	Vdd	v
Output Voltage						
Low Level	Vol	IoL=0.75mA	0	-	0.3	v
Output Voltage						
	Idd	VDD-Vss=5.0V	-	13.4	30.0	mA
Supply Current						
	IEE	VDD-VCONT=19.9V	-	3.7	20.0	mA

2.3.AC Characteristics

VDD= $5.0V \pm 10\%$

Parameter	Symbol	Min.	Max.	Units
C/D Setup Time	tcds	100	-	ns
C/D Hold Time	t cdh	10	-	ns
$\overline{\text{CE}}, \overline{\text{RD}}, \overline{\text{WR}}$ Pulse Width	tce, trd, twr	80	-	ns
Data Setup Time	tds	80	-	ns
Data Hold Time	tdн	40	-	ns
Access Time	tacc	-	150	ns
Output Hold Time	tон	10	50	ns





2.5.Lighting Specifications

2.5.1. Absolute Maximum Rating

					Ta=25
Parameter	Conditions	Min.	Тур.	Max.	Units
Input Voltage	-	-	-	150	Vrms
Input Frequency	-	-	-	800	Hz

2.5.2. Operating Characteristics

					Ta=25
Parameter	Conditions	Min.	Тур.	Max.	Units
Input Voltage	-	-	100	-	Vrms
Input Frequency	-	-	400	-	Hz
Current	AC100Vrms, 400Hz	-	13.5	18	mA
Luminance of	AC100Vrms, 400Hz	40	50	-	cd/m²
Backlight Surface					
Life	AC100Vrms, 400Hz	2000	-	-	hrs
	Ta=20 , 60% RH				

Recommended Inverter : NS-106 (DC 5.0V \pm 10%, Produced by NEC)

3.Optical Specifications

3.1.LCD Driving Voltage

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Recommended		Ta= 0	-	-	24.3	v
LCD Driving Voltage	VDD-VCONT	Ta=25	19.1	20.5	21.9	V
Note 1		Ta=50	17.3	-	-	v

Note 1 : Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

3.2. Optical Characteristics

		Ta=25 ,	1/128 Duty, 1/12.3 Bias	s, VD=19.9	V (Note 4)	=0, $=0$,	= - °
Parameter		Symbol	Conditions	Min.	Тур.	Max.	Units
Contrast Ratio Note 1		CR	=0 °, $=$ - °	-	6	-	
Viewing An	gle		Shown in 3.3				
Response	Rise Note 2	Ton	-	-	180	270	ms
Time	Decay Note 3	Toff	-	-	240	360	ms

Note 1 : Contrast ratio is definded as follows.

CR = LOFF / LON

LON : Luminance of the ON segments

LOFF: Luminance of the OFF segments

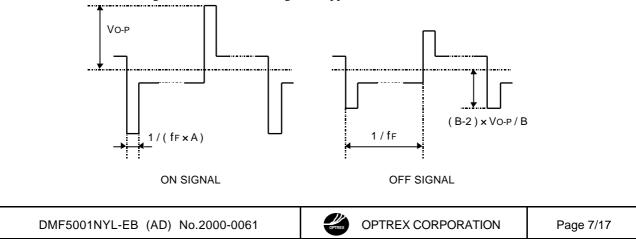
- Note 2 : The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.
- Note 3 : The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.
- Note 4 : Definition of Driving Voltage VD

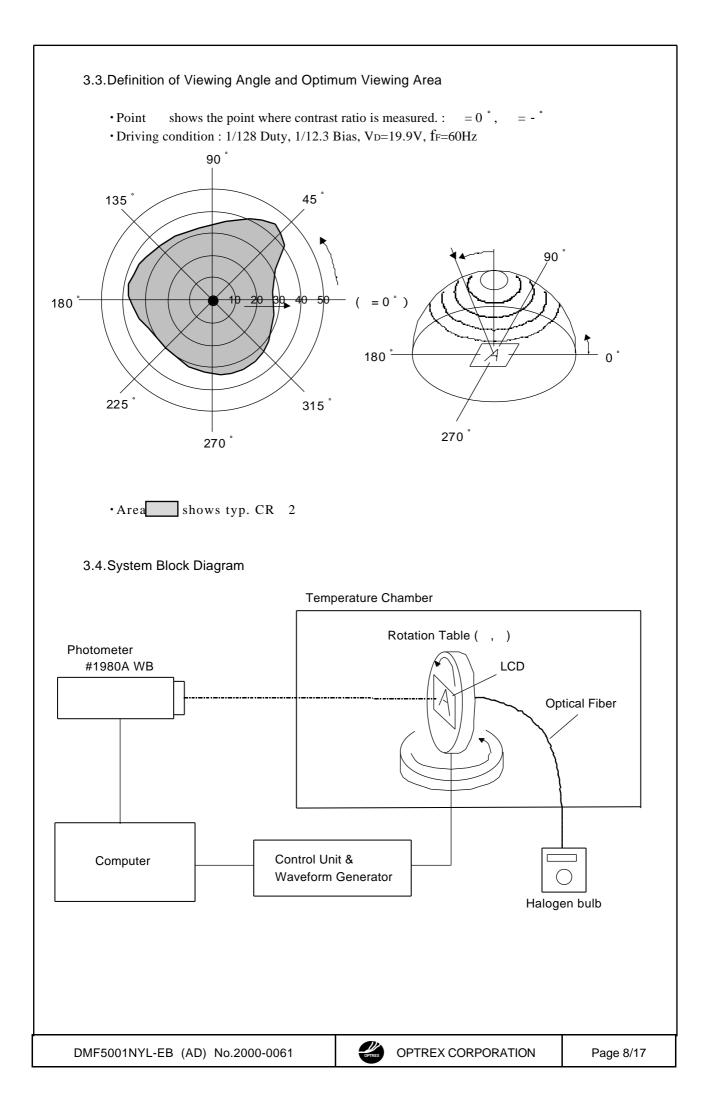
VD=VDD-VCONT-VBE

Assuming that the typical driving waveforms shown below are applied to the LCD Panel at 1/A Duty - 1/B Bias (A : Duty Number, B : Bias Number). Driving voltage VD is definded as follows.

 $V_D = (Vth1+Vth2) / 2$

- Vth1 :The voltage Vo-P that should provide 50% of the saturation level in the luminance at the segment which the ON signal is applied to.
- Vth2 :The voltage Vo-P that should provide 50% of the saturation level in the luminance at the segment which the OFF signal is applied to.





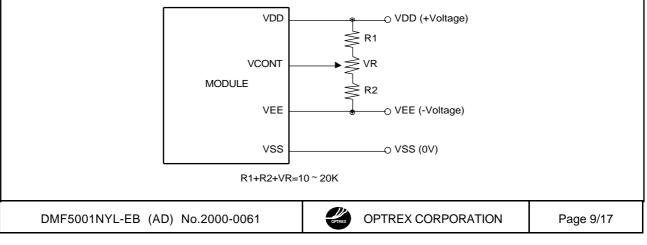
4.I/O Terminal

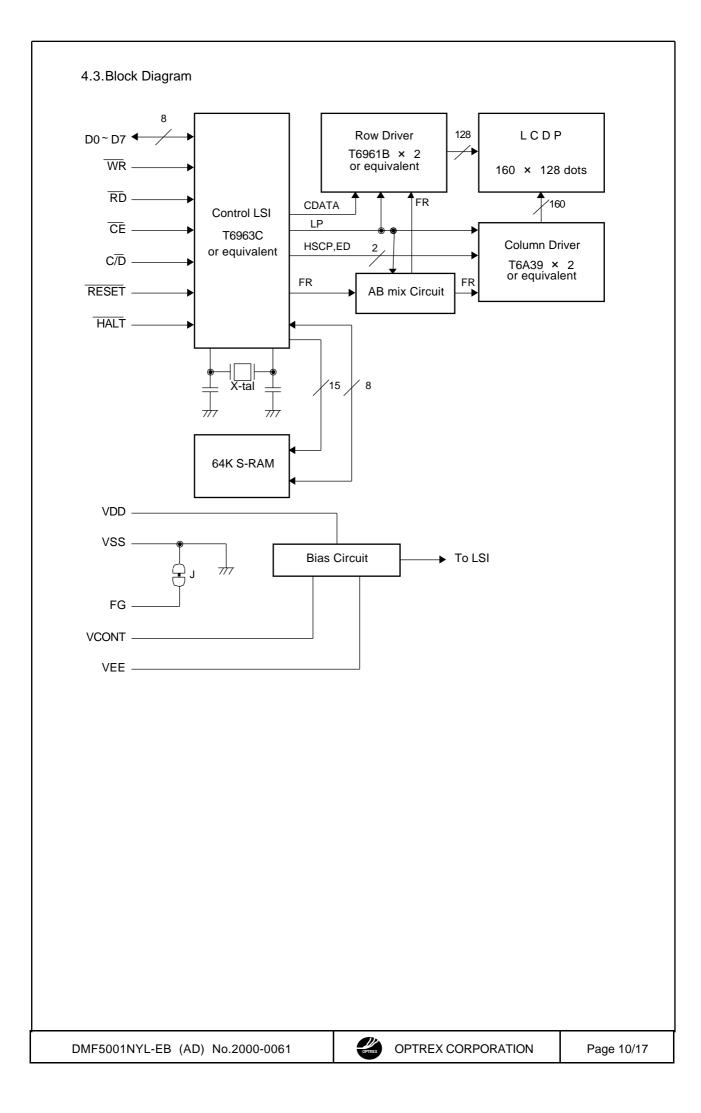
1.1.Pin Assignment

No.	Symbol	Level	Function
1	FG	-	Frame Ground
2	Vss		Power Supply (0V, GND)
3	Vdd		Power Supply for Logic
4	VCONT		Voltage Level for LCD Contrast Adjustment
5	VEE		Power Supply for LCD Drive
6	WR	H/L	Write Signal L : Active
7	RD	H/L	Read Signal L : Active
8	CE	H/L	Chip Enable Signal L : Active
9	C/D	H / L	Write Mode H : Command Write L : Data Write
			Read Mode H : Status Read L : Data Read
10	HALT	H/L	Clock Operating Stop Signal L : Halt
11	RESET	H/L	Reset Signal L : Reset
12	D0	H/L	Display Data
13	D1	H/L	Display Data
14	D2	H/L	Display Data
15	D3	H/L	Display Data
16	D4	H/L	Display Data
17	D5	H/L	Display Data
18	D6	H/L	Display Data
19	D7	H/L	Display Data
20	NC	_	Non-connection
21	EL	-	Power Supply for EL
22	EL	-	Power Supply for EL

4.2. Example of Power Supply

It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.





<u>5.Test</u>

No change on display and in operation under the following test condition.

No.	Parameter	Conditions	Notes
1	High Temperature Operating	50 ± 2 , 96hrs (operation state)	
2	Low Temperature Operating	0 ± 2 , 96hrs (operation state)	3
3	High Temperature Storage	60 ± 2 , 96hrs	4
4	Low Temperature Storage	-20 ± 2 , 96hrs	3, 4
5	Damp Proof Test	40 ± 2 , 90 ~ 95% RH, 96hrs	3, 4
6	Vibration Test	Total fixed amplitude : 1.5mm Vibration Frequency : 10 ~ 55Hz One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes	5
7	Shock Test	To be measured after dropping from 60cm high on the concrete surface in packing state.	

Note 1 : Unless otherwise specified, tests will be conducted under the following condition. Temperature $: 20 \pm 5$

Humidity $: 65 \pm 5\%$

Note 2 : Unless otherwise specified, tests will be not conducted under functioning state.

Note 3 : No dew condensation to be observed.

Note 4 : The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.

Note 5 : Vibration test will be conducted to the product itself without putting it in a container.

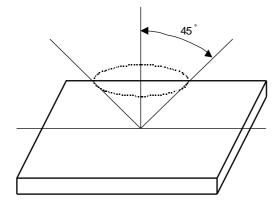
6. Appearance Standards

6.1.Inspection conditions

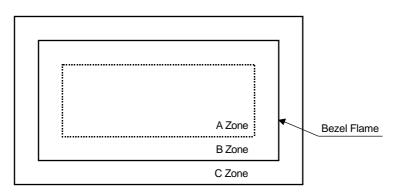
The LCD shall be inspected under 40W white fluorescent light.

The distance between the eyes and the sample shall be more than 30cm.

All directions for inspecting the sample should be within 45 $^{\circ}$ against perpendicular line.



6.2. Definition of applicable Zones



A Zone : Active display area

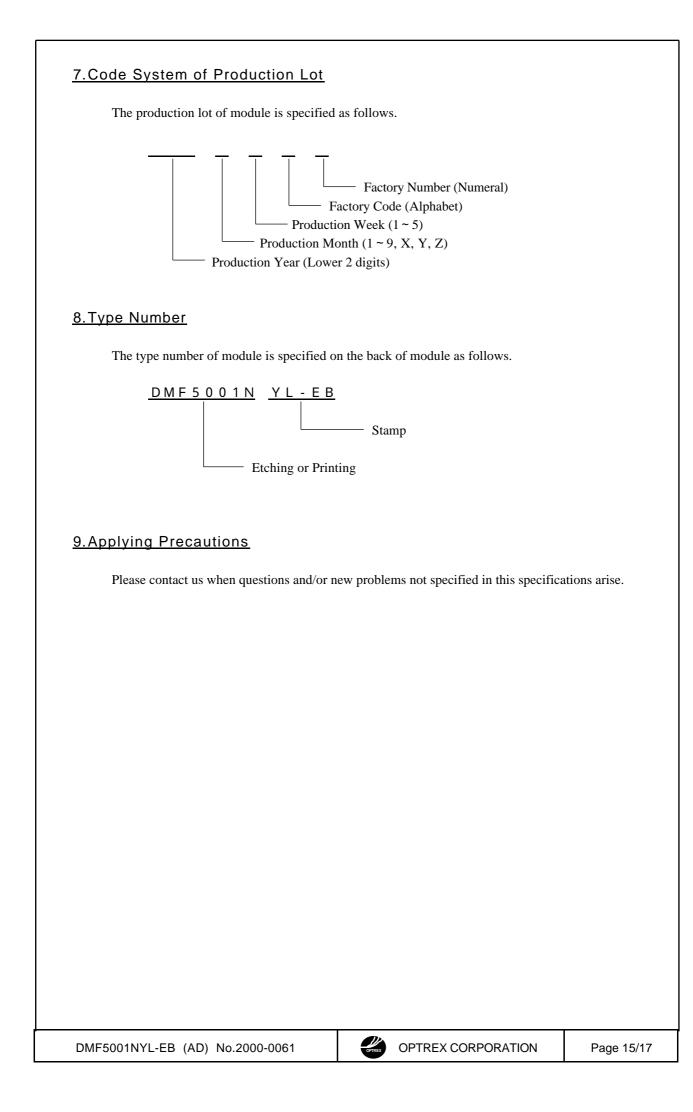
B Zone : Area from outside of "A Zone" to validity viewing area C Zone : Rest parts

A Zone + B Zone = Validity viewing area

6.3.Standards

No.	Parameter		Criteria				
1	Black and	(1) Round Shape					
	White Spots,		Zone	A	cceptable Numb	er	
	Foreign Substances	Dimension (mm)		А	В	С	
		D	0.1	*	*	*	
		0.1 < D	0.2	3	5	*	
		0.2 < D	0.25	2	3	*	
		0.25 < D	0.3	0	1	*	
		0.3 < D		0	0	*	
		D = (Long + S)	hort) / 2	* : Disrega	rd		
		(2) Line Shape					
			Zone	Acceptable Number			
		X (mm) Y (n	ım)	А	В	С	
		- 0	.03 W	*	*	*	
		2.0 L 0	.05 W	3	3	*	
		1.0 L 0	.1 W	3	3	*	
		- 0	.1 < W	In	the same way (1)	
		X:Length Y	: Width	* : Disregard			
		Total defects shall r	not exceed	5.			
2	Air Bubbles						
	(between glass		Zone	Ad	er		
	& polarizer)	Dimension (mm)		А	В	С	
		D	0.3	*	*	*	
		0.3 < D	0.4	3	*	*	
		0.4 < D	0.6	2	3	*	
		0.6 < D		0	0	*	
		* : Disregard					
		Total defects shall 1	not exceed	3.			

No.	Parameter	Criteria
3	The Shape of Dot	(1) Dot Shape (with Dent) 0.15 As per the sketch of left hand.
		(2) Dot Shape (with Projection)
		(3) Pin Hole X Y (X+Y) / 2 0.2mm (Less than 0.1mm is no counted.)
		(4) Deformation (X+Y)/2 0.2mm
		Total acceptable number : 1/dot, 5/cell (Defect number of (4) : 1pc.)
4	Polarizer Scratches	Not to be conspicuous defects.
5	Polarizer Dirts	If the stains are removed easily from LCDP surface, the module is not defective.
6	Complex Foreign Substance Defects	Black spots, line shaped foreign substances or air bubbles between glass & polarizer should be 5pcs maximum in total.
7	Distance between Different Foreign Substance Defects	D 0.2 : 20mm or more 0.2 < D : 40mm or more



10. Precautions Relating Product Handling

The Following precautions will guide you in handling our product correctly.

1) Liquid crystal display devices

The liquid crystal display device panel used in the liquid crystal display module is made of plate glass. Avoid any strong mechanical shock. Should the glass break handle it with care. The polarizer adhering to the surface of the LCD is made of a soft material. Guard against scratching it.

2) Care of the liquid crystal display module against static electricity discharge.

When working with the module, be sure to ground your body and any electrical equipment you may be using. We strongly recommend the use of anti static mats (made of rubber), to protect work tables against the hazards of electrical shock.

Avoid the use of work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

Slowly and carefully remove the protective film from the LCD module, since this operation can generate static electricity.

3) When the LCD module alone must be stored for long periods of time:

Protect the modules from high temperature and humidity. Keep the modules out of direct sunlight or direct exposure to ultraviolet rays. Protect the modules from excessive external forces.

- 4) Use the module with a power supply that is equipped with an overcurrent protector circuit, since the module is not provided with this protective feature.
- 5) Do not ingest the LCD fluid itself should it leak out of a damaged LCD module. Should hands or clothing come in contact with LCD fluid, wash immediately with soap.
- 6) Conductivity is not guaranteed for models that use metal holders where solder connections between the metal holder and the PCB are not used. Please contact us to discuss appropriate ways to assure conductivity.
- 7) For models which use CFL:

High voltage of 1000V or greater is applied to the CFL cable connector area.

Care should be taken not to touch connection areas to avoid burns.

Protect CFL cables from rubbing against the unit and thus causing the wire jacket to become worn. The use of CFLs for extended periods of time at low temperatures will significantly shorten their service life. 8) For models which use touch panels:

Do not stack up modules since they can be damaged by components on neighboring modules. Do not place heavy objects on top of the product. This could cause glass breakage.

9) For models which use COG,TAB,or COF:

The mechanical strength of the product is low since the IC chip faces out unprotected from the rear. Be sure to protect the rear of the IC chip from external forces. Given the fact that the rear of the IC chip is left exposed, in order to protect the unit from electrical damage, avoid installation configurations in which the rear of the IC chip runs the risk of making any electrical contact.

10) Models which use flexible cable, heat seal, or TAB:

In order to maintain reliability, do not touch or hold by the connector area. Avoid any bending, pulling, or other excessive force, which can result in broken connections.

11.Warranty

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.

We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.

We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.

When the product is in CFL models, CFL service life and brightness will vary according to the performance of the inverter used, leaks, etc. We cannot accept responsibility for product performance, reliability, or defect, which may arise.

We cannot accept responsibility for intellectual property of a third party, which may arise through the application of our product to your assembly with exception to those issues relating directly to the structure or method of manufacturing of our product.

Optrex will not be held responsible for any quality guarantee issue for defect products judged as Optrex-origin longer than 2 (two) years from Optrex production or 1(one) year from Optrex, Optrex America, Optrex Europe, Display LC delivery which ever comes later.