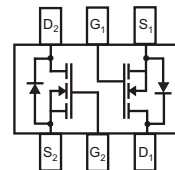
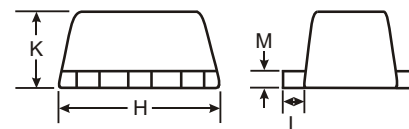
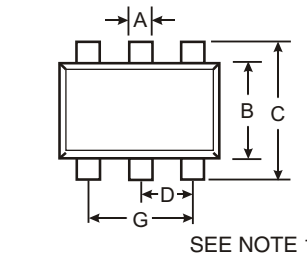


**DUAL N-CHANNEL ENHANCEMENT
MODE FIELD EFFECT TRANSISTOR**
Features

Dual N-Channel MOSFET
 Low On-Resistance
 Low Gate Threshold Voltage
 Low Input Capacitance
 Fast Switching Speed
 Low Input/Output Leakage
 Ultra-Small Surface Mount Package
Lead Free By Design/RoHS Compliant (Note 3)

Mechanical Data

Case: SOT-563
 Case Material: Molded Plastic. UL Flammability
 Classification Rating 94V-0
 Moisture Sensitivity: Level 1 per J-STD-020C
 Terminals Connections: See Diagram
 Terminals: Finish - Matte Tin annealed over Alloy 42
 leadframe. Solderable per MIL-STD-202, Method 208
 Terminals: Lead bearing terminal plating available. See
 Ordering information Page 2, Note 6
 Marking: See Page 2
 Ordering & Date Code Information: See Page 2
 Weight: 0.006 grams (approximate)



SOT-563			
Dim	Min	Max	Typ
A	0.15	0.30	0.25
B	1.10	1.25	1.20
C	1.55	1.70	1.60
D	0.50		
G	0.90	1.10	1.00
H	1.50	1.70	1.60
K	0.56	0.60	0.60
L	0.10	0.30	0.20
M	0.10	0.18	0.11
All Dimensions in mm			

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V_{DS}	60	V
Drain-Gate Voltage $R_{GS} = 1.0M$	V_{DGR}	60	V
Gate-Source Voltage (Note 3)	V_{GSS}	± 20	V
Continuous Pulsed		± 40	
Drain Current (Note 3)	I_D	280	mA
Drain Current (Note 3)	I_{DM}	1.5	A
Total Power Dissipation	P_d	150	mW
Thermal Resistance, Junction to Ambient	R_{JA}	833	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

- Notes:
- Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways).
 - Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
 - No purposefully added Lead.

Electrical Characteristics

 @ T_A = 25 °C unless otherwise specified

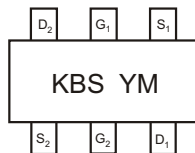
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	70		V	V _{GS} = 0V, I _D = 10 A
Zero Gate Voltage Drain Current @ T _C = 25°C @ T _C = 85°C	I _{DSS}			25 250	nA	V _{DS} = 50V, V _{GS} = 0V
Gate-Body Leakage	I _{GSS}			±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	V _{GS(th)}	1.0		2.5	V	V _{DS} = V _{GS} , I _D = 250 A
Satic Drain-Source On-Resistance	R _{DS (ON)}			7.5 13.5		V _{GS} = 5V, I _D = 0.05A, V _{GS} = 10V, I _D = 0.5A, T _J = 125°C
On-State Drain Current	I _{D(ON)}	0.5	1.0		A	V _{GS} = 10V, V _{DS} = 7.5V
Forward Transconductance	g _{FS}	80			mS	V _{DS} = 10V, I _D = 0.2A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}			50	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHZ
Output Capacitance	C _{oss}			25	pF	
Reverse Transfer Capacitance	C _{rss}			5.0	pF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{D(ON)}			20	ns	V _{DD} = 30V, I _D = 0.2A, R _L = 150 Ω , V _{GEN} = 10V, R _{GEN} = 25
Turn-Off Delay Time	t _{D(OFF)}			20	ns	

Ordering Information (Note 5)

Device	Packaging	Shipping
DMN600V-7	SOT-563	3000/Tape & Reel

- Notes:
- Short duration test pulse used to minimize self-heating effect.
 - For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.
 - "-L" suffix on part number indicates Pb/Sn terminal plating. "-L" version is a Non Lead-Free, Non RoHS-compliant device.

Marking Information



KBS = Product Type Marking Code
 YM = Date Code Marking
 Y = Year ex: S = 2005
 M = Month ex: 9 = September

Date Code Key

Year	2005	2006	2007	2008	2009
Code	S	T	U	V	W

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

