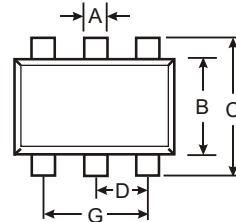


Features

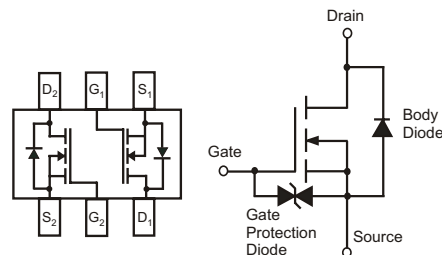
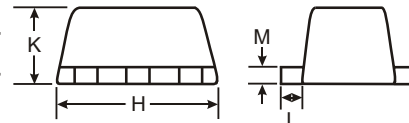
- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **Lead Free By Design/RoHS Compliant (Note 2)**
- **ESD Protected Up To 2kV**
- **"Green" Device (Note 4)**



SOT-563			
Dim	Min	Max	Typ
A	0.15	0.30	0.25
B	1.10	1.25	1.20
C	1.55	1.70	1.60
D	0.50		
G	0.90	1.10	1.00
H	1.50	1.70	1.60
K	0.56	0.60	0.60
L	0.10	0.30	0.20
M	0.10	0.18	0.11
All Dimensions in mm			

Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Marking: See Page 2
- Ordering & Date Code Information: See Page 2
- Weight: 0.006 grams (approximate)



EQUIVALENT CIRCUIT PER ELEMENT



ESD protected up to 2kV

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V _{DSS}	60	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current (Note 1)	Continuous Pulsed (Note 3)	I _D	305 800	mA
Total Power Dissipation (Note 1)		P _d	250	mW
Thermal Resistance, Junction to Ambient		R _{θJA}	500	°C/W
Operating and Storage Temperature Range		T _j , T _{STG}	-65 to +150	°C

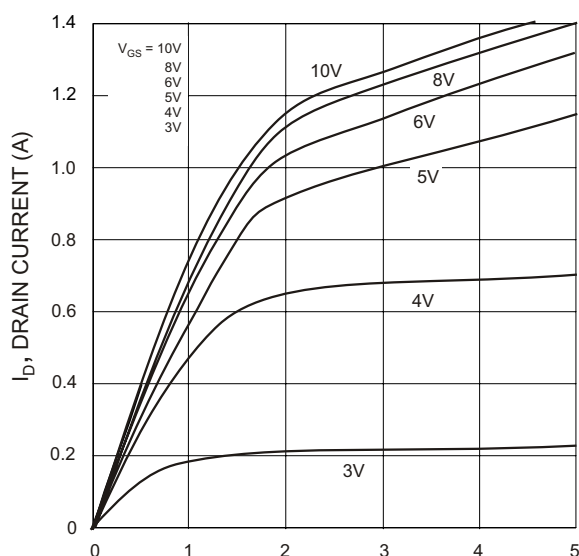
- Note:
1. Device mounted on FR-4 PCB.
 2. No purposefully added lead.
 3. Pulse width ≤10μs, Duty Cycle ≤1%.
 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

Electrical Characteristics

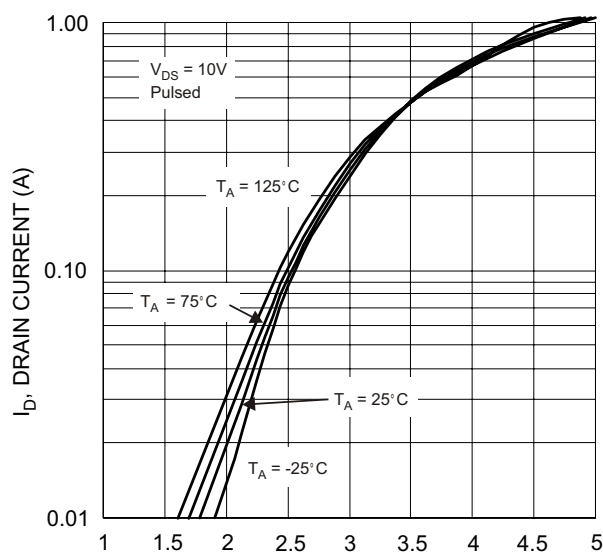
 @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	V _{GS} = 0V, I _D = 10μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	250	nA	V _{DS} = 50V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±200	nA	V _{GS} = ±10V, V _{DS} = 0V
		—	—	±50		V _{GS} = ±5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V _{GS(th)}	1.0	1.6	2.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS (ON)}	—	—	2.0	Ω	V _{GS} = 10V, I _D = 0.5A
				3.0		V _{GS} = 4.5V, I _D = 200mA
Forward Transfer Admittance	Y _{fs}	—	284	—	ms	V _{DS} = 10V, I _D = 0.2A
Diode Forward Voltage (Note 5)	V _{SD}	0.5	—	1.4	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	—	—	50	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	—	25	pF	
Reverse Transfer Capacitance	C _{rss}	—	—	5.0	pF	

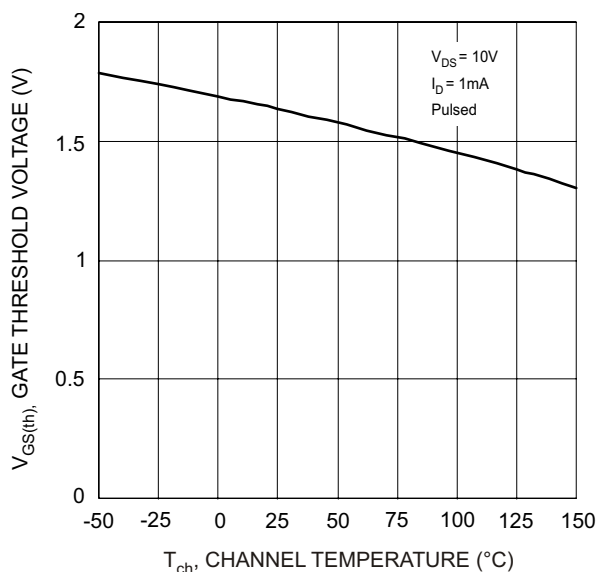
Notes: 5. Short duration test pulse used to minimize self-heating effect.



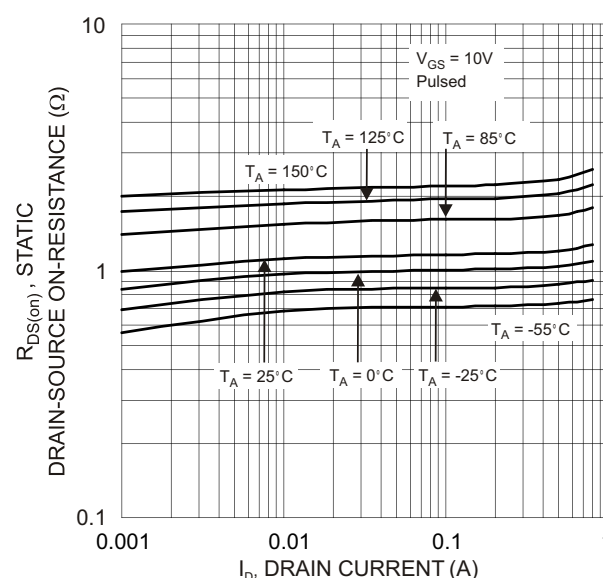
V_{GS} , DRAIN-SOURCE VOLTAGE (V)
Fig. 1 Typical Output Characteristics



V_{GS} , GATE-SOURCE VOLTAGE (V)
Fig. 2 Typical Transfer Characteristics



T_{ch} , CHANNEL TEMPERATURE ($^\circ\text{C}$)
Fig. 3 Gate Threshold Voltage
vs. Channel Temperature



I_D , DRAIN CURRENT (A)
Fig. 4 Static Drain-Source On-Resistance
Vs. Drain Current

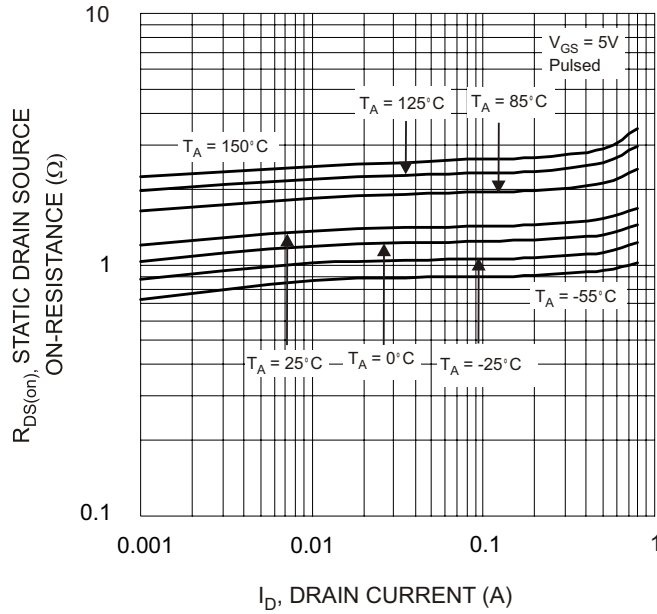


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

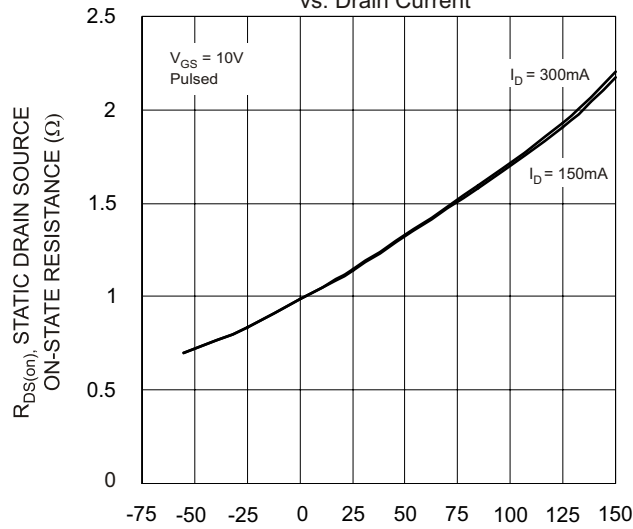


Fig. 7 Static Drain-Source On-State Resistance vs. Channel Temperature

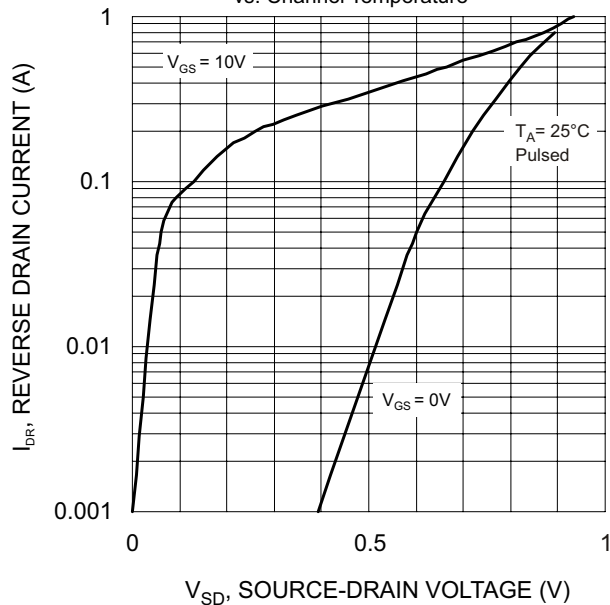


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage

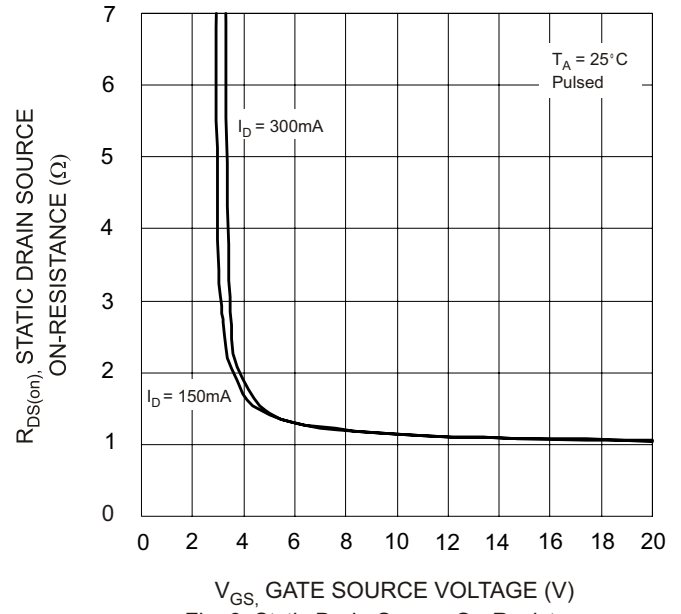


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage

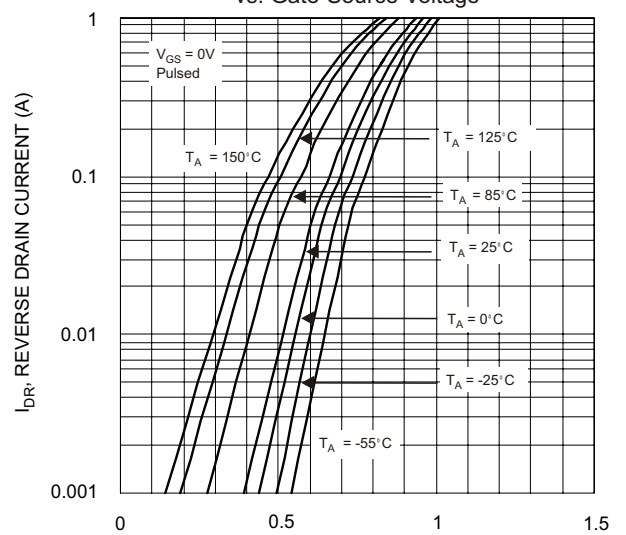


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

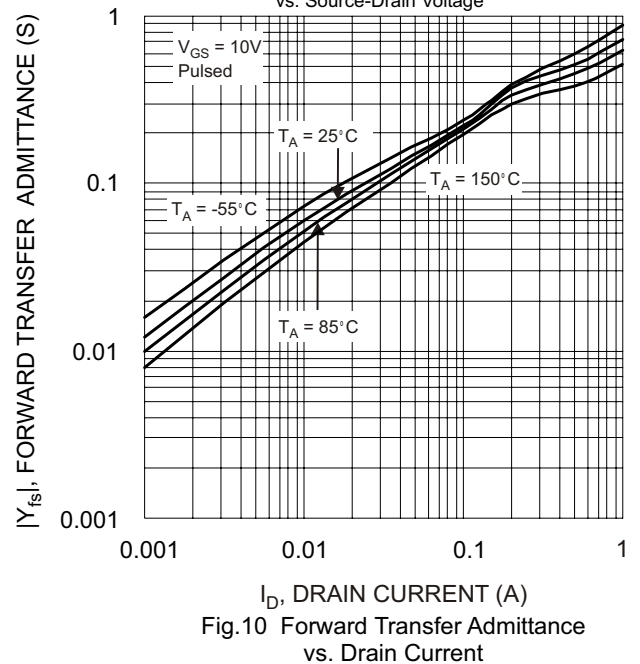


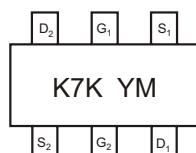
Fig. 10 Forward Transfer Admittance vs. Drain Current

Ordering Information (Note 6)

Device	Packaging	Shipping
DMN601VK-7	SOT-563	3000/Tape & Reel

Notes: 6. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



K7K = Marking Code
YM = Date Code Marking
Y = Year ex: S = 2005
M = Month ex: 9 = September

Date Code Key

Year	2005	2006	2007	2008	2009
Code	S	T	U	V	W

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D