





#### **60V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub> T <sub>A</sub> = 25°C		
60V	66mΩ @ V <sub>GS</sub> = 10V	5.0A		
600	97mΩ @ V <sub>GS</sub> = 4.5V	4.1A		

## **Description and Applications**

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor control
- Backlighting
- DC-DC Converters
- Power management functions

### **Features and Benefits**

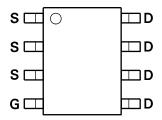
- Low on-resistance
- Fast switching speed
- "Green" component and RoHS compliant (Note 1)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

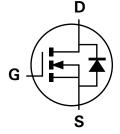
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See diagram below
- Terminals: Finish Matte Tin annealed over Copper lead frame.
   Solderable per MIL-STD-202, Method 208
- Weight: 0.074 grams (approximate)







Top View



**Equivalent Circuit** 

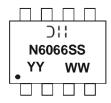
### Ordering Information (Note 1)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN6066SSS-13	N6066SS	13	12	2,500

1. Diodes, Inc. defines "Green" products as those which are RoHS compliant and contain no halogens or antimony compounds; further information about Diodes Inc.'s "Green" Policy can be found on our website. For packaging details, go to our website.

## **Marking Information**

Note:



DII = Manufacturer's Marking
N6066SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 09 = 2009)
WW = Week (01-53)





### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

	Characteristic		Symbol	Value	Unit	
Drain-Source voltage Gate-Source voltage (Note 2)			V <sub>DSS</sub>	60	V	
			V <sub>GS</sub>	±20		
		(Note 4)		5.0		
Continuous Drain current	$V_{GS} = 10V$	$T_A = 70$ °C (Note 4)	I <sub>D</sub>	4.0	Α	
		(Note 3)		3.7		
Pulsed Drain current V <sub>GS</sub> = 10V		(Note 5)	I <sub>DM</sub>	23	А	
Continuous Source current (Body diode)		(Note 4)	I <sub>S</sub>	4.0	А	
Pulsed Source current (Body diode)		(Note 5)	I <sub>SM</sub>	23	A	

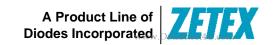
## Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Power dissipation	(Note 3)		1.56 12.5	W
Linear derating factor	(Note 4)	P <sub>D</sub>	2.81 22.5	mW/°C
Thermal Desistance Junction to Ambient	(Note 3)		80.0	
Thermal Resistance, Junction to Ambient	(Note 4)	$R_{ hetaJA}$	44.5	°C/W
Thermal Resistance, Junction to Lead	(Note 6)	$R_{ hetaJL}$	37.0	
Operating and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

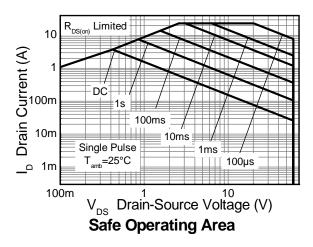
#### Notes:

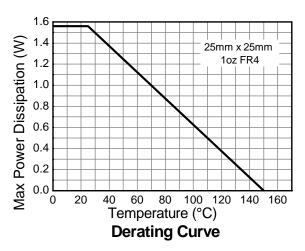
- 2. AEC-Q101  $V_{GS}$  maximum is  $\pm 16V$ .
- 3. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 4. Same as note (3), except the device is measured at t ≤ 10 sec.
  5. Same as note (3), except the device is pulsed with D= 0.02 and pulse width 300 μs. The pulse current is limited by the maximum junction temperature.
  6. Thermal resistance from junction to solder-point (at the end of the drain lead).

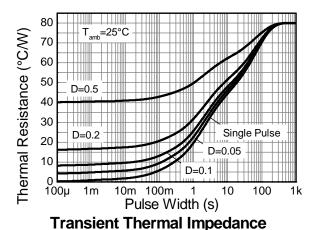


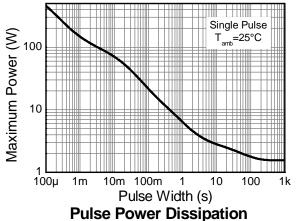


## **Thermal Characteristics**













# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

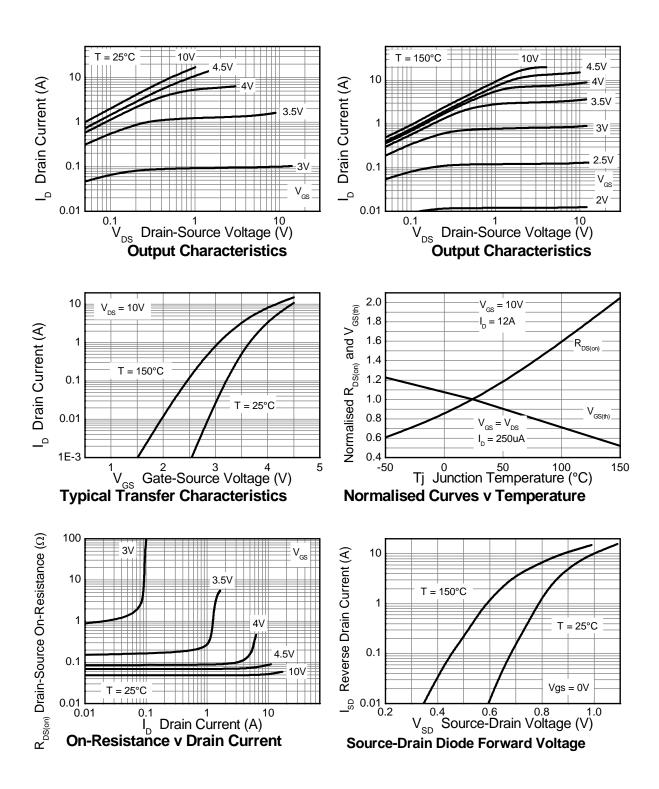
Characteristic	Symbol	Min	Тур	Max	Unit	Test Co	ondition	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60			V	$I_D = 250 \mu A, V_{GS}$	= 0V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		0.5	μΑ	$V_{DS}$ = 60V, $V_{GS}$ =	0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> :	= 0V	
ON CHARACTERISTICS								
Gate Threshold Voltage	$V_{GS(th)}$	1.0		3.0	V	$I_{D}$ = 250 $\mu$ A, $V_{DS}$ =	$V_{GS}$	
Static Drain Source On Decistones (Note 7)			0.048	0.066	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.	5A	
Static Drain-Source On-Resistance (Note 7)	R <sub>DS</sub> (ON)	_	0.068	0.097	77	$V_{GS} = 4.5V, I_{D} = 3$	.5A	
Forward Transconductance (Notes 7 & 8)	9 <sub>fs</sub>	_	19.2	_	S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 6/	A	
Diode Forward Voltage (Note 7)	$V_{SD}$	_	0.89	1.15	V	I <sub>S</sub> = 4.5A, V <sub>GS</sub> = 0V		
Reverse recovery time (Note 8)	t <sub>rr</sub>		23	_	ns	I <sub>S</sub> = 2.4A, di/dt= 100A/μs		
Reverse recovery charge (Note 8)	$Q_{rr}$	_	19.7	_	nC	$I_S = 2.4A$ , $\alpha I/\alpha t = 1$	υυΑ/μ\$	
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	C <sub>iss</sub>	_	502	_	рF	.,	0.7	
Output Capacitance	Coss	_	45.7		pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = f= 1MHz	UV	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	27.1	_	pF	TIE TIVILIZ		
Total Gate Charge (Note 9)	Qg	_	5.4	_	nC	V <sub>GS</sub> = 4.5V		
Total Gate Charge (Note 9)	$Q_g$	_	10.3	_	nC		$V_{DS} = 30V$	
Gate-Source Charge (Note 9)	Q <sub>qs</sub>	_	1.7	_	nC	V <sub>GS</sub> = 10V	$I_{D} = 4.5A$	
Gate-Drain Charge (Note 9)	Q <sub>gd</sub>	_	3.2	_	nC			
Turn-On Delay Time (Note 9)	t <sub>D(on)</sub>	_	2.7	_	ns	·		
Turn-On Rise Time (Note 9)	t <sub>r</sub>	_	2.4	_	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V		
Turn-Off Delay Time (Note 9)	t <sub>D(off)</sub>	_	14.7	_	ns	$I_{D}=1A, R_{G} \cong 6.0\Omega$		
Turn-Off Fall Time (Note 9)	t <sub>f</sub>	_	5.4	_	ns			

Notes:

- Measured under pulsed conditions. Pulse width ≤ 300μs; duty cycle ≤ 2%
   For design aid only, not subject to production testing.
   Switching characteristics are independent of operating junction temperatures.

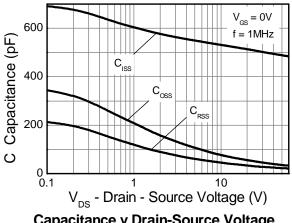


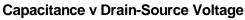
# **Typical Characteristics**

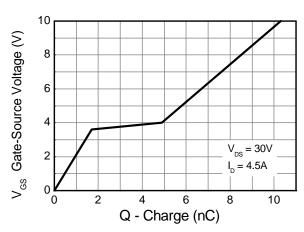




## **Typical Characteristics - continued**

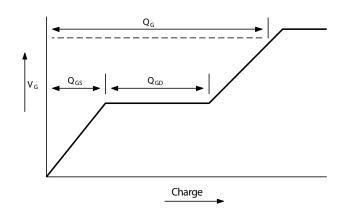




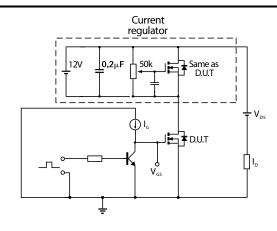


Gate-Source Voltage v Gate Charge

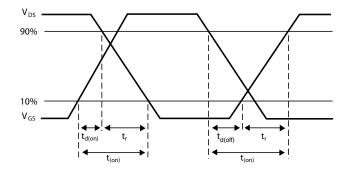
## **Test Circuits**



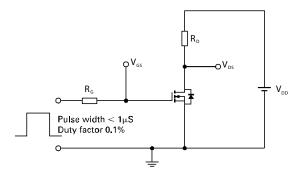
Basic gate charge waveform



Gate charge test circuit



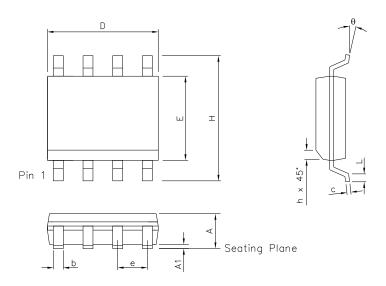
Switching time waveforms



Switching time test circuit

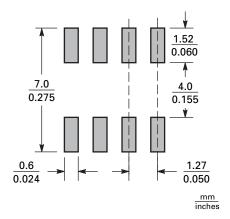


# **Package Outline Dimensions**



DIM	Inc	Inches N		Millimeters		Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
Α	0.053	0.069	1.35	1.75	е	0.050 BSC		1.27 BSC	
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	С	0.008	0.010	0.19	0.25
Н	0.228	0.244	5.80	6.20	θ	0°	8°	0°	8°
Е	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-	-	-

# **Suggested Pad Layout**







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