



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	I _D Τ _A = 25°C
	$32m\Omega@V_{GS} = -4.5V$	-5.5A
-12V	$45m\Omega@V_{GS} = -2.5V$	-4.5A
	$75m\Omega@V_{GS} = -1.8V$	-3.2A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power management functions
- Analog Switch

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 3kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

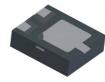
Mechanical Data

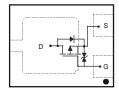
- Case: X2-DFN2015-3
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable
- per MIL-STD-202, Method 208 @4
- Weight: 0.008 grams (approximate)

X2-DFN2015-3









Top View Bottom View

Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging		
DMP1045UFY4-7	X2-DFN2015-3	3,000/Tape & Reel		
DMP1045UFY4-13	X2-DFN2015-3	10,000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information

15P YM 15P = Marking Code YM = Date Code Marking Y = Year (ex: W = 2009) M = Month (ex: 9 = September)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-12	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current // - 45// (Note 6)	Steady State	$T_A = +25$ °C $T_A = +70$ °C	L	-5.5 -4.3	А
Continuous Drain Current V _{GS} = -4.5V (Note 6)	t<5s	$T_A = +25$ °C $T_A = +70$ °C	lD	-6.5 -5.1	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	-2.2	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	-25	А

Thermal Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Dawer Dissipation (Note 5)	T _A = +25°C	Б	0.7	W	
Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P _D	0.4		
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	R _{eJA}	193	°C/W	
Thermal Resistance, Junction to Ambient (Note 3)	t<5s	Мөда	135		
Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	В	1.7	W	
Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	P _D	1.1		
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	D	73	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	$R_{ heta JA}$	52		
Thermal Resistance, Junction to Case (Notes 6)	stance, Junction to Case (Notes 6) Steady state		17		
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C		

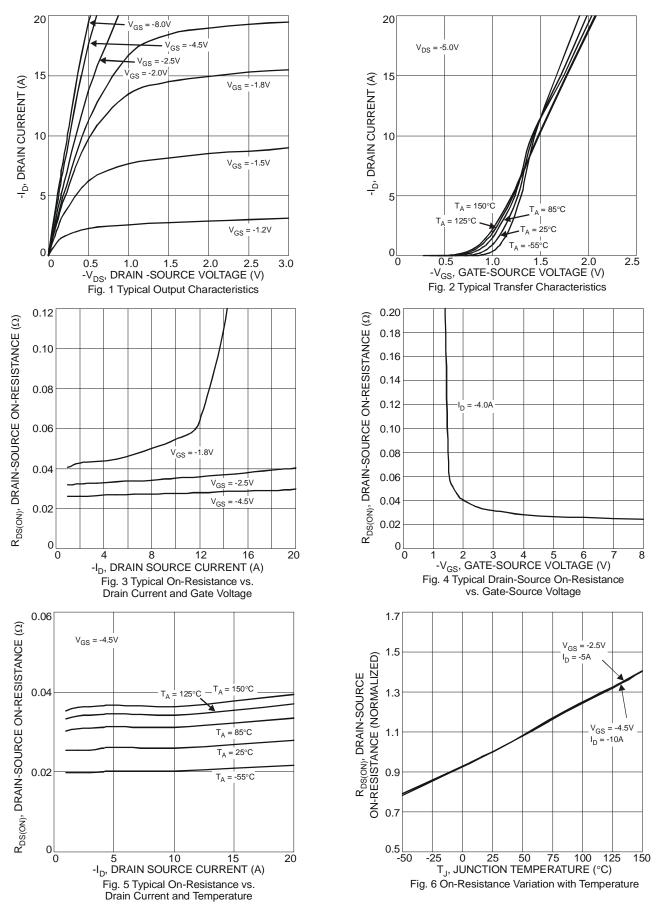
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-12	-	-	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	-1.0	μΑ	$V_{DS} = -12V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±10	μΑ	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	-0.3	-0.55	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			26	32		$V_{GS} = -4.5V$, $I_{D} = -4.0A$	
Static Drain-Source On-Resistance	R _{DS (ON)}	-	31	45	mΩ	$V_{GS} = -2.5V$, $I_D = -3.5A$	
			51	75		$V_{GS} = -1.8V$, $I_{D} = -2.7A$	
Forward Transfer Admittance	Y _{fs}	-	12	-	S	$V_{DS} = -5V, I_{D} = -4A$	
Diode Forward Voltage	V_{SD}	-	-0.6	-	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	-	1291	-	pF	101/11/	
Output Capacitance	Coss	-	266	-	pF	$V_{DS} = -10V, V_{GS} = 0V$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	-	242	-	pF	T = 1.0WHZ	
Gate Resistnace	Rg	-	13	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
SWITCHING CHARACTERISTICS (Note 8)							
Total Gate Charge (V _{GS} = -8V)	Qg	-	23.7	-	nC		
Total Gate Charge (V _{GS} = -4.5V)	Qg	-	14.7		nC	$V_{DS} = -10V, I_D = -4A$	
Gate-Source Charge	Q _{gs}	-	1.8	-	nC		
Gate-Drain Charge	Q _{gd}	-	4.6	-	nC		
Turn-On Delay Time	t _{D(on)}	-	14	-	ns		
Turn-On Rise Time	t _r	-	22	-	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$	
Turn-Off Delay Time	t _{D(off)}	-	74	-	ns	$R_L = 2.5\Omega$, $R_G = 3.0\Omega$	
Turn-Off Fall Time	t _f	-	75	-	ns		

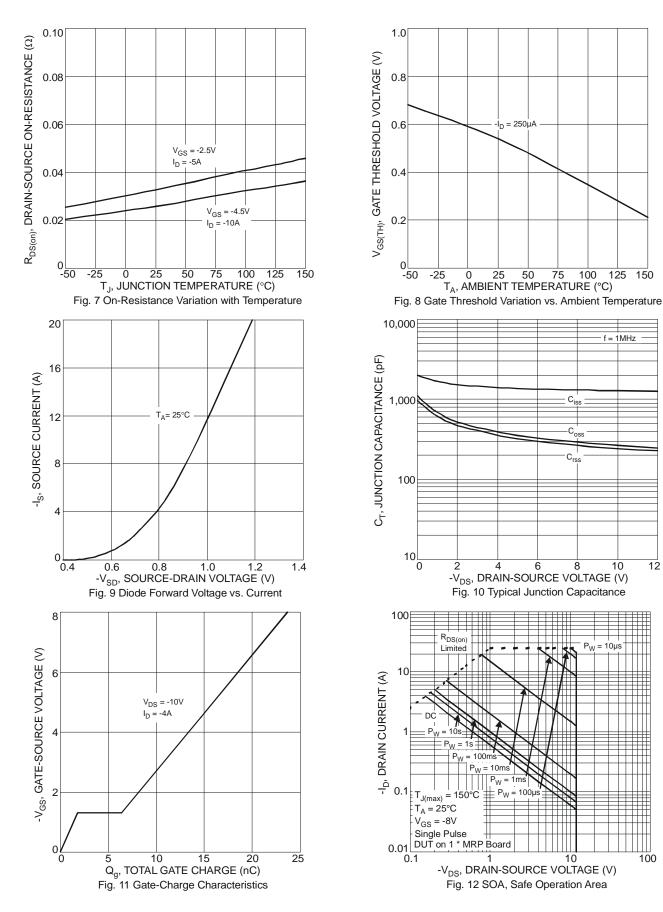
Notes:

- 5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
- 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.

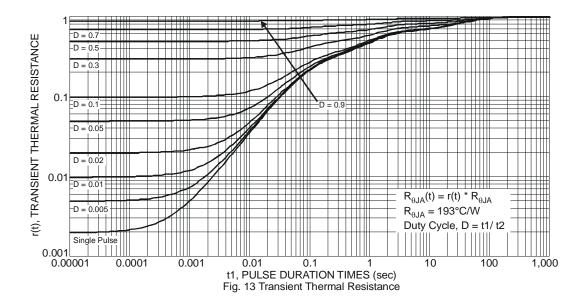






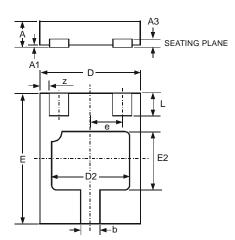






Package Outline Dimensions

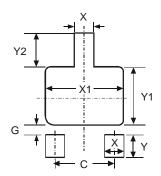
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



X2-DFN2015-3					
Dim	Min	Max	Тур		
Α	1	0.40	_		
A1	0	0.05	0.02		
А3	1	_	0.13		
b	0.20	0.30	0.25		
D	1.45	1.575	1.50		
D2	1.00	1.20	1.10		
е	1	_	0.50		
Е	1.95	2.075	2.00		
E2	0.70	0.90	0.80		
L	0.25	0.35	0.30		
z	1	_	0.125		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	1.00
G	0.15
Х	0.31
X1	1.30
Y	0.50
Y1	1.00
Y2	0.65



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