



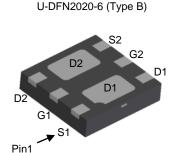
Product Summary

BV _{DSS}	Rds(on) max	I _D max T _A = +25°C
221/	50mΩ @V _{GS} = -4.5V	-4.5A
-20V	100mΩ @V _{GS} = -2.5V	-3.2A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors



Bottom View

DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

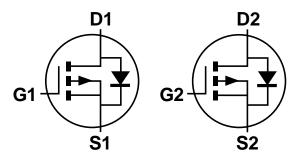
https://www.diodes.com/products/automotive/automotiveproducts/.

This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/guality/product-definitions/

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208@4
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)



Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2065UFDB-7	U-DFN2020-6 (Type B)	3,000/Tape & Reel
DMP2065UFDB-13	U-DFN2020-6 (Type B)	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



P5 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	D		Н		J	К	L	М	Ν	0	Р	R
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



P5 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	6		0	1	2	3	4	5	6	7	8	9
Week		1-26			27-52				53			
Code		A	λ-Ζ			a	-Z			2	2	
Internal Code	Sun	n 🛛	Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т		U		V	V	V	Х		Y		Z



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		Vdss	-20	V	
Gate-Source Voltage		V _{GSS}	±12	V	
Continuous Drain Current (Note 6) $V_{GS} = -4.5V$	T _A = +25°C T _A = +85°C	ID	-4.5 -3.6	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		•	Ідм	-25	A
Maximum Continuous Body Diode Forward Current (No		ls	-1.4	A	
Avalanche Current (Note 7) L = 0.1mH		las	-13	A	
Avalanche Energy (Note 7) L = 0.1mH			Eas	9	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	PD	0.74	W	
Thermal Desistance, Junction to Ambient (Note E)	Steady State	Devi	171	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	Reja	131		
Total Power Dissipation (Note 6)	T _A = +25°C	PD	1.54	W	
Thermal Desistance, Junction to Ambient (Note 6)	Steady State	Devi	82		
Thermal Resistance, Junction to Ambient (Note 6)	Reja	60	°C/W		
Thermal Resistance, Junction to Case (Note 6)	Rejc	13			
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Turn	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)	Symbol	IVIIII	Тур	IVIAX	Unit	Test condition
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS			-1.0	μÂ	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	IGSS			±100	nA	$V_{\rm DS} = \pm 20^{\circ}, V_{\rm DS} = 0^{\circ}$ $V_{\rm GS} = \pm 8^{\circ}, V_{\rm DS} = 0^{\circ}$
ON CHARACTERISTICS (Note 8)	IGSS			100	ПА	$VGS = \pm 0V, VDS = 0V$
Gate Threshold Voltage	VGS(TH)	-0.4	_	-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$
5	100(11)		40	50		$V_{GS} = -4.5V, I_D = -2.0A$
Ctatia Ducia Courses On Desistance		_	55	100		Vgs = -2.5V, ID = -2.0A
Static Drain-Source On-Resistance	RDS(ON)		75	150	mΩ	V _{GS} = -1.8V, I _D = -1.6A
			95	200		V _{GS} = -1.5V, I _D = -1.0A
Diode Forward Voltage	Vsd	_	-0.75	-1.1	V	$V_{GS} = 0V$, $I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 9)						·
Input Capacitance	Ciss	_	752	_	pF	
Output Capacitance	Coss	_	87	—	pF	V _{DS} = -15V, V _{GS} = 0V - f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	78	_	pF	
Gate Resistance	Rg	—	15.2	—	Ω	$V_{GS} = 0V$, $V_{DS} = 0V$, $f = 1.0MHz$
Total Gate Charge	Qg	_	9.1	_	nC	
Gate-Source Charge	Qgs	—	1.2	—	nC	$V_{GS} = -4.5V, V_{DS} = -4V,$
Gate-Drain Charge	Qgd	_	1.9	—	nC	ID = -3.5A
Turn-On Delay Time	tD(ON)	_	5.4	_	ns	
Turn-On Rise Time	tR	_	8.3	_	ns	$V_{DS} = -4V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	_	47	—	ns	$R_G = 6\Omega, I_D = -1A$
Turn-Off Fall Time	tF	_	20	—	ns	

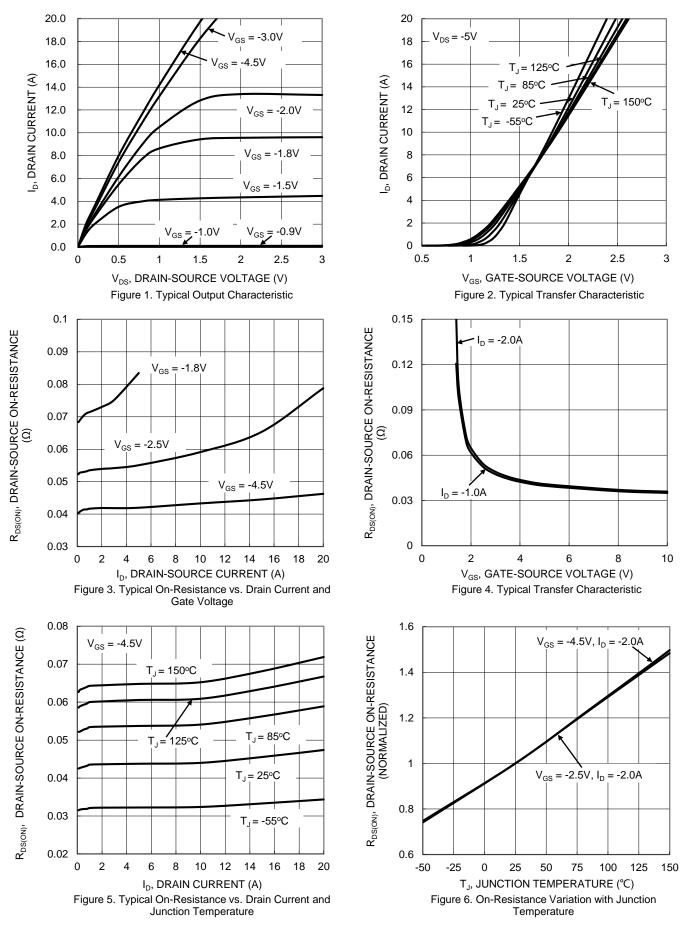
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. Notes:

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_{J} = +25^{\circ}C$. 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.



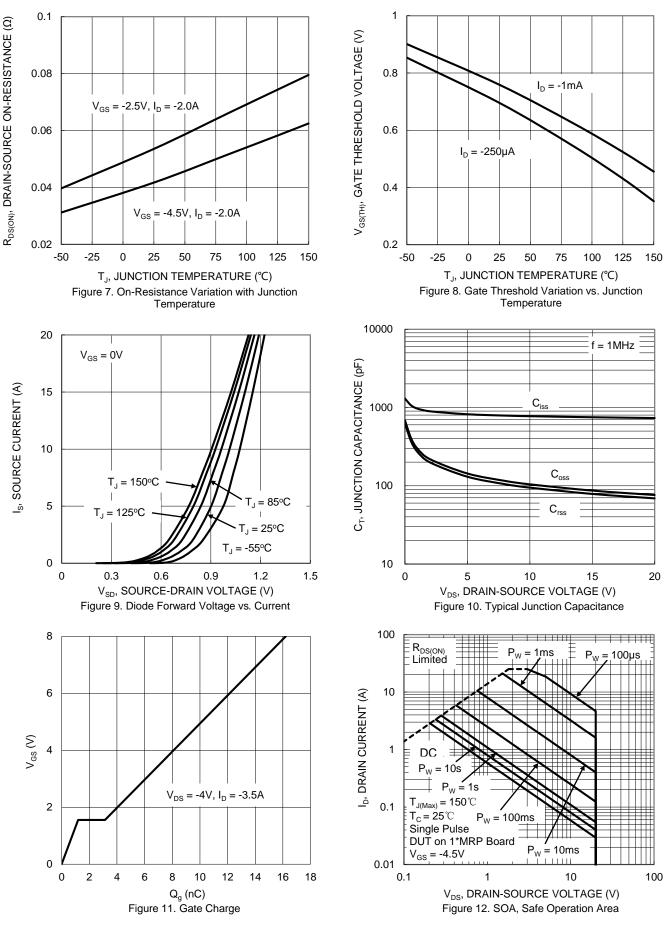
DMP2065UFDB



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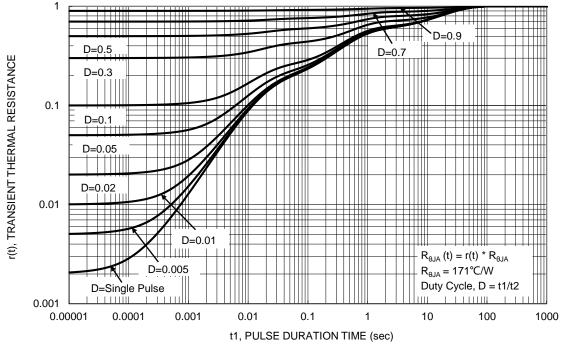
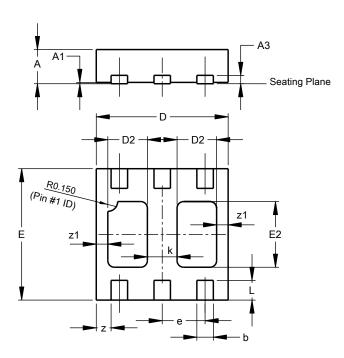


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

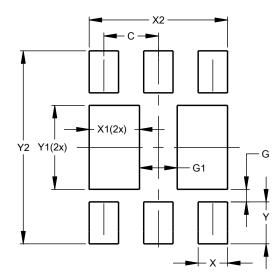


		2020-6 e B					
Dim	Min Max Typ						
Α	0.545	0.605	0.575				
A1	0.00	0.05	0.02				
A3	-	-	0.13				
b	0.20	0.30	0.25				
D	1.95	2.075	2.00				
D2	0.50	0.70	0.60				
е	-	-	0.65				
Е	1.95	2.075	2.00				
E2	0.90	1.10	1.00				
k	-	-	0.45				
L	0.25	0.35	0.30				
z	-	-	0.225				
z1	-	-	0.175				
All	Dimens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value (in mm)
С	0.650
G	0.150
G1	0.450
Х	0.350
X1	0.600
X2	1.650
Ý	0.500
Y1	1.000
Y2	2.300



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