



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	6.0mΩ @ V _{GS} = 10V	14.1A
24V	7.2mΩ @ V _{GS} = 4.5V	12.9A
	12.5mΩ @ V _{GS} = 2.5V	9.8A

Description

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery Management Application
- Power Management Functions
- DC-DC Converters

Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part.
 A listing can be found at

 $\underline{\text{https://www.diodes.com/products/automotive/automotive-products/.}}$

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

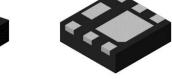
https://www.diodes.com/quality/product-definitions/

Mechanical Data

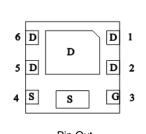
- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- · Weight: 0.0065 grams (Approximate)

U-DFN2020-6 (Type F)

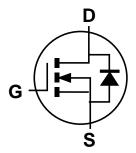




Bottom View



Pin Out Bottom View



Internal Schematic

Ordering Information (Note 4)

Top View

Part Number	Case	Reel Size (inches)	Quantity per Reel
DMT2004UFDF-7	U-DFN2020-6 (Type F)	7	3,000
DMT2004UFDF-13	U-DFN2020-6 (Type F)	13	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



4M = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	D		Н	ı	J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Site 2



4M = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = week 27; z represents week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

	Year	2016	 2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
	Code	6	 0	1	2	3	4	5	6	7	8	9
_												

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	X	Y	Z



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			VDSS	24	V
Gate-Source Voltage	V_{GSS}	±12	V		
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	l _D	14.1 11.2	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	70	Α
Continuous Source-Drain Diode Current (Note 6)	Is	2	Α		
Avalanche Current (Note 7) L = 0.1mH	alanche Current (Note 7) L = 0.1mH		las	26	Α
Avalanche Energy (Note 7) L = 0.1mH			Eas	36	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	149	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	1.8	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	70	°C/W
Total Power Dissipation (Note 6)	Tc = +25°C	PD	12.5	W
Thermal Resistance, Junction to Case (Note 6)	Steady State	Rелс	12	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

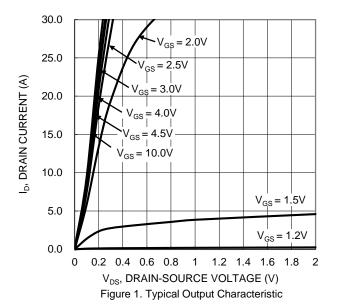
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						•
Drain-Source Breakdown Voltage	BV _{DSS}	24	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current (T _J = +25°C)	I _{DSS}	_	_	1	μΑ	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	lgss	_	_	±100	nA	$V_{GS} = \pm 10V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)	•					•
Gate Threshold Voltage	V _{GS(TH)}	0.55	_	1.45	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
		_	4.8	6		$V_{GS} = 10V, I_D = 9A$
Static Drain-Source On-Resistance	RDS(ON)	_	5.8	7.2	mΩ	$V_{GS} = 4.5V, I_{D} = 8A$
		_	9.6	12.5		V _G S = 2.5V, I _D = 5A
Diode Forward Voltage	Vsp	_	0.65	1.0	V	V _G S = 0V, I _S = 2A
DYNAMIC CHARACTERISTICS (Note 9)						•
Input Capacitance	Ciss	_	1683	_		4514.14
Output Capacitance	Coss	_	581	_	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	559	_		1 = 1.0WH2
Gate Resistance	RG	_	1.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	29.6	_		
Total Gate Charge (V _{GS} = 10V)	Qg	_	53.7	_		151/ 1 00
Gate-Source Charge	Qgs	_	4.2	_	nC	$V_{DD} = 15V$, $I_D = 9A$
Gate-Drain Charge	Q _{qd}	_	13.4	_		
Turn-On Delay Time	t _D (ON)	_	3.9	_		
Turn-On Rise Time	t _R	_	9.6	_		V _{DD} = 15V, V _{GS} = 10V,
Turn-Off Delay Time	t _{D(OFF)}	_	30.8	_	ns	$R_G = 3\Omega$, $I_D = 9A$
Turn-Off Fall Time	tF	_	38.6	_	1	
Reverse Recovery Time	trr	_	11.2	_	ns	1. 1.54 11/1/ 1004/
Reverse Recovery Charge	QRR	_	22.9	_	nC	$I_F = 1.5A$, di/dt = 100A/ μ s

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.





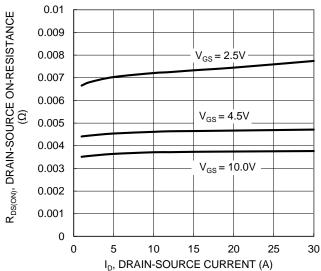


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

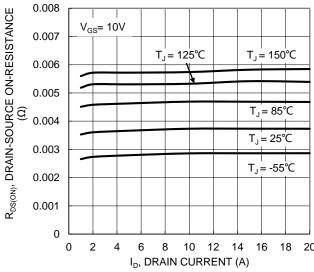


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

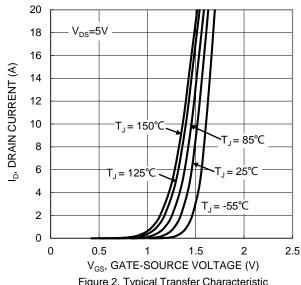
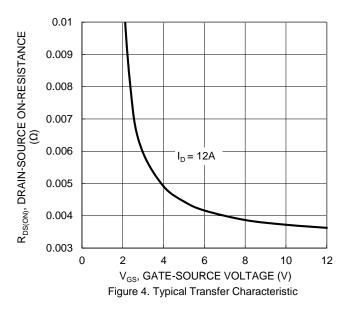


Figure 2. Typical Transfer Characteristic

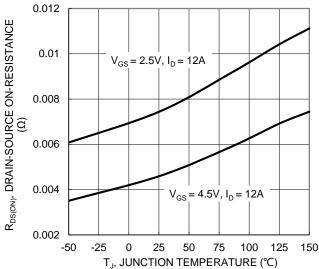


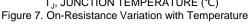
1.8 R_{DS(ON}, DRAIN-SOURCE ON-RESISTANCE (NORMALIZED) 1.6 $V_{GS} = 4.5V, I_D = 12A$ 1.4 1.2 $V_{GS} = 2.5V, I_D = 12A$ 1 0.8 0.6 -50 25 50 75 100 125 150 -25 T_.I, JUNCTION TEMPERATURE (°C)

Figure 6. On-Resistance Variation with Temperature









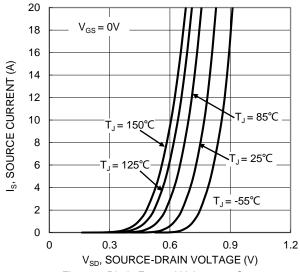


Figure 9. Diode Forward Voltage vs. Current

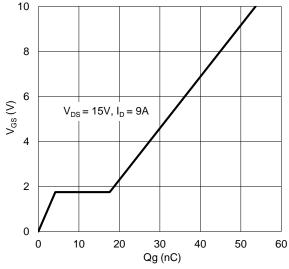


Figure 11. Gate Charge

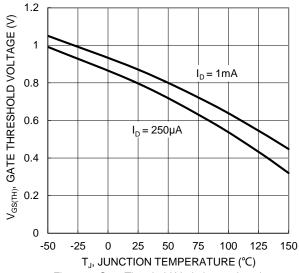
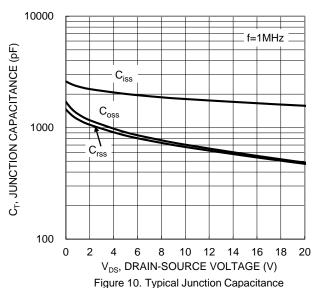


Figure 8. Gate Threshold Variation vs.Junction Temperature



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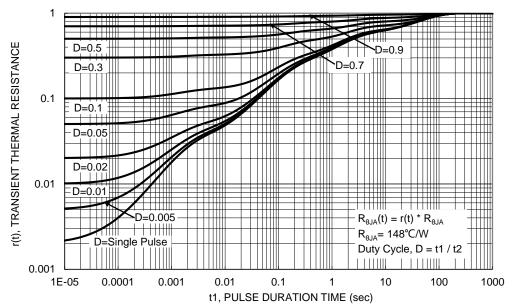


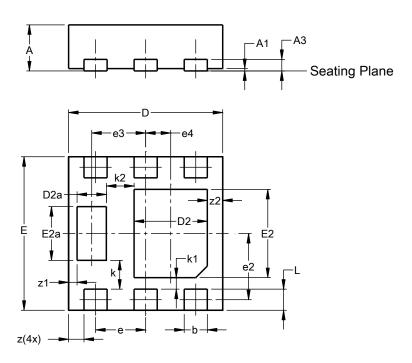
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

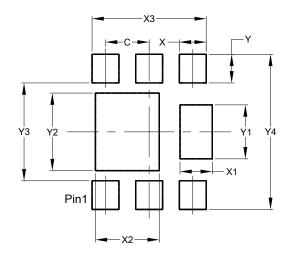


	U-DFN2020-6						
(Type F) Dim Min Max Typ							
			Тур				
Α	0.57	0.63	0.60				
A1	0.00	0.05	0.03				
A3	-	-	0.15				
b	0.25	0.35	0.30				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
D2a	0.33	0.43	0.38				
E	1.95	2.05	2.00				
E2	1.05	1.25	1.15				
E2a	0.65	0.75	0.70				
е		0.65 BS	С				
e2	().863 BS	SC				
е3		0.70 BS	С				
e4	().325 BS	SC				
k		0.37 BS	С				
k1		0.15 BS	C				
k2		0.36 BS	С				
L	0.225	0.325	0.275				
Z	0.20 BSC						
z 1	0.110 BSC						
z2		0.20 BS	С				
All C	imens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
Χ	0.400
X1	0.480
X2	0.950
Х3	1.700
Υ	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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