

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _c = +25°C
80V	2.5mΩ @ V _{GS} = 10V	173A

Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)} – Minimizes On State Losses
- Wettable Flank for Improved Optical Inspection
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **The DIODES™ DMTH8003STLWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

Description and Applications

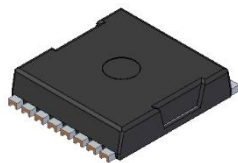
This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor controls
- DC-DC converters
- Power managements

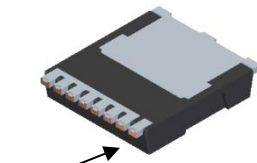
Mechanical Data

- Package: POWERDI[®]1012-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.388 grams (Approximate)

POWERDI1012-8

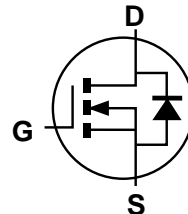


Top View

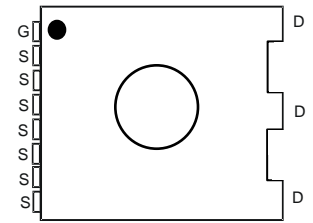


Pin1

Bottom View



Internal Schematic



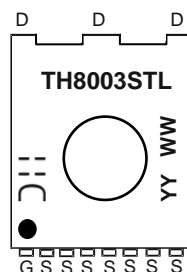
Top View
Pin Configuration

Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMTH8003STLWQ-13	POWERDI1012-8	1500	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



)|; = Manufacturer's Marking
 TH8003STL = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 22 = 2022)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	80	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	I _D	T _C = +25°C	173
		T _C = +100°C	122
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	692	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	692	A
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	173	A
Avalanche Current, L = 0.3mH	I _{AS}	73	A
Avalanche Energy, L = 0.3mH	E _{AS}	800	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	5.6	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	27	°C/W
Total Power Dissipation (Note 6)	P _D	150	W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	1	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	80	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	µA	V _{DS} = 64V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	2	—	4	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	1.42	2.5	mΩ	V _{GS} = 10V, I _D = 30A
Diode Forward Voltage	V _{SD}	—	0.8	1.2	V	V _{GS} = 0V, I _S = 30A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	8191	—	pF	V _{DS} = 40V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	2905	—		
Reverse Transfer Capacitance	C _{rss}	—	120	—		
Gate Resistance	R _g	—	1.1	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	124	—	nC	V _{DS} = 40V, I _D = 30A, V _{GS} = 10V
Gate-Source Charge	Q _{gs}	—	37	—		
Gate-Drain Charge	Q _{gd}	—	32	—		
Turn-On Delay Time	t _{D(ON)}	—	33	—	ns	V _{DD} = 40V, V _{GS} = 10V, I _D = 30A, R _G = 6Ω
Turn-On Rise Time	t _r	—	45	—		
Turn-Off Delay Time	t _{D(OFF)}	—	80	—		
Turn-Off Fall Time	t _f	—	55	—		
Body Diode Reverse Recovery Time	t _{RR}	—	99	—	ns	I _S = 25A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{RR}	—	243	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

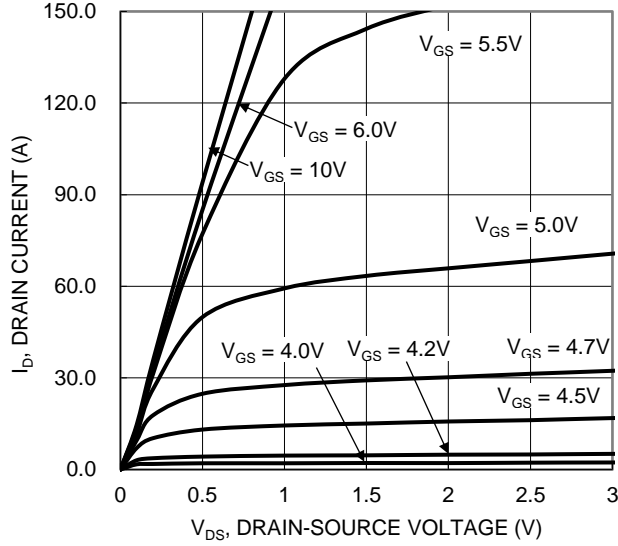


Figure 1. Typical Output Characteristic

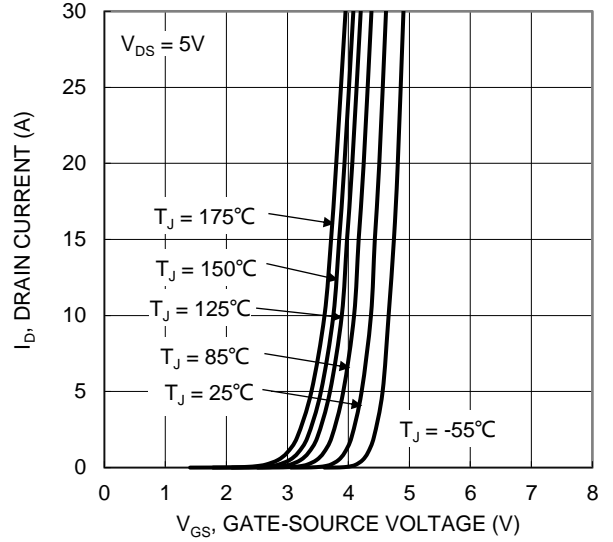


Figure 2. Typical Transfer Characteristic

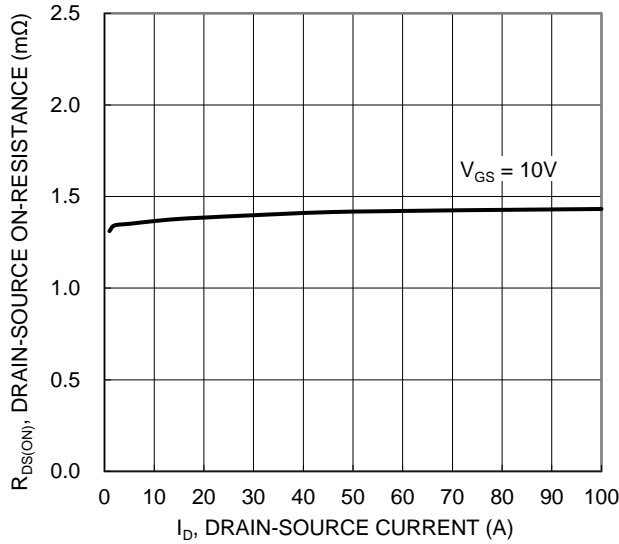


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

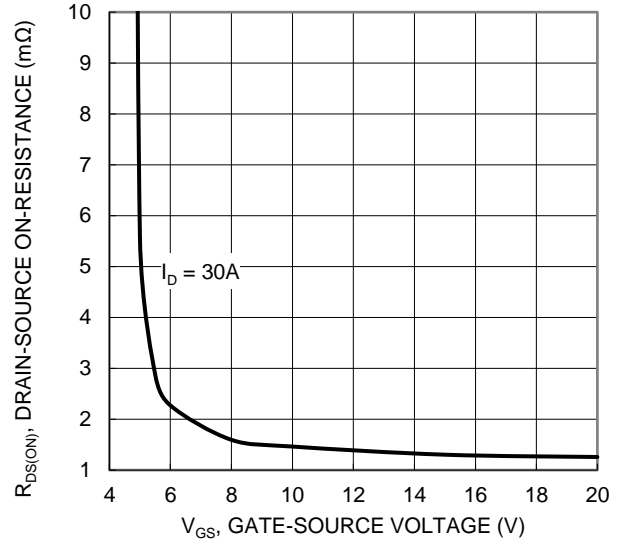


Figure 4. Typical Transfer Characteristic

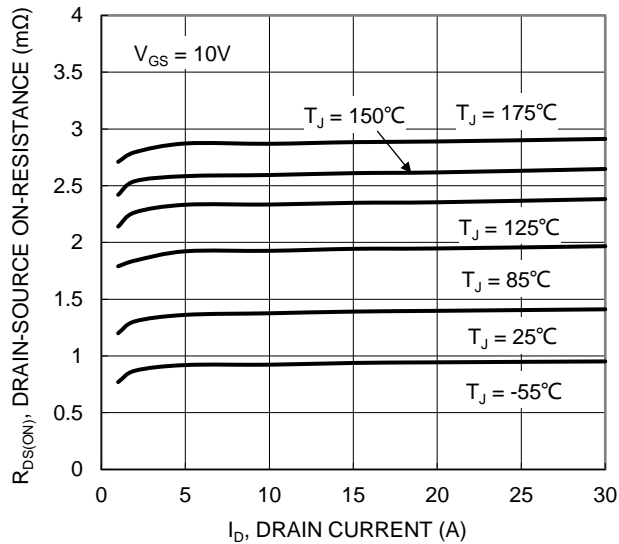


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

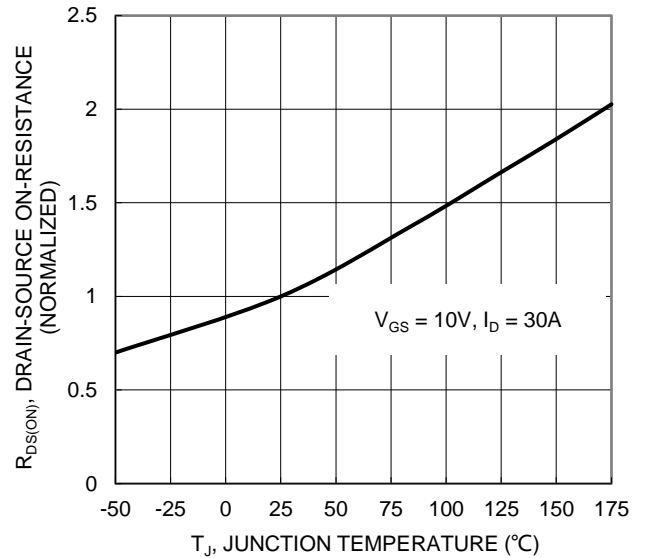


Figure 6. On-Resistance Variation with Temperature

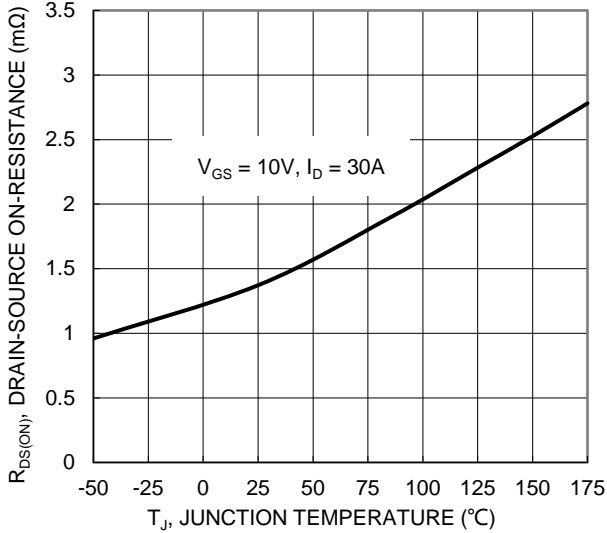


Figure 7. On-Resistance Variation with Temperature

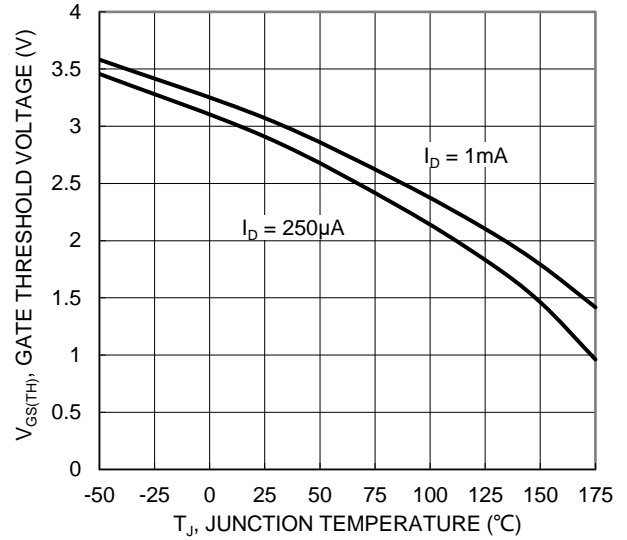


Figure 8. Gate Threshold Variation vs. Junction Temperature

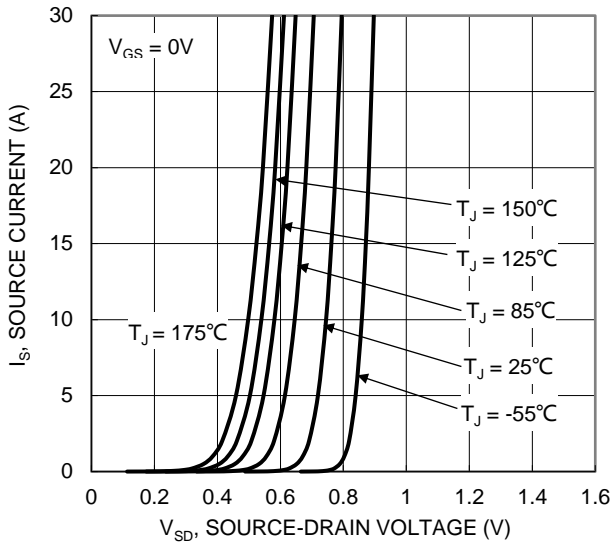


Figure 9. Diode Forward Voltage vs. Current

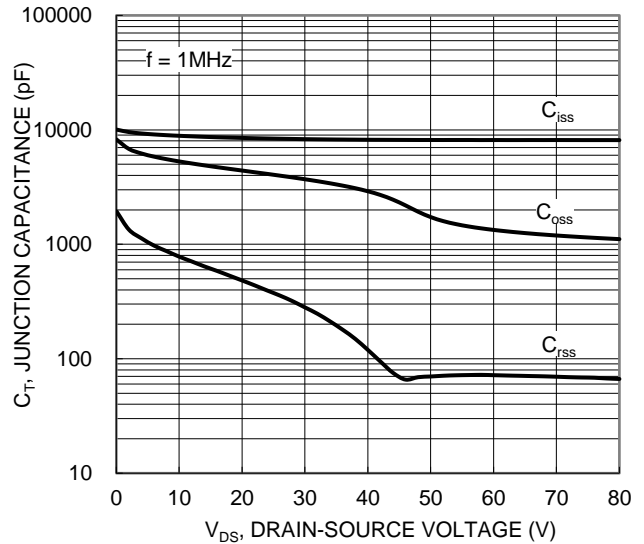


Figure 10. Typical Junction Capacitance

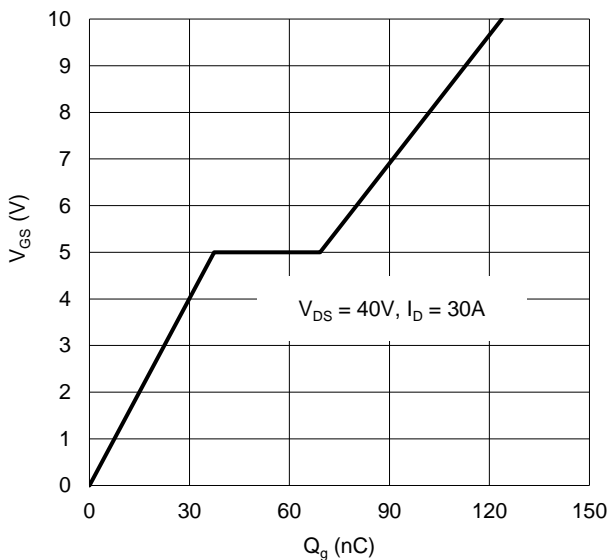


Figure 11. Gate Charge

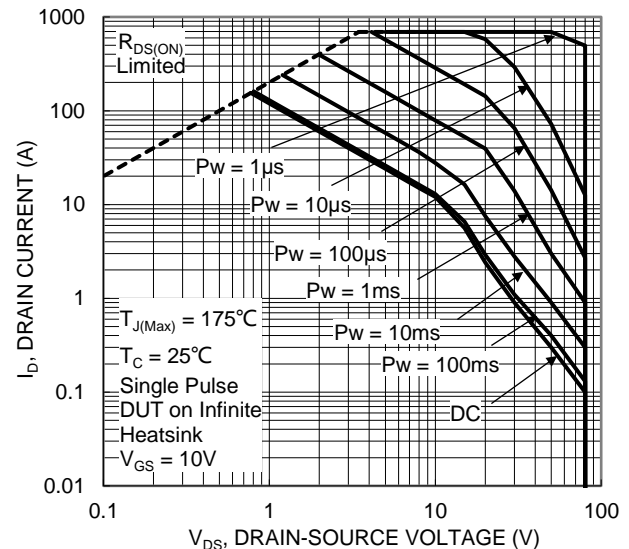


Figure 12. SOA, Safe Operation Area

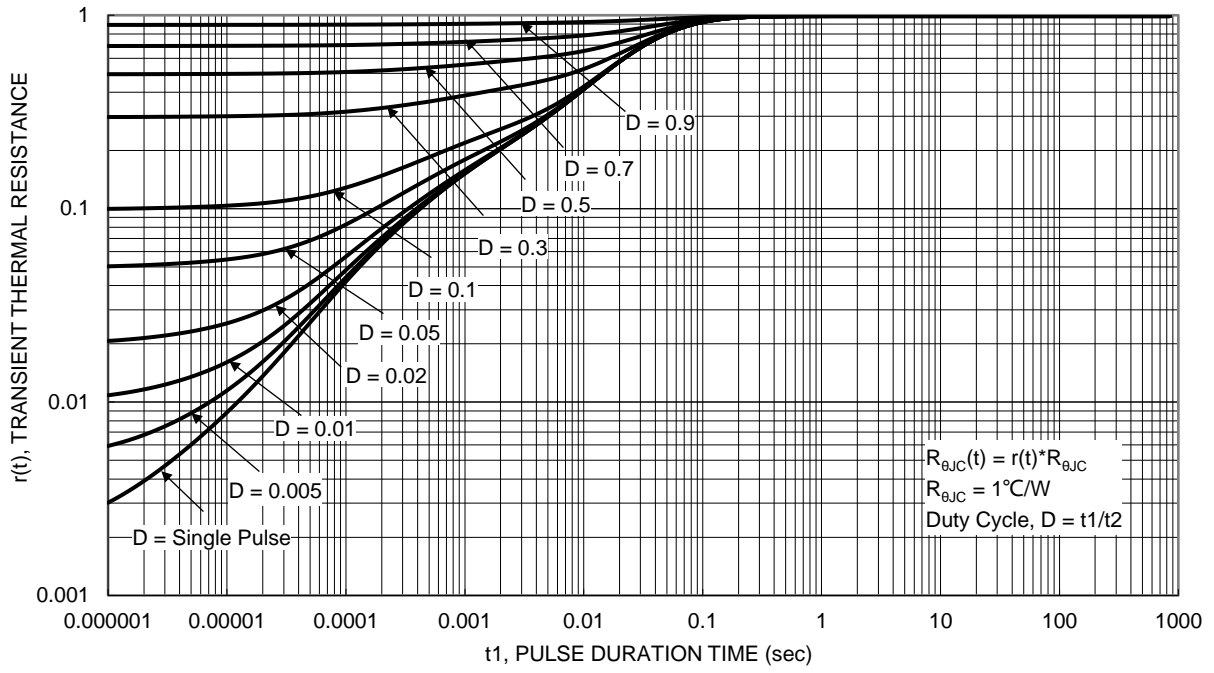


Figure 13. Transient Thermal Resistance

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