

# DN74LS42

## BCD to Decimal Decoders

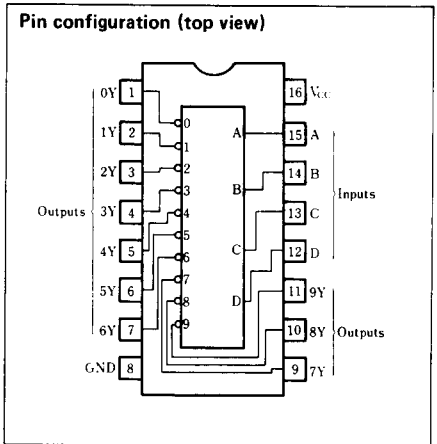
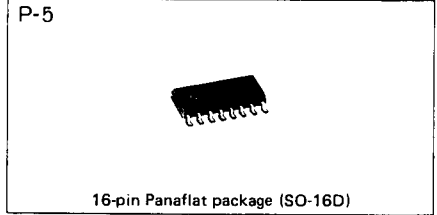
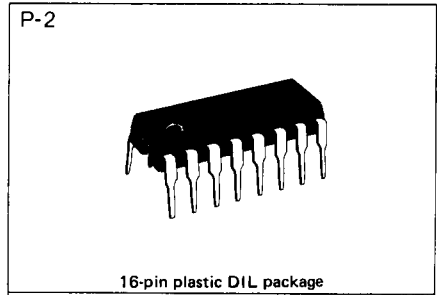
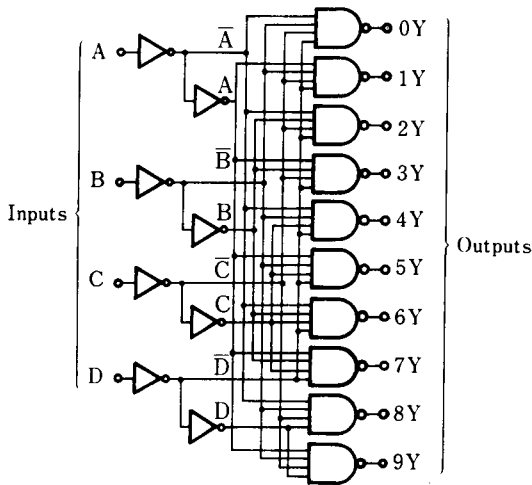
### Description

DN74LS42 is a binary-coded decimal to decimal decoder.

### Features

- During invalid input, all outputs become HIGH
- Also can be used as 3-bit binary to octal decoder
- Wide operating temperature range ( $T_a = -20$  to  $+75^\circ\text{C}$ )

### Logic diagram



### Recommended operating conditions

Parameter	Sym	Min	Typ	Max	Unit
Supply voltage	$V_{cc}$	4.75	5.00	5.25	V
Output current	$I_{OH}$			-400	$\mu\text{A}$
	$I_{OL}$			8	mA
Operating temperature range	$T_{opr}$	-20	25	75	$^\circ\text{C}$

■ DC characteristics (Ta = -20 ~ +75°C)

Parameter	Sym	Test conditions	Min	Typ*	Max	Unit
Input voltage	V <sub>IH</sub>		2.0			V
	V <sub>IL</sub>				0.8	V
Output voltage	V <sub>OH</sub>	V <sub>CC</sub> = 4.75V, V <sub>IH</sub> = 2V, V <sub>IL</sub> = 0.8V, I <sub>OH</sub> = -400 μA	2.7	3.4		V
	V <sub>OL1</sub>	V <sub>CC</sub> = 4.75V, V <sub>IH</sub> = 2V, I <sub>OL</sub> = 4mA		0.25	0.4	V
	V <sub>OL2</sub>	V <sub>CC</sub> = 4.75V, V <sub>IH</sub> = 2V, V <sub>IL</sub> = 0.8V, I <sub>OL</sub> = 8mA		0.35	0.5	V
Input current	I <sub>IH</sub>	V <sub>CC</sub> = 5.25V, V <sub>I</sub> = 2.7V			20	μA
	I <sub>IL</sub>	V <sub>CC</sub> = 5.25V, V <sub>I</sub> = 0.4V			-0.4	mA
	I <sub>I</sub>	V <sub>CC</sub> = 5.25V, V <sub>I</sub> = 7V			0.1	mA
Output short circuit current**	I <sub>OS</sub>	V <sub>CC</sub> = 5.25V, V <sub>O</sub> = 0V	-15		-100	mA
Input clamp voltage	V <sub>IK</sub>	V <sub>CC</sub> = 4.75V, I <sub>I</sub> = -18mA			-1.5	V
Supply current***	I <sub>CC</sub>	I <sub>CC</sub> = 5.25V		7	13	mA

\* When constant at V<sub>CC</sub> = 5V, Ta = 25°C.

\*\* Only one output at a time short circuited to GND. Also, short circuit time to GND within 1 second.

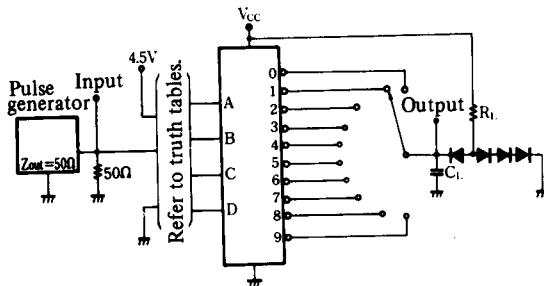
\*\*\* Measured with all outputs open and all inputs grounded.

■ Switching characteristics (V<sub>CC</sub> = 5V, Ta = 25°C)

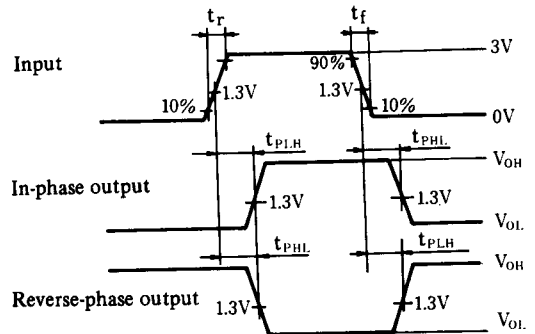
Parameter	Sym	Test conditions	Min	Typ	Max	Unit	
Propagation delay time	2 stage	C <sub>L</sub> = 15pF R <sub>L</sub> = 2kΩ			15	25	ns
	3 stage				20	30	ns
	2 stage				15	25	ns
	3 stage				20	30	ns

※ Switching parameter measurement information

1. Measurement circuit



2. Waveforms



Notes

- C<sub>L</sub> includes probe and tool floating capacitance.
- Diodes are all MA161 or equivalent.

Notes

- Input waveform: t<sub>r</sub> ≤ 15ns, t<sub>f</sub> ≤ 6ns, PRR = 1MHz, duty cycle = 50%.

■ Truth tables

NO.	BCD Inputs				Decimal Outputs									
	D	C	B	A	0	1	2	3	4	5	6	7	8	9
0	L	L	L	L	L	H	H	H	H	H	H	H	H	H
1	L	L	L	H	H	L	H	H	H	H	H	H	H	H
2	L	L	H	L	H	H	L	H	H	H	H	H	H	H
3	L	L	H	H	H	H	H	L	H	H	H	H	H	H
4	L	H	L	L	H	H	H	H	L	H	H	H	H	H
5	L	H	L	H	H	H	H	H	H	L	H	H	H	H
6	L	H	H	L	H	H	H	H	H	H	L	H	H	H
7	L	H	H	H	H	H	H	H	H	H	H	L	H	H
8	H	L	L	L	H	H	H	H	H	H	H	H	L	H
9	H	L	L	H	H	H	H	H	H	H	H	H	H	L
INVALID	H	L	H	L	H	H	H	H	H	H	H	H	H	H
	H	L	H	H	H	H	H	H	H	H	H	H	H	H
	H	H	L	L	H	H	H	H	H	H	H	H	H	H
	H	H	L	H	H	H	H	H	H	H	H	H	H	H
	H	H	H	L	H	H	H	H	H	H	H	H	H	H

## Notes

1. H: HIGH voltage level.
2. L: LOW voltage level.

