

DELPHI SERIES



FEATURES

- ◆ High Efficiency: 94%@ 5Vin, 3.3V/3A out
- ◆ Small size and low profile:
 - 0.80" x 0.45" x 0.27" (SMD)
 - 0.90" x 0.40" x 0.25" (SIP)
- ◆ Standard footprint and pinout
- ◆ Resistor-based trim
- ◆ Output voltage programmable from 0.75V to 3.3V via external resistors
- ◆ Pre-bias startup
- ◆ No minimum load required
- ◆ Fixed frequency operation
- ◆ Input UVLO, Output OTP, OCP
- ◆ Remote ON/OFF
- ◆ ISO 9001, TL 9000, ISO 14001, QS 9000, OHSAS 18001 certified manufacturing facility
- ◆ UL/cUL 60950 (US & Canada) Recognized, and TUV (EN60950)- pending
- ◆ CE mark meets 73/23/EEC and 93/68/EEC-pending

Delphi DNT04, Non-Isolated Point of Load DC/DC Power Modules: 2.4~5.5Vin, 0.75~3.3Vo, 3A out

The Delphi Series DNT04, 2.4-5.5V input, single output, non-isolated Point of Load DC/DC converters are the latest offering from a world leader in power systems technology and manufacturing — Delta Electronics, Inc. The DNT04 series provides a programmable output voltage from 0.75V to 3.3V using external resistors. This product family is available in surface mount or SIP package and provides up to 3A of output current in an industry standard footprint. With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions. The DNT04, 3A modules have excellent thermal performance and can provide full output current at up to 85°C ambient temperature with no airflow.

OPTIONS

- ◆ Negative On/Off logic
- ◆ SMD or SIP package

APPLICATIONS

- ◆ Telecom/DataCom
- ◆ Distributed power architectures
- ◆ Servers and workstations
- ◆ LAN/WAN applications
- ◆ Data processing applications

TECHNICAL SPECIFICATIONS

(T_A = 25°C, airflow rate = 300 LFM, V_{in} = 2.4Vdc and 5.5Vdc, nominal V_{out} unless otherwise noted.)

PARAMETER	NOTES AND CONDITIONS	DNT04S0A0S03NFA			
		Min.	Typ.	Max.	Units
ABSOLUTE MAXIMUM RATINGS					
Input Voltage (Continuous)		0		5.8	Vdc
Operating Temperature	Refer to Figure 34 for measuring point	-40		125	°C
Storage Temperature		-55		125	°C
INPUT CHARACTERISTICS					
Operating Input Voltage	V _o ≤ V _{in} - 0.5V	2.4		5.5	V
Input Under-Voltage Lockout					
Turn-On Voltage Threshold		1.95	2.05	2.15	V
Turn-Off Voltage Threshold		1.8	1.9	2.0	V
Maximum Input Current	V _{in} =4.5V V _o =3.3V, I _o =I _{o,max}			2.4	A
No-Load Input Current			15	25	mA
Off Converter Input Current			0.2	0.5	mA
Inrush Transient	V _{in} =2.4V to 5.5V, I _o =I _{o,min} to I _{o,max}			0.1	A ² S
Recommended Inout Fuse				TBD	A
OUTPUT CHARACTERISTICS					
Output Voltage Set Point	V _{in} =5V, I _o =I _{o,max}	-1.5	V _{o,set}	+1.5	% V _{o,set}
Output Voltage Adjustable Range		0.7525		3.63	V
Output Voltage Regulation					
Over Line	V _{in} =2.4V to 5.5V		0.2		% V _{o,set}
Over Load	I _o =I _{o,min} to I _{o,max}		0.2		% V _{o,set}
Over Temperature	T _a =-40°C to 85°C		0.4		% V _{o,set}
Total Output Voltage Range	Over sample load, line and temperature	-3.0		+3.0	% V _{o,set}
Output Voltage Ripple and Noise					
5Hz to 20MHz bandwidth					
Peak-to-Peak	Full Load, 1μF ceramic, 10μF tantalum		30	50	mV
RMS	Full Load, 1μF ceramic, 10μF tantalum		10	15	mV
Output Current Range		0		3	A
Output Voltage Over-shoot at Start-up					
Output DC Current-Limit Inception			200		% I _o
Output Short-Circuit Current (Hiccup Mode)	I _{o,s/c}		1.5		Adc (rms)
DYNAMIC CHARACTERISTICS					
Dynamic Load Response					
10μF Tan & 1μF Ceramic load cap, 2.5A/μs					
Positive Step Change in Output Current	50% I _{o,max} to 100% I _{o,max}		180		mV
Negative Step Change in Output Current	100% I _{o,max} to 50% I _{o,max}		180		mV
Setting Time to 10% of Peak Deviation			50		μs
Turn-On Transient					
I _o =I _{o,max}					
Start-Up Time, From On/Off Control	V _{on/off} , V _o =10% of V _{o,set}		7		ms
Start-Up Time, From Input	V _{in} =V _{in,min} , V _o =10% of V _{o,set}		7		ms
Maximum Output Startup Capacitive Load					
Full load; ESR ≥ 1mΩ					
Full load; ESR ≥ 10mΩ					
Full load; ESR ≥ 100mΩ					
Full load; ESR ≥ 1000mΩ					
EFFICIENCY					
V _o =3.3V	V _{in} =5V, 100% Load		94.0		%
V _o =2.5V	V _{in} =5V, 100% Load		92.0		%
V _o =1.8V	V _{in} =5V, 100% Load		90.5		%
V _o =1.5V	V _{in} =5V, 100% Load		89.0		%
V _o =1.2V	V _{in} =5V, 100% Load		86.5		%
V _o =0.75V	V _{in} =5V, 100% Load		80.0		%
FEATURE CHARACTERISTICS					
Switching Frequency					
		250	300	350	kHz
ON/OFF Control, (Negative logic)					
Logic Low Voltage	Module On, V _{on/off}	-0.2		0.3	V
Logic High Voltage	Module Off, V _{on/off}	2.5		V _{in,max}	V
Logic Low Current	Module On, I _{on/off}			10	μA
Logic High Current	Module Off, I _{on/off}		0.2	1	mA
GENERAL SPECIFICATIONS					
MTBF	I _o =100% of I _{o,max} ; T _a =25°C		TBD		M hours
Weight			2.3		grams



ELECTRICAL CHARACTERISTICS CURVES

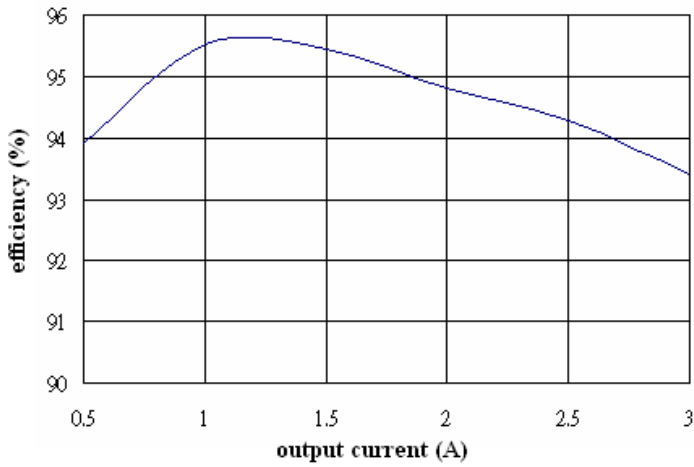


Figure 1: Converter efficiency vs. output current (5Vin/3.3Vout)

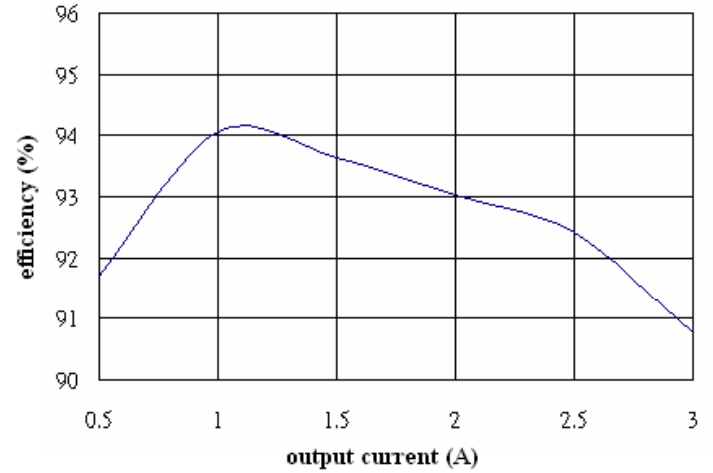


Figure 2: Converter efficiency vs. output current (5Vin/2.5Vout)

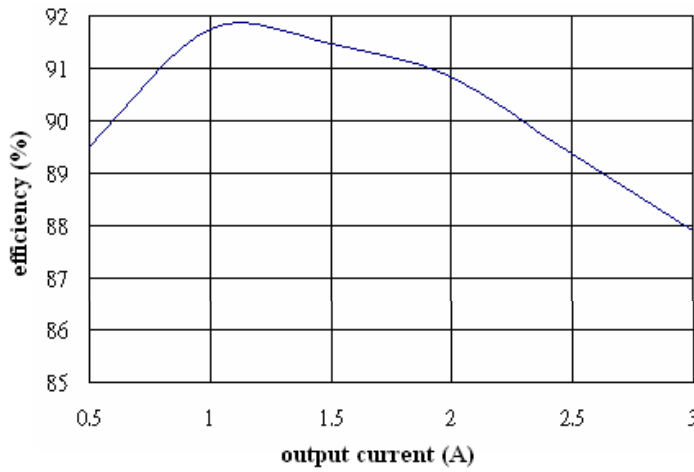


Figure 3: Converter efficiency vs. output current (5Vin/1.8Vout)

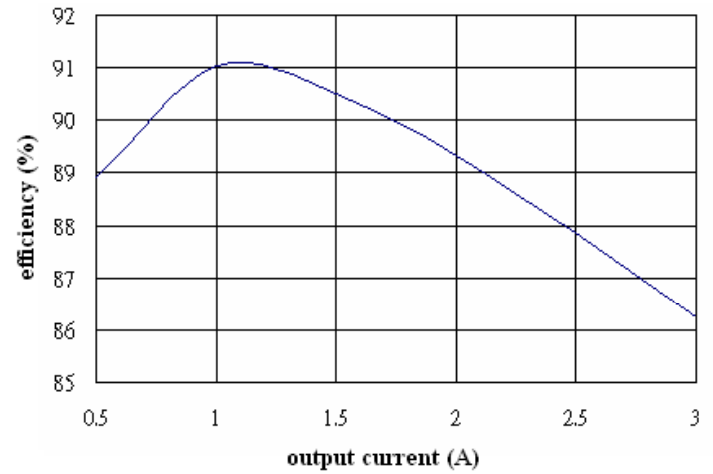


Figure 4: Converter efficiency vs. output current (5Vin/1.5Vout)

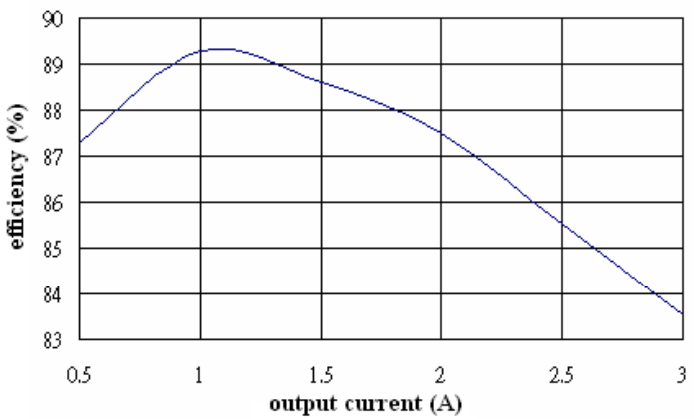


Figure 5: Converter efficiency vs. output current (5Vin/1.2Vout)

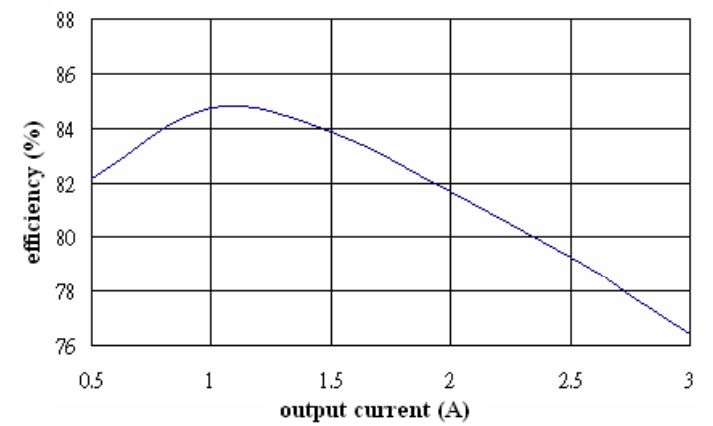


Figure 6: Converter efficiency vs. output current (5Vin/0.75Vout)



ELECTRICAL CHARACTERISTICS CURVES (CON.)

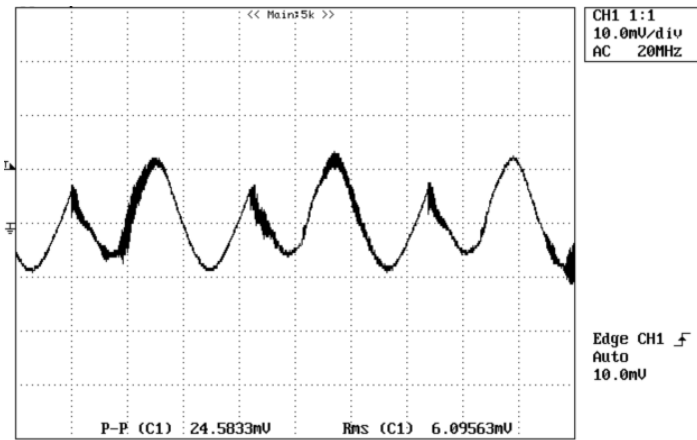


Figure 7: Output ripple & noise at 5Vin, 3.3V/3A out

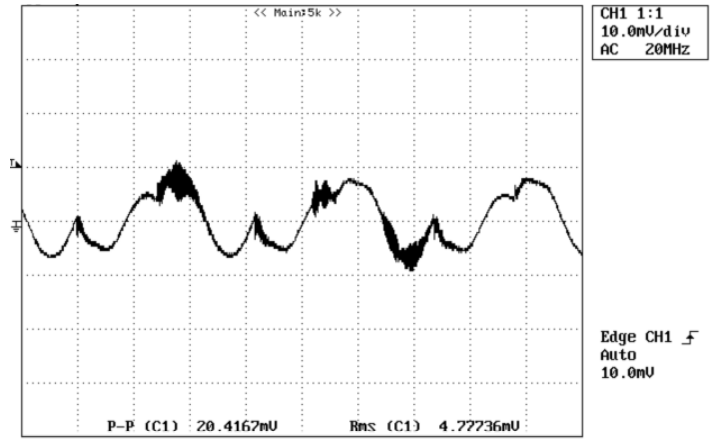


Figure 8: Output ripple & noise at 5Vin, 2.5V/3A out

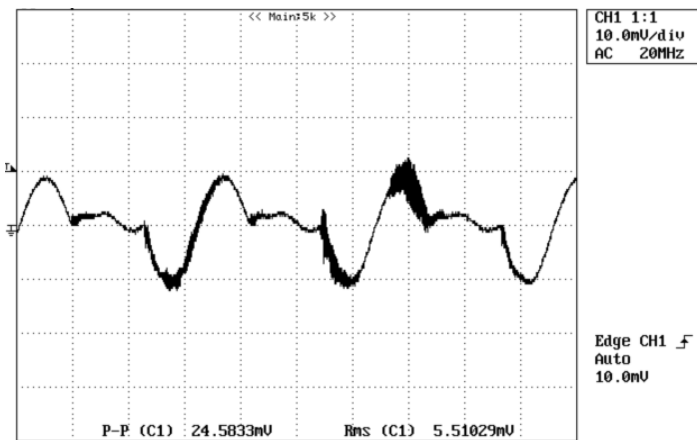


Figure 9: Output ripple & noise at 5Vin, 1.8V/3A out

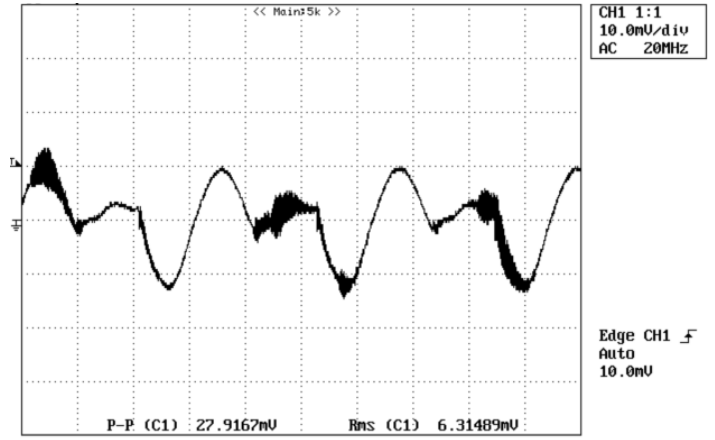


Figure 10: Output ripple & noise at 5Vin, 1.5V/3A out

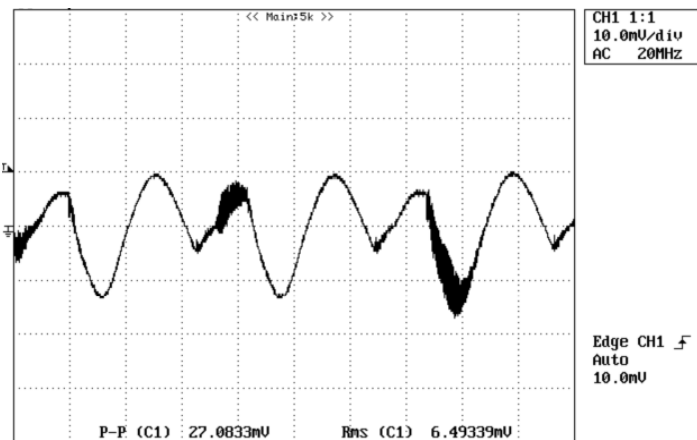


Figure 11: Turn on delay time at 3.3Vin, 1.2V/3A out

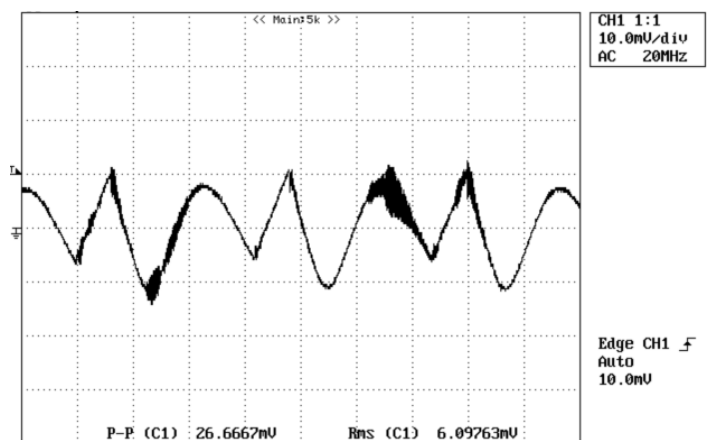


Figure 12: Turn on delay time at 5Vin, 0.75V/3A out



ELECTRICAL CHARACTERISTICS CURVES (CON.)

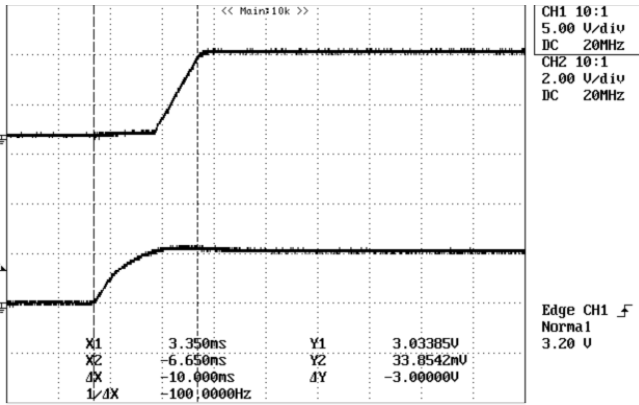


Figure 13: Turn on delay time at 5Vin, 3.3V/3A out

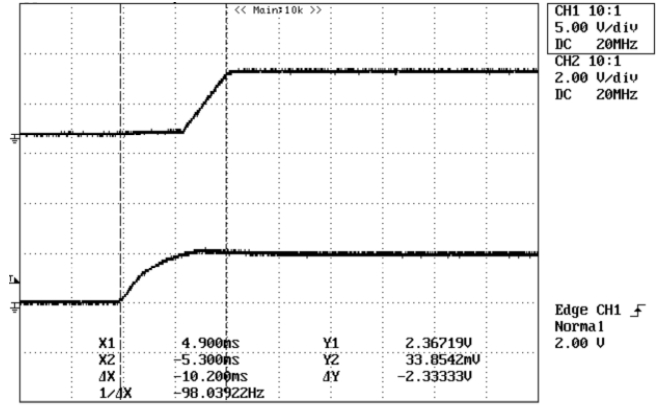


Figure 14: Turn on delay time at 5Vin, 2.5V/3A out

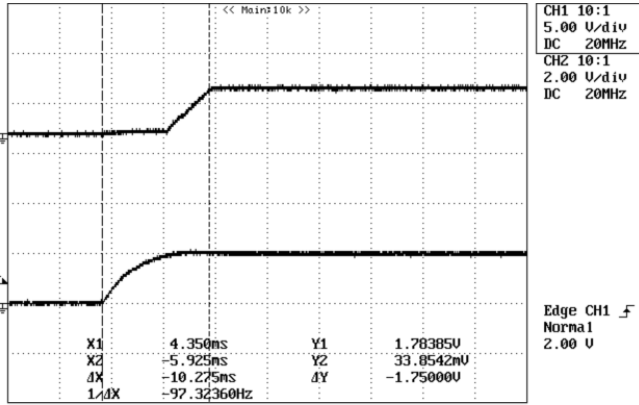


Figure 15: Turn on delay time at 5Vin, 1.8V/3A out

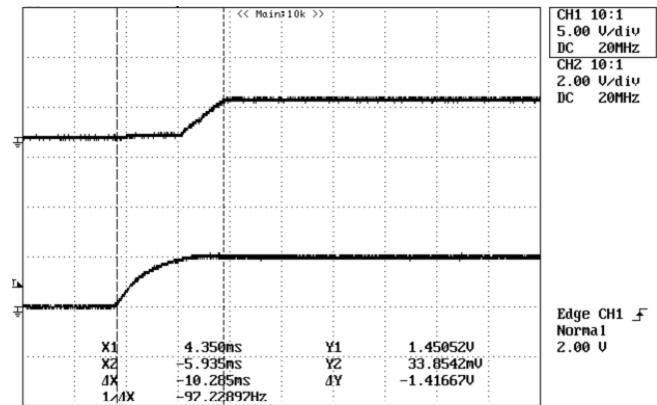


Figure 16: Turn on delay time at 5Vin, 1.5V/3A out

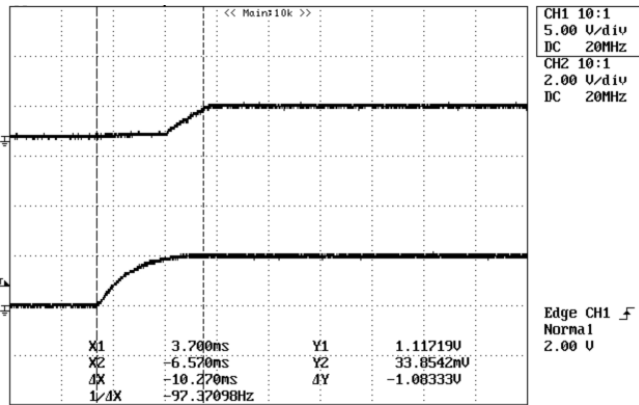


Figure 17: Turn on delay time at 5Vin, 1.2V/3A out

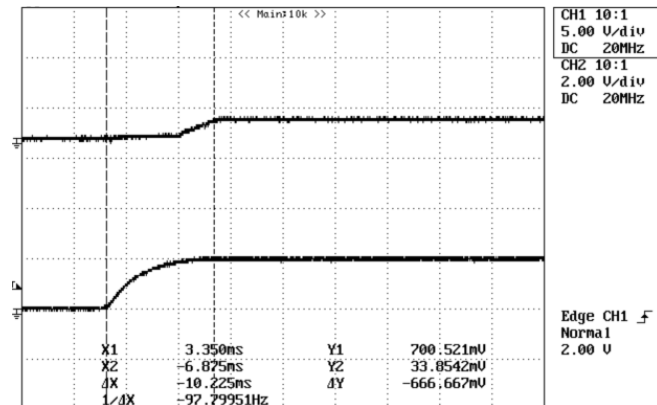


Figure 18: Turn on delay time at 5Vin, 0.75V/3A out

ELECTRICAL CHARACTERISTICS CURVES

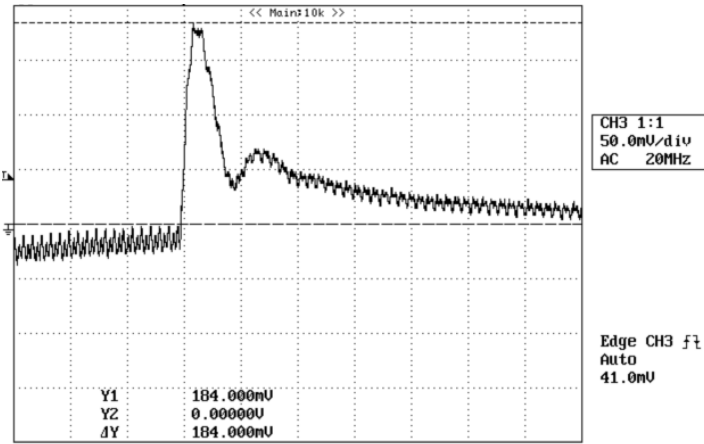


Figure 19: Typical transient response to step load change at $2.5A/\mu S$ from 100% to 50% of I_o , max at $5V_{in}$, $3.3V_{out}$ ($C_{out} = 1\mu F$ ceramic, $10\mu F$ tantalum)

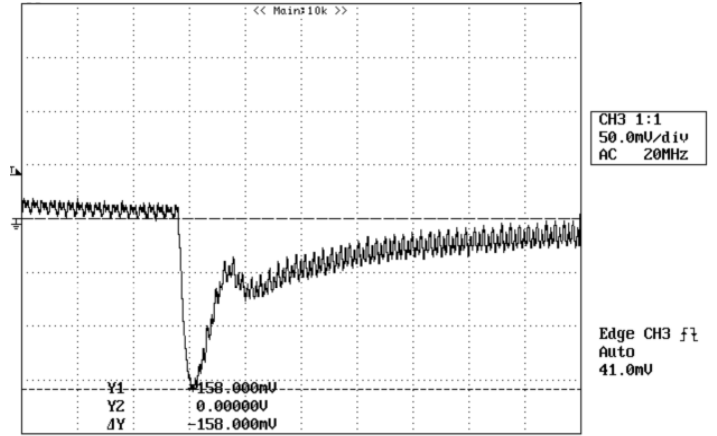


Figure 20: Typical transient response to step load change at $2.5A/\mu S$ from 50% to 100% of I_o , max at $5V_{in}$, $3.3V_{out}$ ($C_{out} = 1\mu F$ ceramic, $10\mu F$ tantalum)

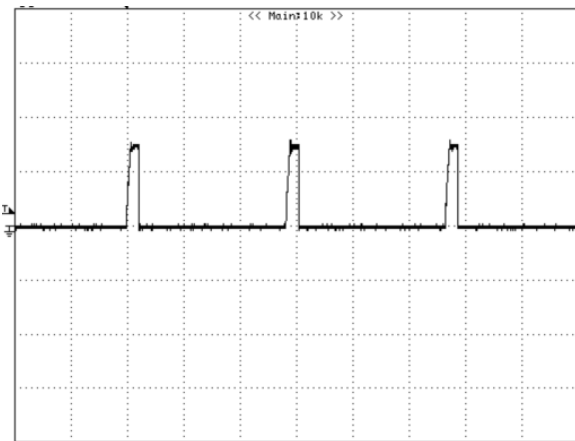


Figure 21: Output short circuit current $5V_{in}$, $0.75V_{out}$

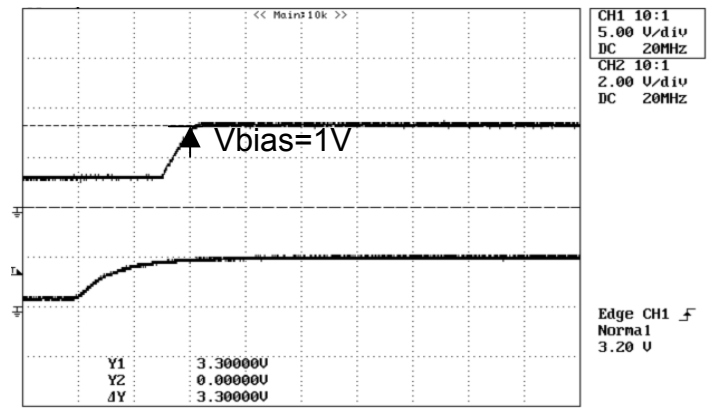
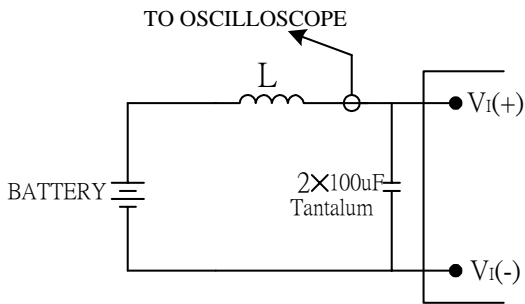


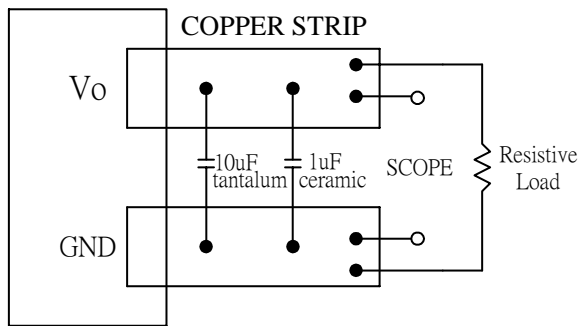
Figure 22: Turn on with Prebias $5V_{in}$, $3.3V/0A$ out, $V_{bias} = 1.0V_{dc}$

TEST CONFIGURATIONS



Note: Input reflected-ripple current is measured with a simulated source inductance. Current is measured at the input of the module.

Figure 23: Input reflected-ripple test setup



Note: Use a 10µF tantalum and 1µF capacitor. Scope measurement should be made using a BNC connector.

Figure 24: Peak-peak output noise and startup transient measurement test setup.

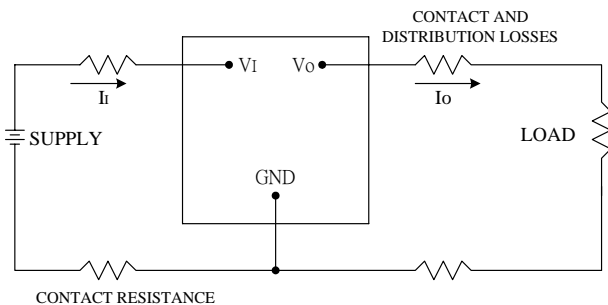


Figure 25: Output voltage and efficiency measurement test setup

Note: All measurements are taken at the module terminals. When the module is not soldered (via socket), place Kelvin connections at module terminals to avoid measurement errors due to contact resistance.

$$\eta = \left(\frac{V_o \times I_o}{V_i \times I_i} \right) \times 100 \quad \%$$

DS_DNT04SMD3A_01182007

DESIGN CONSIDERATIONS

Input Source Impedance

To maintain low noise and ripple at the input voltage, it is critical to use low ESR capacitors at the input to the module. Figure 26 shows the input ripple voltage (mVp-p) for various output models using 2x100 µF low ESR tantalum capacitor (KEMET p/n: T491D107M016AS, AVX p/n: TAJD107M106R, or equivalent) in parallel with 47 µF ceramic capacitor (TDK p/n:C5750X7R1C476M or equivalent). Figure 27 shows much lower input voltage ripple when input capacitance is increased to 400 µF (4 x 100 µF) tantalum capacitors in parallel with 94 µF (2 x 47 µF) ceramic capacitor.

The input capacitance should be able to handle an AC ripple current of at least:

$$I_{rms} = I_{out} \sqrt{\frac{V_{out}}{V_{in}} \left(1 - \frac{V_{out}}{V_{in}} \right)} \quad A_{rms}$$

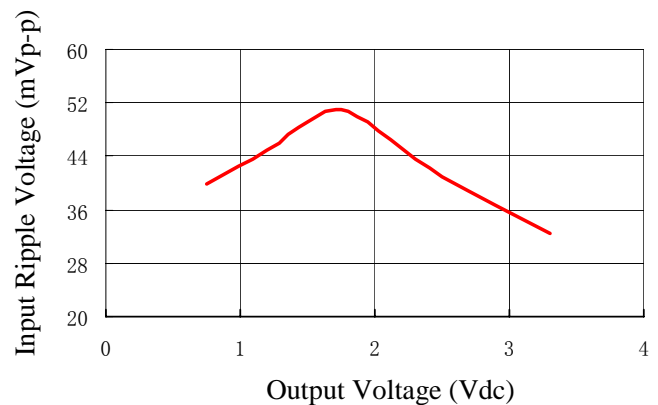


Figure 26: Input voltage ripple for various output models, $I_o = 3A$ ($C_{IN} = 2 \times 100 \mu F$ tantalum // $47 \mu F$ ceramic)

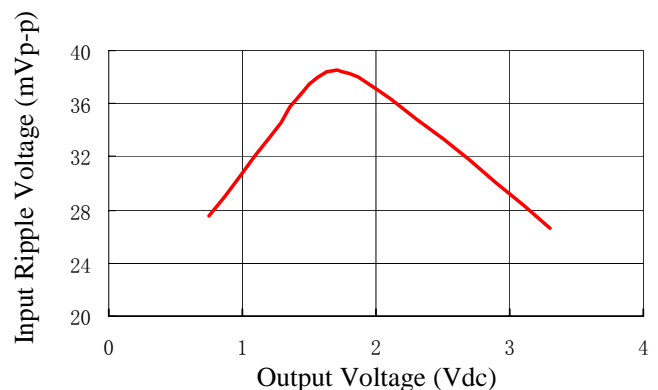


Figure 27: Input voltage ripple for various output models, $I_o = 3A$ ($C_{IN} = 4 \times 100 \mu F$ tantalum // $2 \times 47 \mu F$ ceramic)

DESIGN CONSIDERATIONS (CON.)

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the module. An input capacitance must be placed close to the modules input pins to filter ripple current and ensure module stability in the presence of inductive traces that supply the input voltage to the module.

Safety Considerations

For safety-agency approval the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standards.

For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV), the input must meet SELV requirements. The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

The input to these units is to be provided with a adequate time-delay fuse in the ungrounded lead.

FEATURES DESCRIPTIONS

Remote On/Off

The DNT series power modules have an On/Off pin for remote On/Off operation. Both positive and negative On/Off logic options are available in the DNT series power modules.

For positive logic module, connect an open collector (NPN) transistor or open drain (N channel) MOSFET between the On/Off pin and the GND pin (see figure 28). Positive logic On/Off signal turns the module ON during the logic high and turns the module OFF during the logic low. When the positive On/Off function is not used, leave the pin floating or tie to V_{in} (module will be On).

For negative logic module, the On/Off pin is pulled high with an external pull-up resistor (see figure 29). Negative logic On/Off signal turns the module OFF during logic high and turns the module ON during logic low. If the negative On/Off function is not used, leave the pin floating or tie to GND. (module will be On)

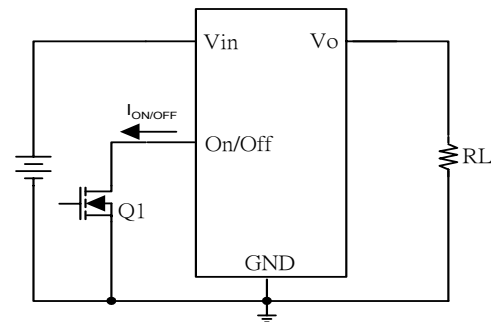


Figure 28: Positive remote On/Off implementation

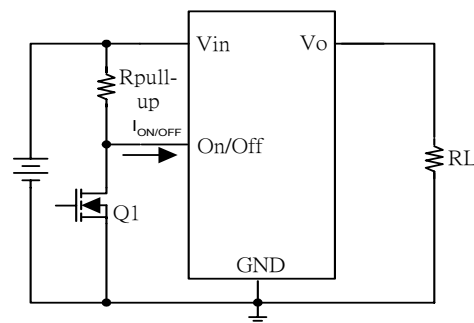


Figure 29: Negative remote On/Off implementation

Over-Current Protection

To provide protection in an output over load fault condition, the unit is equipped with internal over-current protection. When the over-current protection is triggered, the unit enters hiccup mode. The units operate normally once the fault condition is removed.



FEATURES DESCRIPTIONS (CON.)

Over-Temperature Protection

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the temperature exceeds the over-temperature threshold the module will shut down. The module will try to restart after shutdown. If the over-temperature condition still exists during restart, the module will shut down again. This restart trial will continue until the temperature is within specification.

Output Voltage Programming

The output voltage of the DNT can be programmed to any voltage between 0.75Vdc and 3.3Vdc by connecting one resistor (shown as Rtrim in Figure 30) between the TRIM and GND pins of the module. Without this external resistor, the output voltage of the module is 0.7525 Vdc. To calculate the value of the resistor Rtrim for a particular output voltage Vo, please use the following equation:

$$R_{trim} = \left[\frac{21070}{V_o - 0.7525} - 5110 \right] \Omega$$

For example, to program the output voltage of the DNS module to 1.8Vdc, Rtrim is calculated as follows:

$$R_{trim} = \left[\frac{21070}{1.8 - 0.7525} - 5110 \right] \Omega = 15K\Omega$$

DNT can also be programmed by apply a voltage between the TRIM and GND pins (Figure 31). The following equation can be used to determine the value of Vtrim needed for a desired output voltage Vo:

$$V_{trim} = 0.7 - 0.1698 \times (V_o - 0.7525)$$

For example, to program the output voltage of a DNT module to 3.3 Vdc, Vtrim is calculated as follows

$$V_{trim} = 0.7 - 0.1698 \times (3.3 - 0.7525) = 0.267V$$

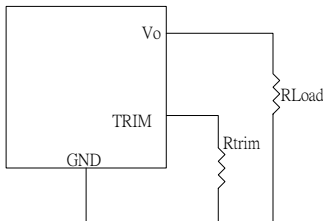


Figure 30: Circuit configuration for programming output voltage using an external resistor

FEATURE DESCRIPTIONS (CON.)

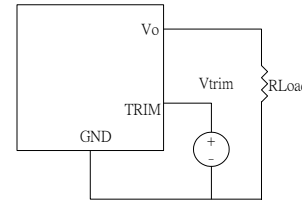


Figure 31: Circuit Configuration for programming output voltage using external voltage source

The amount of power delivered by the module is the voltage at the output terminals multiplied by the output current. When using the trim feature, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module must not exceed the maximum rated power ($V_o.set \times I_o.max \leq P_{max}$).

Voltage Margining

Output voltage margining can be implemented in the DNT modules by connecting a resistor, Rmargin-up, from the Trim pin to the ground pin for margining-up the output voltage and by connecting a resistor, Rmargin-down, from the Trim pin to the output pin for margining-down. Figure 32 shows the circuit configuration for output voltage margining. If unused, leave the trim pin unconnected. A calculation tool is available from the evaluation procedure which computes the values of Rmargin-up and Rmargin-down for a specific output voltage and margin percentage.

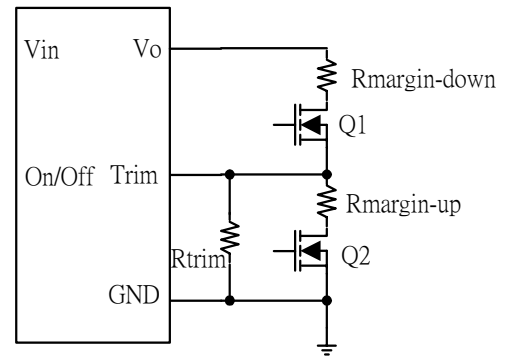


Figure 32: Circuit configuration for output voltage margining



THERMAL CONSIDERATIONS

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

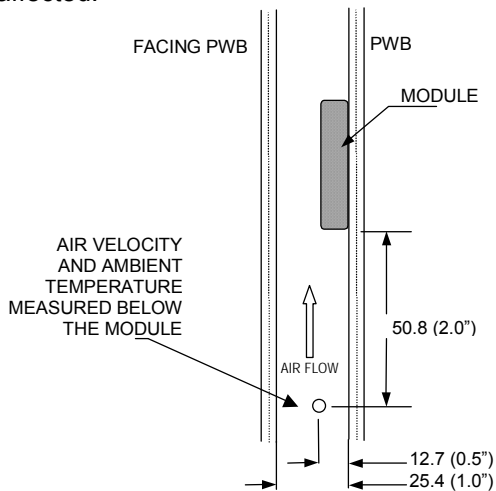
Thermal Testing Setup

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The height of this fan duct is constantly kept at 25.4mm (1").

Thermal Derating

Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inch)

Figure 33: Wind tunnel test setup

THERMAL CURVES

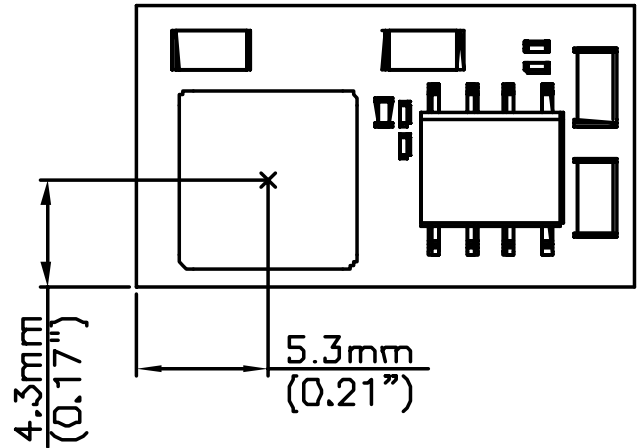


Figure 34: Temperature measurement location
The allowed maximum hot spot temperature is defined at 125°C

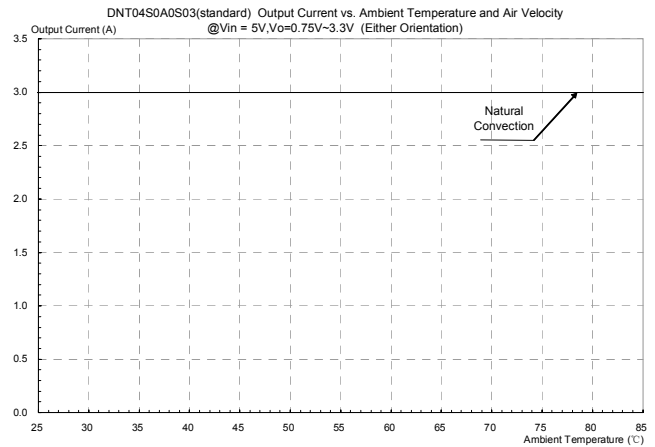
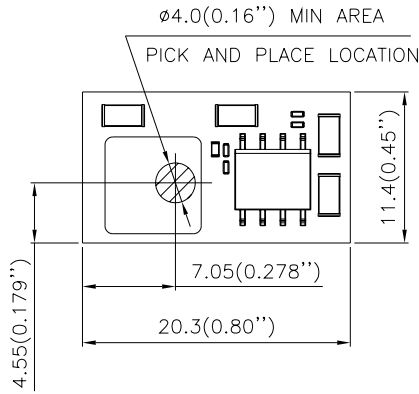


Figure 35: Derating curves, output current vs. ambient temperature and air velocity @ Vin=5V, Vout=0.75V~3.3V(Either Orientation)



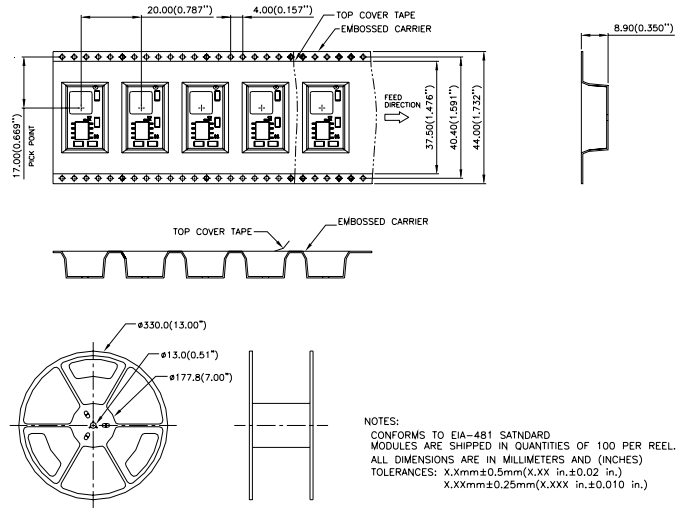
PICK AND PLACE LOCATION



NOTES:

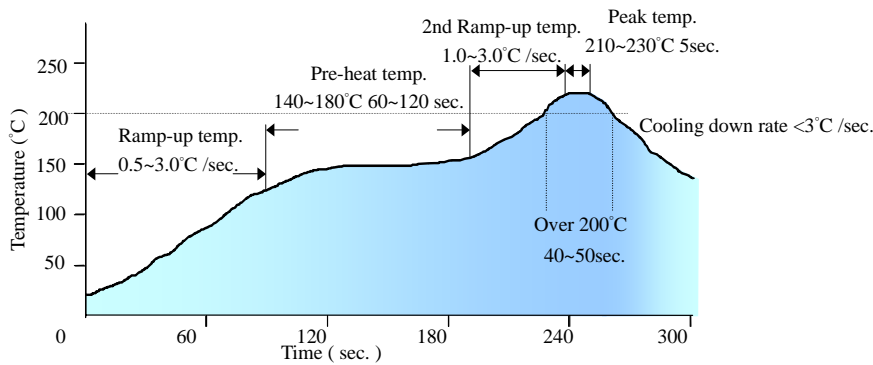
ALL DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
 TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
 X.XXmm±0.25mm(X.XXX in.±0.010 in.)

SURFACE- MOUNT TAPE & REEL

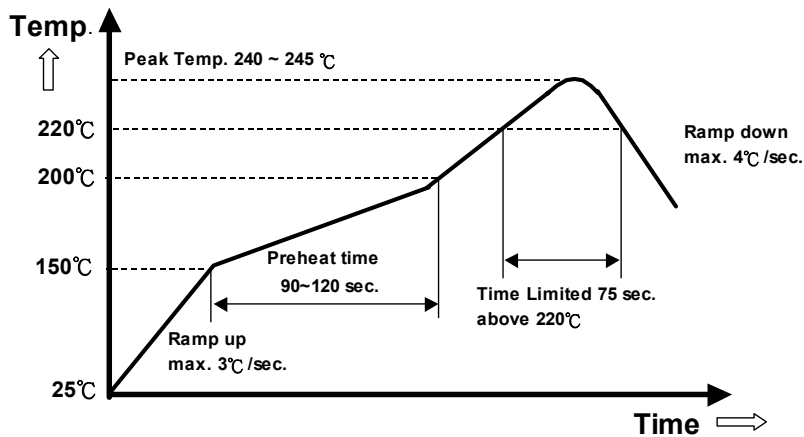


NOTES:
 CONFORMS TO EIA-481 STANDARD
 MODULES ARE SHIPPED IN QUANTITIES OF 100 PER REEL.
 ALL DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
 TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
 X.XXmm±0.25mm(X.XXX in.±0.010 in.)

LEAD (Sn/Pb) PROCESS RECOMMEND TEMP. PROFILE

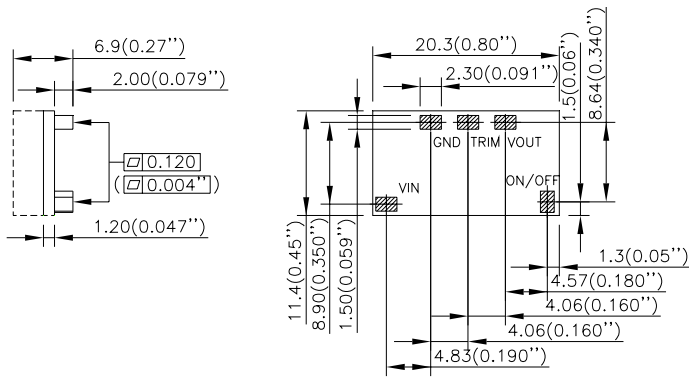


LEAD FREE (SAC) PROCESS RECOMMEND TEMP. PROFILE



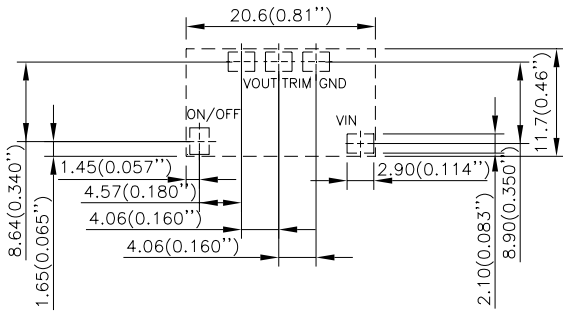
MECHANICAL DRAWING

SMD PACKAGE



SIDE VIEW

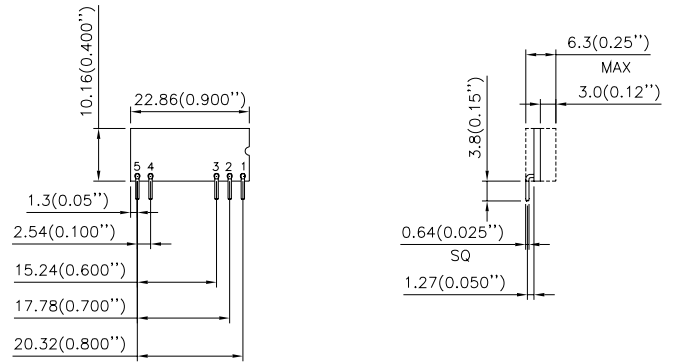
BOTTOM VIEW



RECOMMEND PWB PAD LAYOUT

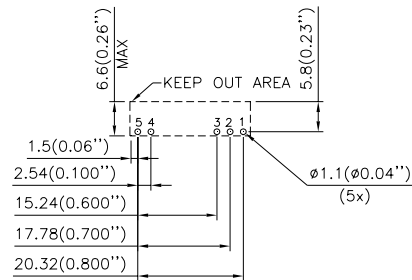
NOTES:
 DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
 TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
 X.XXmm±0.25mm(X.XXX in.±0.010 in.)

SIP PACKAGE (OPTIONAL)



BACK VIEW

SIDE VIEW



RECOMMEND PWB PAD LAYOUT

NOTES:
 DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
 TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
 X.XXmm±0.25mm(X.XXX in.±0.010 in.)

PIN#	FUNCTION
1	Vout
2	TRIM
3	GND
4	Vin
5	On/Off

PART NUMBERING SYSTEM

DNT	04	S	0A0	S	03	N	F	A
Product Series	Input Voltage	Numbers of Outputs	Output Voltage	Package Type	Output Current	On/Off logic		Option Code
DNT- 3A/5A	04 - 2.4V~5.5V	S - Single	0A0 - Programmable	R - SIP S - SMD	03 - 3A	N- negative P- positive	F- RoHS 6/6 (Lead Free)	A - Standard Function

MODEL LIST

Model Name	Package	Input Voltage	Output Voltage	Output Current	Efficiency 5Vin, 3.3Vdc full load
DNT04S0A0S03NFA	SMD	2.4V ~ 5.5Vdc	0.75V ~ 3.3Vdc	3A	94%
DNT04S0A0R03NFA	SIP	2.4V ~ 5.5Vdc	0.75V ~ 3.3Vdc	3A	94%
DNT04S0A0S05NFA	SMD	2.4V ~ 5.5Vdc	0.75V ~ 3.3Vdc	5A	94%
DNT04S0A0R05NFA	SIP	2.4V ~ 5.5Vdc	0.75V ~ 3.3Vdc	5A	94%

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