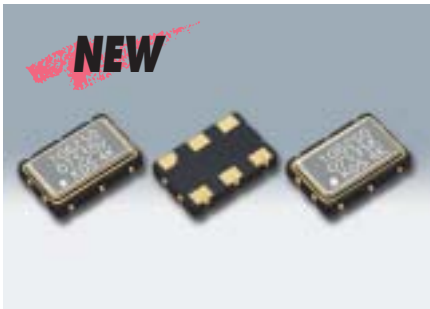


# SMD Type Crystal Oscillators

## DS0753SV, DS0753SK (LVPECL outputs)



Actual size

### Features

- Features a package size of 7.3×4.9×1.5, miniature SMD-SPXO
- 3.3V operation, super high speed (90MHz to 140MHz)
- Tri-state function
- LVPECL Output (DS0753SK)



### Applications

- PC, peripherals, and other appliances

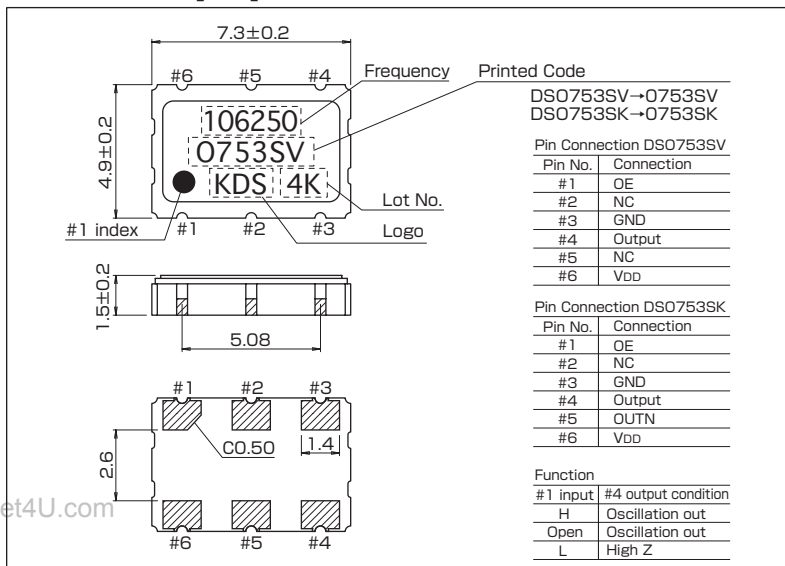
### Standard Specification

Item	Type	Legend	DS0753SV	DS0753SK	Condition
Standard Characteristics	Output Frequency Range	Fo	90MHz~140MHz		
	Frequency Stability	$\Delta f/Fo$	$\pm 50 \times 10^{-6}$ , $\pm 100 \times 10^{-6}$		-10~+70°C, Includes frequency tolerance at room temperature.
	Supply Voltage	VDD	+3.3V±0.3V	+3.3V±0.165V	
	Operating Temperature Range	TOPR	-10~+70°C		
	Current Consumption 1	IDD1	60mA max.	90mA max.	When no load is applied.
	Current Consumption 2 (1 pin, "L" Level)	IDD2	0.01mA max.	0.03mA max.	High impedance output/Oscillation stop
Output Characteristics	Output Disable Time	TPLZ	200nsec. max.		
	Output Enable Time	TPZL	5msec. max.		
	Symmetry 1	Symmetry 1	40%~60%		SV : VDD×0.5 Level SK : +2.0V Level
	Symmetry 2	Symmetry 2	—		SK : Measured at the crossing point of output
	Rise and Fall Time	Tr, Tf	3nsec. max.	1nsec. max.	SV : VDD×0.1~VDD×0.9 Level SK : output amplitude×0.2~output amplitude×0.8 Level
	"0" Level	VoL	VDD×0.1 min.	1.490V~1.680V	
	"1" Level	VoH	VDD×0.9 min.	2.275V~2.420V	
	Load Condition(C-MOS)	CL	15pF max.	—	
Input Characteristics	Input Voltage Level	VIL / VIH	VIL = VDD×0.3 max. VIH = VDD×0.7 min.	VIL = VDD×0.3 max. VIH = VDD×0.7 min.	
	Input Current	IIL / IIH	IIL = -100 μA max. / IIH = +100 μA max.		

Packing Unit : 1000pcs./reel (φ 254)

Consult our sales representative for other specifications.

### Dimensions[mm]



### Recommended Land Pattern [mm]

