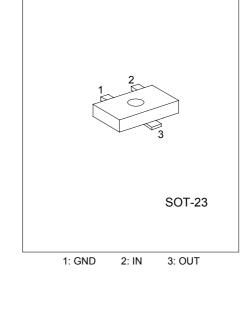
NPN DIGITAL TRANSISTOR (BUILT-IN RESISTORS)

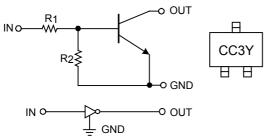
FEATURES

- *Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- *The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input They also have the advantage of almost completely eliminating parasitic effects.
- *Only the on/off conditions need to be set for operation, making device design easy.



EQUIVALENT CIRCUIT





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ABSOLUTE MAXIMUM RATINGS (Ta=25℃)

PARAMETER	SYMBOL	RATINGS	UNIT				
Supply Voltage	Vcc	50	V				
Input Voltage	Vin	-5 ~ +12	V				
Output Current	lo	100	mA				
	IC(MAX)	100	mA				
Power Dissipation	Po	200	mW				
Junction Temperature	Tj	150	$^{\circ}\mathbb{C}$				
Storage Temperature	Tstg	-55 ~ +150	$^{\circ}\mathbb{C}$				

ELECTRICAL CHARACTERISTICS (Ta=25°C)

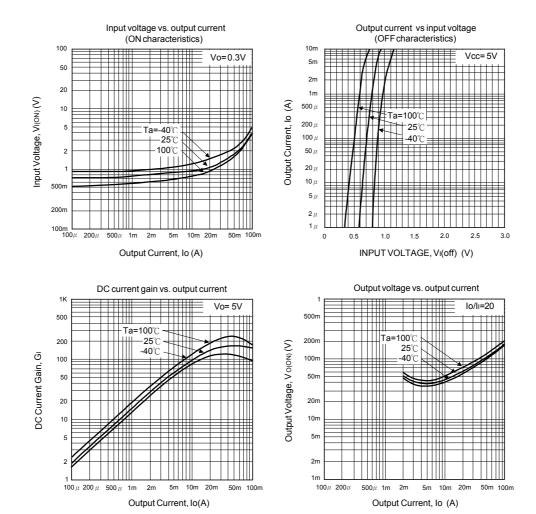
		,				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	VI(off)	Vcc= 5V, Io=100 μ A			0.3	V
	VI(ON)	Vo= 0.3V, Io= 20mA	3			V
Output Voltage	Vo(on)	Io/I _I = 10mA/0.5 mA		0.1	0.3	V
Input Current	lı .	Vi= 5V			3.8	mA
Output Current	IO(off)	Vcc= 50V, Vi=0V			0.5	μ A
DC Current Gain	Gi	Vo= 5V, Io= 10mA	33			
Input Resistance	R ₁		1.54	2.2	2.86	kΩ
Resistance Ratio	R2/R1		3.6	4.5	5.5	
Transition Frequency	fτ	VcE= 10 V, IE= -5mA, f=100MHz*	, and the second	250		MHz

^{*}Transition frequency of the device



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ELECTRICAL CHARACTERISTIC CURVES



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