

NPN 500mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

Parameter	Value
V _{CC}	50V
I _{C(MAX.)}	500mA
R ₁	1kΩ
R_2	1kΩ

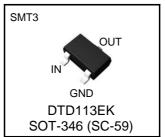
Features

- 1) Built-In Biasing Resistors, R1 = R2 = $1k\Omega$.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types :DTB113EK series
- 6) Lead Free/RoHS Compliant.

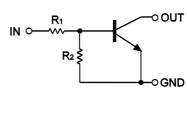
Application

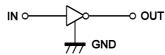
Switching circuit, Inverter circuit, Interface circuit, Driver circuit

Outline



•Inner circuit





Packaging specifications

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Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
DTD113EK	SMT3	2928	T146	180	8	3,000	F21

● Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V _{CC}	50	V
Input voltage	V _{IN}	-10 to +10	V
Collector current	I _C *1	500	mA
Power dissipation	P _D *2	200	mW
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

●Electrical characteristics(Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
longet coaltana	$V_{I(off)}$	$V_{CC} = 5V, I_{O} = 100 \mu A$	-	-	0.5	V
Input voltage	V _{I(on)}	$V_0 = 0.3V, I_0 = 20mA$	3.0	-	-	V
Output voltage	$V_{O(on)}$	$I_0 / I_1 = 50 \text{mA} / 2.5 \text{mA}$	-	0.1	0.3	V
Input current	I _I	V _I = 5V	-	-	7.2	mA
Output current	I _{O(off)}	$V_{CC} = 50V, V_{I} = 0V$	-	-	0.5	μΑ
DC current gain	Gı	$V_0 = 5V, I_0 = 50mA$	33	-	-	-
Input resistance	R ₁	-	0.7	1	1.3	kΩ
Resistance ratio	R ₂ /R ₁	-	0.8	1	1.2	-
Transition frequency	f _T *1	$V_{CE} = 10V, I_{E} = -50mA,$ f = 100MHz	ı	200	ı	MHz

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference footprint

●Electrical characteristic curves(Ta = 25°C)

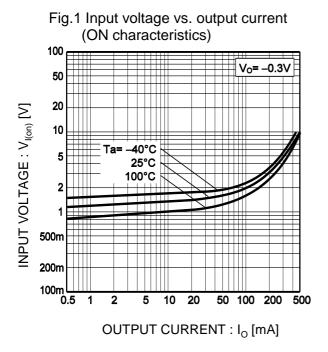


Fig.2 Output current vs. input voltage (OFF characteristics) 10m Ta= 100°C V_{CC}= 5V 5m 25°C 2m 40°C OUTPUT CURRENT : Io [A] 1m 500µ 200μ 100µ 50µ 20μ 10μ 5μ 2μ INPUT VOLTAGE : $V_{I(off)}[V]$

Fig.3 Output current vs. output voltage

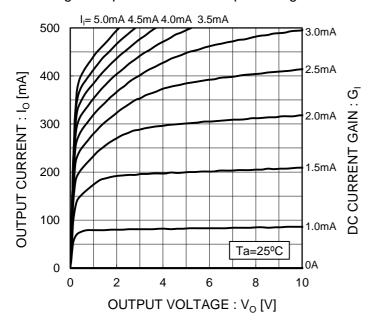
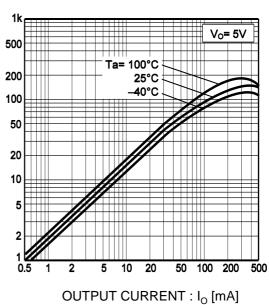
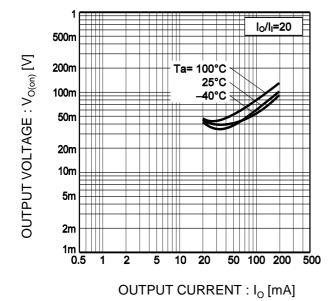


Fig.4 DC current gain vs. output current



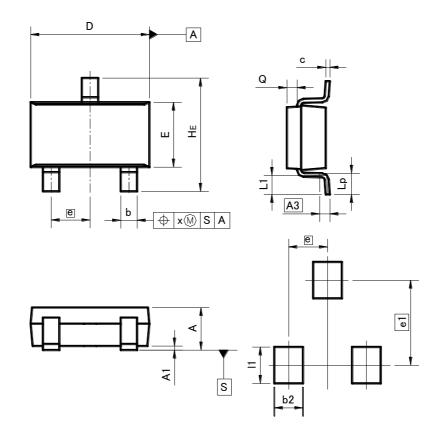
●Electrical characteristic curves(Ta = 25°C)

Fig.5 Output voltage vs. output current



●Dimensions (Unit:mm)

SMT3



Patterm of terminal position areas

DIM	MILIMETERS		INC	HES
DIN	MIN	MAX	MIN	MAX
Α	1.00	1.30	ı	0.051
A1	0.00	0.10	0	0.004
A3	0.2	25	0.01	
b	0.35	0.50	0.014	0.02
С	0.09	0.25	0.004	0.01
D	2.80	3.00	0.11	0.118
E	1.50	1.80	0.059	0.071
е	0.95		0.0	04
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
х		0.10	_	0.004
У	_	0.10	_	0.004

DIM	MILIMETERS		MILIMETERS INCHES		HES
DIM	MIN MAX		MIN	MAX	
e1	2.10		0.08		
b2		0.60	-	0.024	
l1	-	0.90	-	0.035	

Dimension in mm/inches

Notes

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DTD113EK - Web Page

Distribution Inventory

Part Number	DTD113EK
Package	SMT3
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes