

# P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
- 60	0.065 at V <sub>GS</sub> = - 10 V	- 6.7			
- 60	0.070 at V <sub>GS</sub> = - 4.5 V	- 5.4			

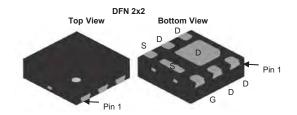
#### **FEATURES**

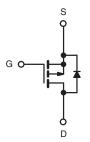
- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET<sup>®</sup> Power MOSFET
- Fast Switching

# ROHS COMPLIANT HALOGEN FREE

### **APPLICATIONS**

- Load Switches
- Half-Bridge Motor Drives
- High Voltage Non-Synchronous Buck Converters





P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T	A = 25 °C, unles	ss otherwise r	oted		
Parameter	Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 60		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current /T 150 °C\8	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 6.7	- 4.6	^
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 4.6	- 2.9	
Pulsed Drain Current		I <sub>DM</sub>	- 30		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	- 3.2	- 1.3		
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	3.8	1.5	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		2.0	0.8	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		00
Soldering Recommendations (Peak Temperature) <sup>b, c</sup>			260		°C

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maniana la destructo de Amelianda	t ≤ 10 s	R <sub>thJA</sub>	26	33		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' ¹thJA	65	81	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	1.9	2.4		

#### Notes

- a. Surface Mounted on 1" x 1" FR4 board.
- b. The DFN2X2 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- c. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.



# www.din-tek.jp

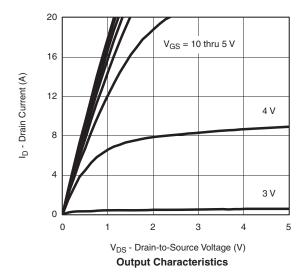
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	- 1		- 3	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zava Cata Valtaga Dvain Cuwant	1	V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V			- 1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C			- 5	μΑ
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 20			Α
	В	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5.7 A		0.060	0.065	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -4.4 \text{ A}$		0.070	0.076	Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5.7 A		11		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = - 3.2 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V
Dynamic <sup>b</sup>						
Total Gate Charge	$Q_g$			15	25	
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ = - 30 V, $V_{GS}$ = - 10 V, $I_D$ = - 5.7 A		4		nC
Gate-Drain Charge	$Q_{gd}$			3.2		
Turn-On Delay Time	t <sub>d(on)</sub>			12	20	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 30 V, $R_L$ = 30 $\Omega$		12	20	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 1 A, $V_{GEN}=$ - 10 V, $R_g=6~\Omega$		22	35	ns
Fall Time	t <sub>f</sub>			16	25	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 3.2 A, dl/dt = 100 A/μs		45	90	

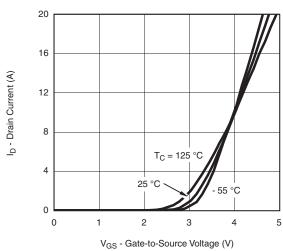
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

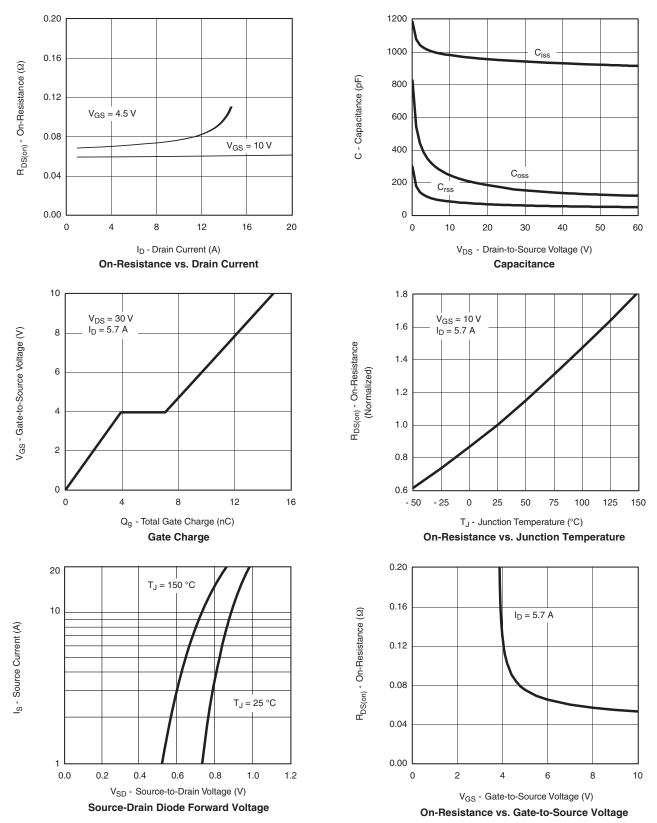




**Transfer Characteristics** 

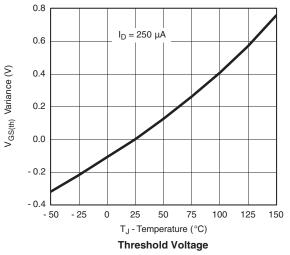


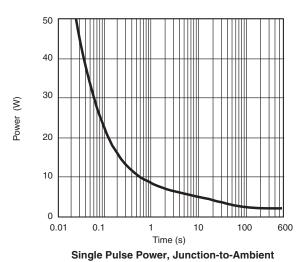
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

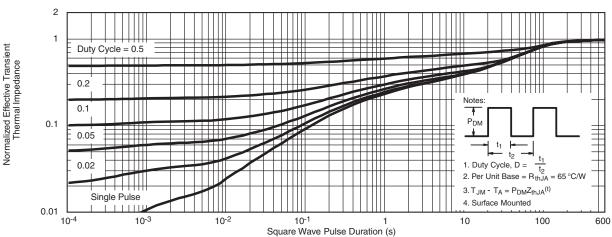


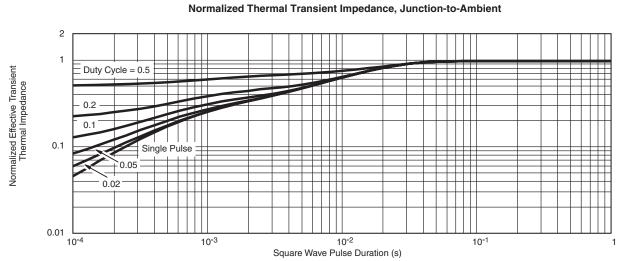


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





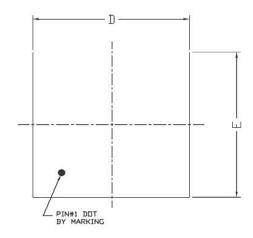


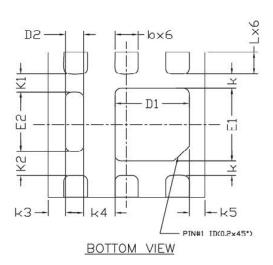


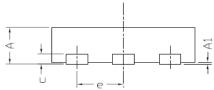
Normalized Thermal Transient Impedance, Junction-to-Case

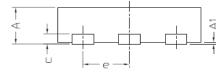


# DFN2x2 \_6L\_EP1\_S PACKAGE OUTLINE









RECOMMENDED LAND PATTERN

0.650	0.300 000
2.050	-0.820 -0.330 -1.725

0.400--1.600-

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.50	0. 55	0.60	0.020	0.022	0.024	
A1	0.00		0.05	0.000		0.002	
ь	0.25	0.30	0.35	0.010	0.012	0.014	
С		0.152 REF		0.006 REF			
D	1.90	2.00	2.10	0.075	0.079	0.083	
D1	0.85	0.95	1.05	0.033	0.037	0.041	
D2	0.13	0.23	0.33	0.005	0.009	0.013	
E	1.90	2.00	2.10	0.075	0.079	0.083	
E1	0.90	1.00	1.10	0.035	0.039	0.043	
E2	0.72	0.82	0.92	0.028	0.032	0.036	
e	0.65 BSC			0. 026 BSC			
K	0. 20 BSC			0.008 BSC			
K1	0. 25 BSC			0.010 BSC			
K2	0.33 BSC				0.013 BSC		
K3	0. 22 BSC			0.009 BSC			
K4	0.40 BSC			0. 016 BSC			
K5	0. 20 BSC			0.008 BSC			
L	0.25	0.30	0.35	0.010	0.012	0.014	

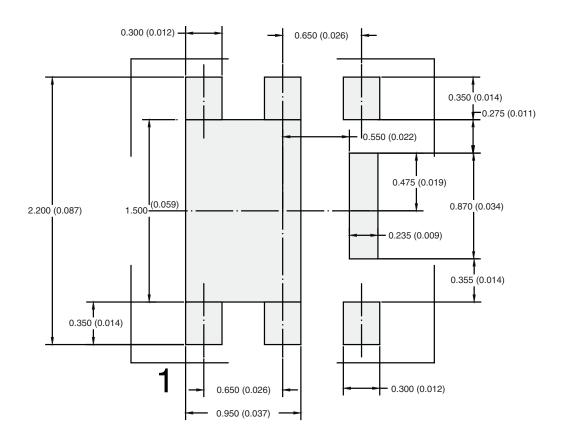
#### NOTE

CONTROLLING DIMENSION IS MILLIMETER.
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

UNIT: mm



#### **RECOMMENDED PAD LAYOUT FOR DFN2X2**



Dimensions in mm/(Inches)





www.din-tek.jp

## **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Din-Tek Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Din-Tek"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Din-Tek makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Din-Tek disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Din-Tek's knowledge of typical requirements that are often placed on Din-Tek products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Din-Tek's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Din-Tek products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Din-Tek product could result in personal injury or death. Customers using or selling Din-Tek products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Din-Tek personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Din-Tek. Product names and markings noted herein may be trademarks of their respective owners.

# **Material Category Policy**

Din-Tek Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Din-Tek documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Din-Tek Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Din-Tek documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.