

P-Channel 30 V (D-S) MOSFET

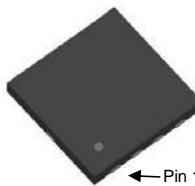


RoHS
COMPLIANT

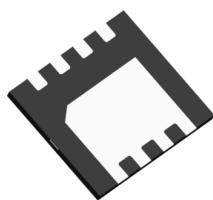
| PRODUCT SUMMARY | | | |
|------------------------|-----------------------------|-------------------|--------------|
| V_{DS} (V) | $R_{DS(on)}$ (Ω) | I_D (A) | Q_g (Typ.) |
| - 30 | 0.0123 at $V_{GS} = -10$ V | - 45 ^d | 40 nC |
| | 0.0179 at $V_{GS} = -4.5$ V | - 30 ^d | |

DFN 3x3 EP

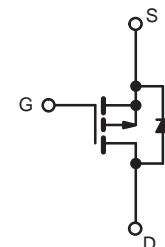
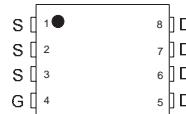
Top View



Bottom View



Top View



P-Channel MOSFET

FEATURES

- DT-Trench Power MOSFET
- Low On-Resistance for Low Voltage Drop
- 100 % R_g and UIS Tested

APPLICATIONS

- Battery, Load and Adaptor Switches
- Notebook Computers
- Notebook Battery Packs

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|--------------------------------------------------------------|----------------|-----------------------|------|
| Drain-Source Voltage | V_{DS} | - 30 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | |
| Continuous Drain Current ($T_J = 150$ °C) | $T_C = 25$ °C | - 45 ^d | A |
| | $T_C = 70$ °C | | |
| | $T_A = 25$ °C | | |
| | $T_A = 70$ °C | | |
| Pulsed Drain Current ($t = 100$ μ s) | I_{DM} | - 180 | |
| Continuous Source-Drain Diode Current | $T_C = 25$ °C | - 45 ^d | |
| | $T_A = 25$ °C | - 3.8 ^{a, b} | |
| Avalanche Current | I_{AS} | - 25 | |
| Single-Pulse Avalanche Energy | E_{AS} | 35 | mJ |
| Maximum Power Dissipation | $T_C = 25$ °C | 45 | W |
| | $T_C = 70$ °C | 28 | |
| | $T_A = 25$ °C | 4.2 ^{a, b} | |
| | $T_A = 70$ °C | 2.6 ^{a, b} | |
| Operating Junction and Storage Temperature Range | T_J, T_{stg} | - 55 to 150 | °C |
| Soldering Recommendations (Peak Temperature) ^{e, f} | | 260 | |

THERMAL RESISTANCE RATINGS

| Parameter | Symbol | Typical | Maximum | Unit |
|---------------------------------------------|------------|---------|---------|------|
| Maximum Junction-to-Ambient ^{a, c} | R_{thJA} | 25 | 30 | °C/W |
| Maximum Junction-to-Case | R_{thJC} | 2.5 | 2.78 | |

Notes:

- Surface mounted on 1" x 1" FR4 board.
- $t = 10$ s.
- Maximum under steady state conditions is 70 °C/W.
- Package limited.
- The DFN3X3 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

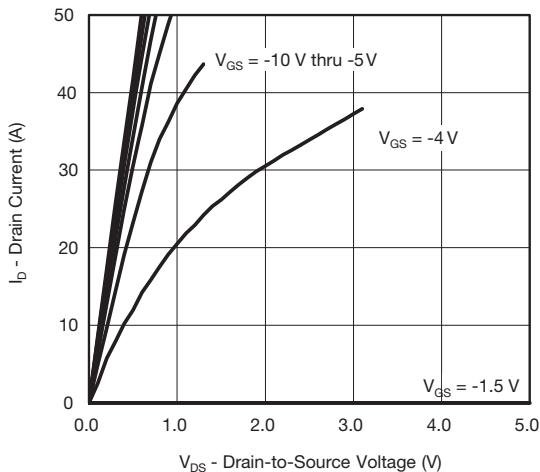
| SPECIFICATIONS ($T_J = 25^\circ\text{C}$, unless otherwise noted) | | | | | | |
|----------------------------------------------------------------------------|--------------------------------|----------------------------------------------------------------------------------------------------------------------------|------|--------|-----------|-------|
| Parameter | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
| Static | | | | | | |
| Drain-Source Breakdown Voltage | V_{DS} | $V_{GS} = 0$, $I_D = -250\ \mu\text{A}$ | -30 | | | V |
| V_{DS} Temperature Coefficient | $\Delta V_{DS}/T_J$ | $I_D = -250\ \mu\text{A}$ | | -22 | | mV/°C |
| $V_{GS(\text{th})}$ Temperature Coefficient | $\Delta V_{GS(\text{th})}/T_J$ | | | 4.1 | | |
| Gate-Source Threshold Voltage | $V_{GS(\text{th})}$ | $V_{DS} = V_{GS}$, $I_D = -250\ \mu\text{A}$ | -1 | | -3 | V |
| Gate-Source Leakage | I_{GSS} | $V_{DS} = 0\ \text{V}$, $V_{GS} = \pm 20\ \text{V}$ | | | ± 100 | nA |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = -24\ \text{V}$, $V_{GS} = 0\ \text{V}$ | | | -1 | μA |
| | | $V_{DS} = -24\ \text{V}$, $V_{GS} = 0\ \text{V}$, $T_J = 55^\circ\text{C}$ | | | -5 | |
| On-State Drain Current ^a | $I_{D(\text{on})}$ | $V_{DS} \geq -10\ \text{V}$, $V_{GS} = -10\ \text{V}$ | -180 | | | A |
| Drain-Source On-State Resistance ^a | $R_{DS(\text{on})}$ | $V_{GS} = -10\ \text{V}$, $I_D = -15\ \text{A}$ | | 0.0123 | 0.014 | Ω |
| | | $V_{GS} = -4.5\ \text{V}$, $I_D = -10\ \text{A}$ | | 0.0179 | 0.02 | |
| Forward Transconductance ^a | g_{fs} | $V_{DS} = -10\ \text{V}$, $I_D = -15\ \text{A}$ | | 60 | | S |
| Dynamic^b | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS} = -24\ \text{V}$, $V_{GS} = 0\ \text{V}$, $f = 1\ \text{MHz}$ | | 3150 | | pF |
| Output Capacitance | C_{oss} | | | 975 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 384 | | |
| Total Gate Charge | Q_g | $V_{DS} = -24\ \text{V}$, $V_{GS} = -10\ \text{V}$, $I_D = -10\ \text{A}$ | | 92 | | nC |
| | | $V_{DS} = -24\ \text{V}$, $V_{GS} = -4.5\ \text{V}$, $I_D = -10\ \text{A}$ | | 41 | | |
| Gate-Source Charge | Q_{gs} | | | 14 | | |
| Gate-Drain Charge | Q_{gd} | | | 29 | | |
| Gate Resistance | R_g | $f = 1\ \text{MHz}$ | 0.5 | 2.9 | 5.8 | Ω |
| Turn-On Delay Time | $t_{d(\text{on})}$ | $V_{DS} = -15\ \text{V}$, $R_L = 1.5\ \Omega$ $I_D \geq -10\ \text{A}$, $V_{GS} = -10\ \text{V}$, $R_g = 1\ \Omega$ | | 16 | | ns |
| Rise Time | t_r | | | 14 | | |
| Turn-Off DelayTime | $t_{d(\text{off})}$ | | | 60 | | |
| Fall Time | t_f | | | 12 | | |
| Turn-On Delay Time | $t_{d(\text{on})}$ | $V_{DS} = -15\ \text{V}$, $R_L = 1.5\ \Omega$ $I_D \geq -10\ \text{A}$, $V_{GS} = -4.5\ \text{V}$, $R_g = 1\ \Omega$ | | 60 | | |
| Rise Time | t_r | | | 59 | | |
| Turn-Off DelayTime | $t_{d(\text{off})}$ | | | 50 | | |
| Fall Time | t_f | | | 22 | | |
| Drain-Source Body Diode Characteristics | | | | | | |
| Continous Source-Drain Diode Current | I_S | $T_C = 25^\circ\text{C}$ | | | -45 | A |
| Pulse Diode Forward Current (100 μs) | I_{SM} | | | | -180 | |
| Body Diode Voltage | V_{SD} | $I_S = -3\ \text{A}$, $V_{GS} = 0$ | | -0.7 | -1.20 | V |
| Body Diode Reverse Recovery Time | t_{rr} | $I_F = -10\ \text{A}$, $dl/dt = 100\ \text{A}/\mu\text{s}$, $T_J = 25^\circ\text{C}$ | | 22 | | ns |
| Body Diode Reverse Recovery Charge | Q_{rr} | | | 12 | | |
| Reverse Recovery Fall Time | t_a | | | 9 | | ns |
| Reverse Recovery Rise Time | t_b | | | 13 | | |

Notes:

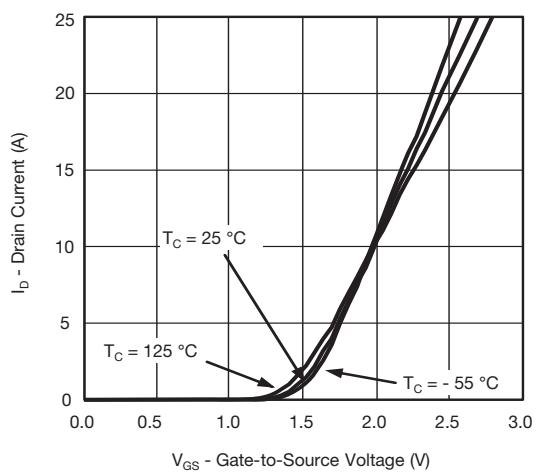
- a. Pulse test; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

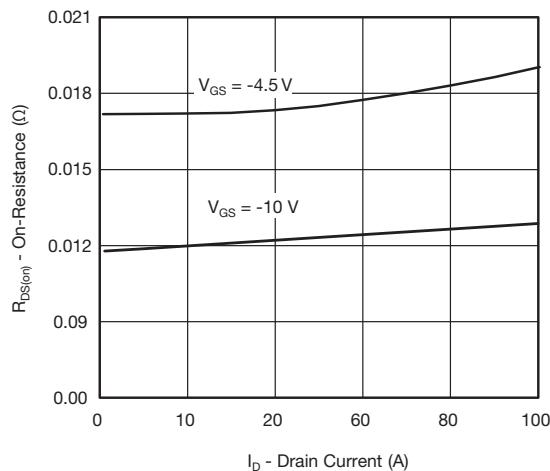
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



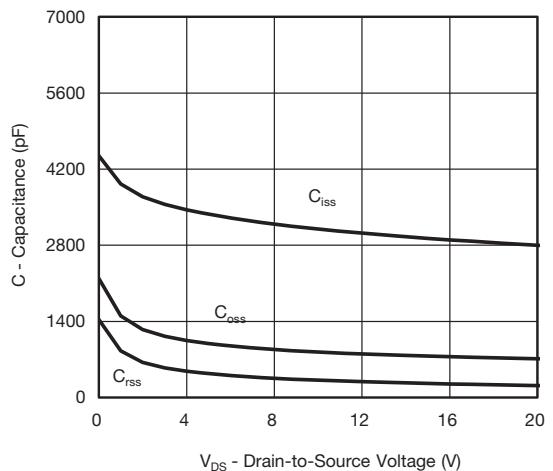
Output Characteristics



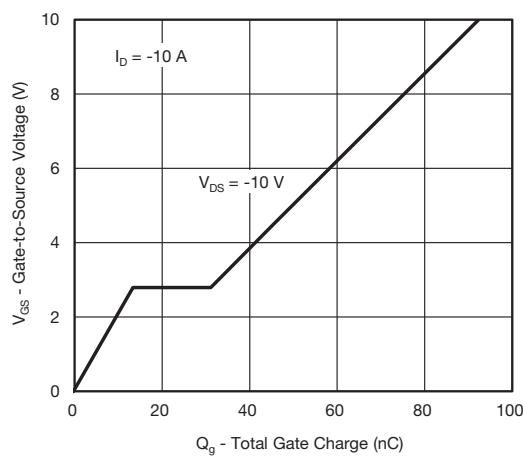
Transfer Characteristics



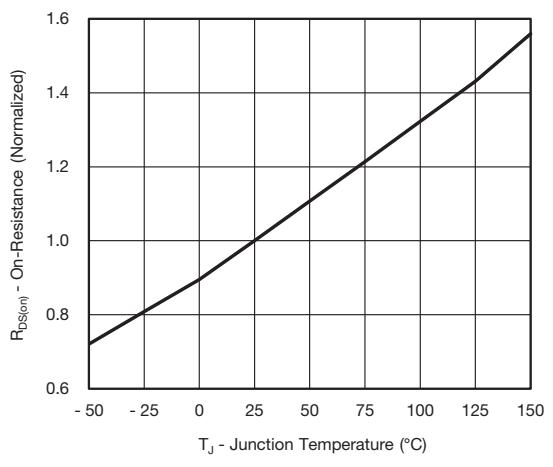
On-Resistance vs. Drain Current



Capacitance

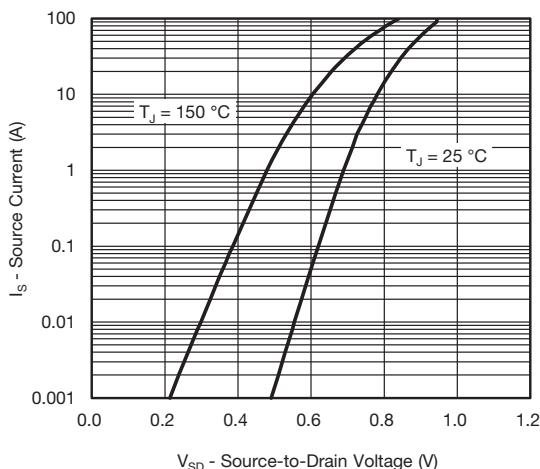


Gate Charge

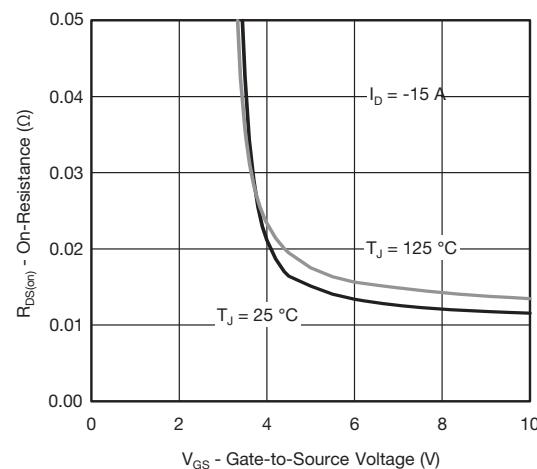


On-Resistance vs. Junction Temperature

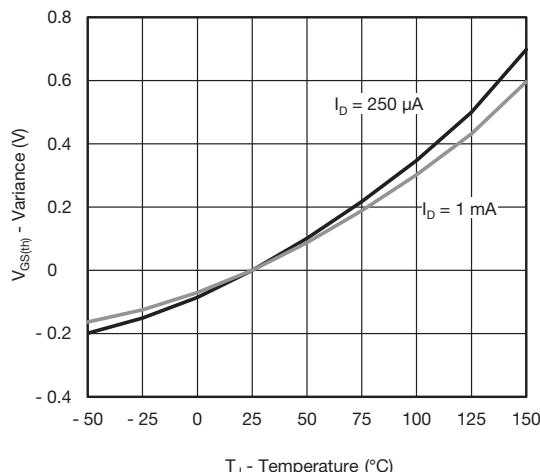
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



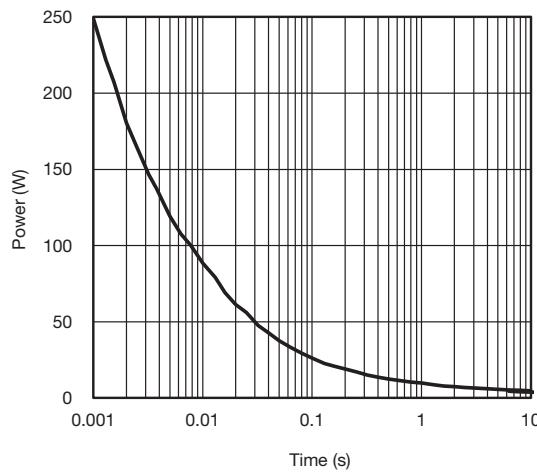
Source-Drain Diode Forward Voltage



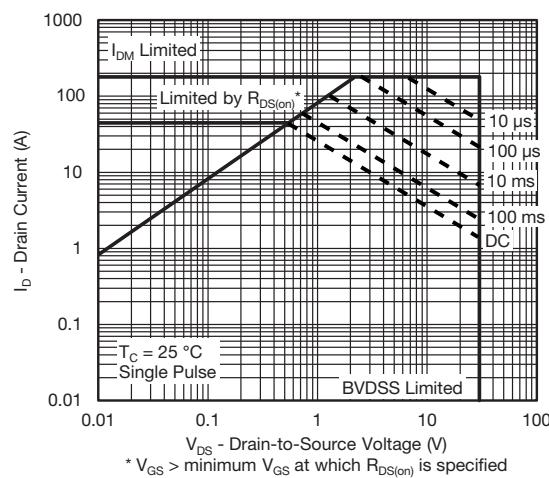
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

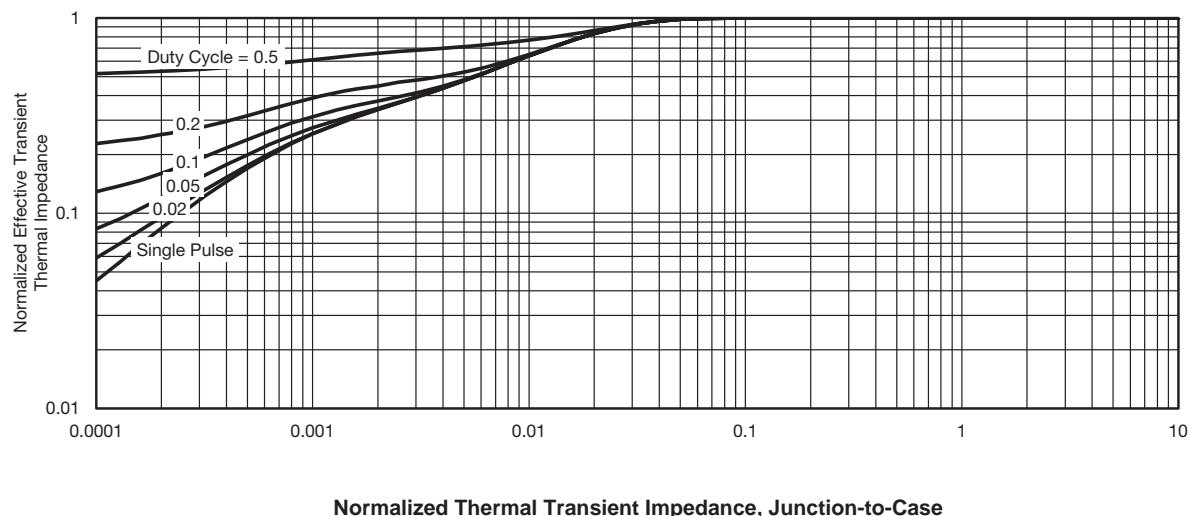
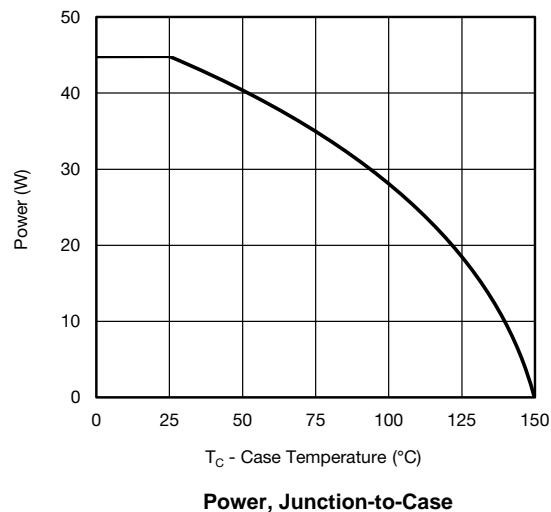


Single Pulse Power, Junction-to-Ambient

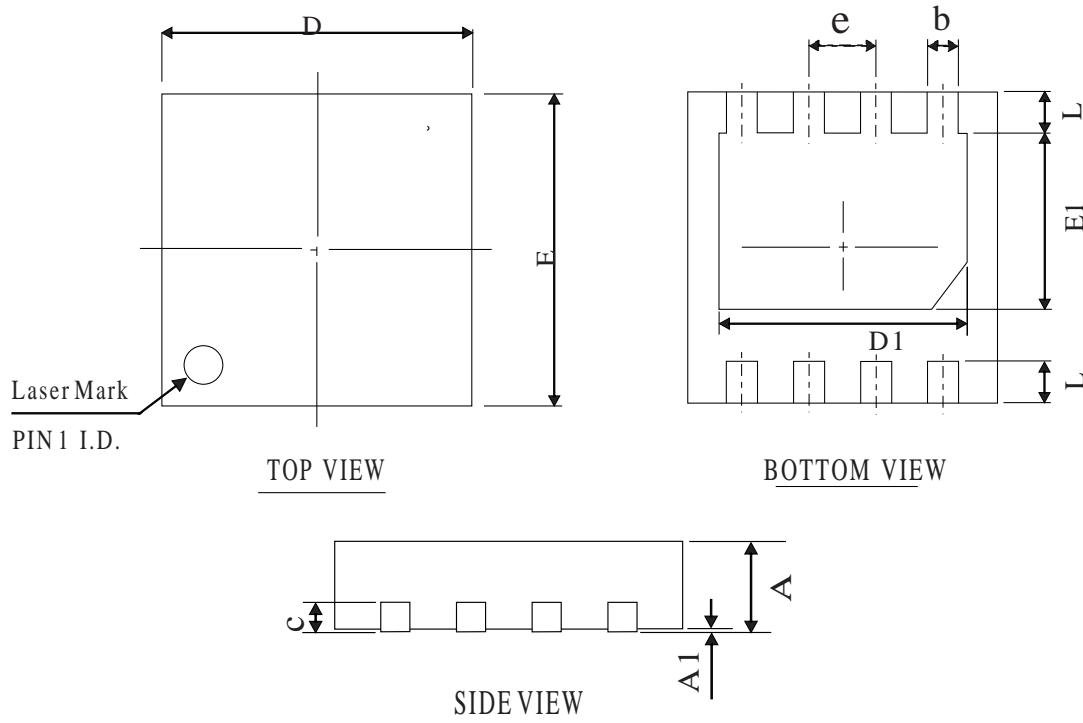


Safe Operating Area

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



DFN3*3-8L PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

| SYMBOL | MIN | NOM | MAX |
|----------|------------|-------|-------|
| A | 0. 60 | 0. 75 | 0. 90 |
| A1 | 0. 00 | 0. 02 | 0. 08 |
| b | 0. 20 | 0. 30 | 0. 45 |
| D | 2. 85 | 3. 00 | 3. 15 |
| E | 2. 85 | 3. 00 | 3. 15 |
| D1 | 2. 10 | 2. 40 | 2. 70 |
| E1 | 1. 50 | 1. 70 | 2. 00 |
| L | 0. 20 | 0. 40 | 0. 60 |
| C | 0. 203 REF | | |
| e | 0. 65 BSC | | |

OTHER DIMENSIONS

| | | | |
|----------|-------|-------|-------|
| A | 0. 50 | 0. 55 | 0. 60 |
| A | 0. 40 | 0. 45 | 0. 50 |

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