



# P-Channel 40 V (D-S) MOSFET

Pb
RoHS
COMPLIANT

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ (m $\Omega$ )(Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
- 40	4.6 at V <sub>GS</sub> = 10 V	- 90	150 nC		
- 40	6.5 at V <sub>GS</sub> = 4.5 V	- 90	158 nC		

#### **FEATU**

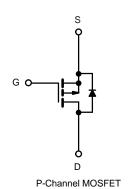
- DT-1
- 100 % Rg and UIS tested

#### **APPLICATIONS**

- Notebook
  - Load Switch

### **DFN5X6-8L Pin Configuration**





ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	- 40	V	
Gate-Source Voltage	$V_{GS}$	± 20	V	
Continuous Prair Correct /T. 450 °C\2	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	- 90	A
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>C</sub> = 100 °C		- 70	
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	- 360	^	
Single Pulse Avalanche Current	las	- 48		
Single Avalanche Energy	E <sub>AS</sub>	- 576	mJ	
Maximum Power Dissipation <sup>c</sup>	T <sub>C</sub> = 25 °C	- P <sub>D</sub>	95	W
iviaximum i owei Dissipation-	T <sub>C</sub> = 100 °C		38	v V
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) <sup>d</sup>	R <sub>thJA</sub>	17	°C /\/		
Junction-to-Case (Drain)	R <sub>thJC</sub>	1.3	°C/W		

#### Notes

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of Reja is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.



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PARAMETER SY		TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 40	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 1	-	- 3	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
Zero Gate Voltage Drain Current	I	V <sub>DS</sub> = - 40 V, V <sub>GS</sub> = 0 V	-	-	- 1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = - 32 V, $V_{GS}$ = 0 V, $T_J$ = 55 °C	-	-	- 10	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	-90	-	-	Α
Drain-Source On-State Resistance <sup>a</sup>	D	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 20 A	-	4.6	5.9	<b>~</b> 0
Diain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 20 A	-	6.5	8.8	mΩ
Forward Transconductancea	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 20 A	-	17	-	S
Dynamic <sup>b</sup>				<u> </u>		
Input Capacitance	C <sub>iss</sub>		-	6470	-	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = -20 \text{ V}, f = 1 \text{ MHz}$	-	494	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	497	-	
Total Gate Charge <sup>c</sup>	Qg		-	158	-	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -20 \text{ A}$	-	34	-	nC
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	32	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	-	3.5	-	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	35	-	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -20 \text{ V}, R_{L} = 15 \Omega, R_{GEN} = 1 \Omega$	-	15	-	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	V <sub>GS</sub> = -10 V	-	110	-	ns
Fall Time <sup>c</sup>	t <sub>f</sub>		-	60	-	
Drain-Source Body Diode Ratings and	Characterist	ics <sup>b</sup> (T <sub>J</sub> = 25 °C)				
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C		-	-90	Α
Pulsed Current	I <sub>SM</sub>			-	-360	Α
Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>F</sub> = - 1 A, V <sub>GS</sub> = 0 V		0.6	-	V
Reverse Recovery Time	t <sub>rr</sub>	1 2 5 A di/dt 100 A /:-		50	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>	$I_F = -3.5 \text{ A, di/dt} = -100 \text{ A/}\mu\text{s}$		65	-	nC

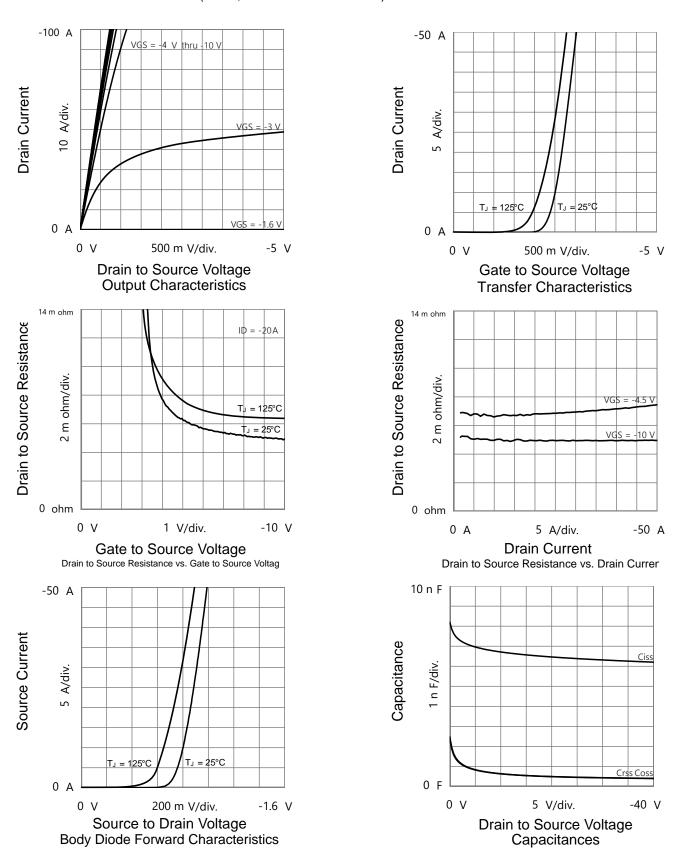
#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those in dicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

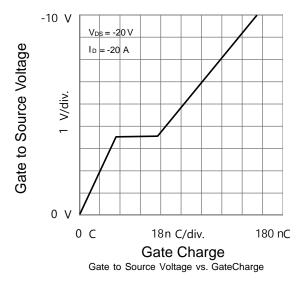


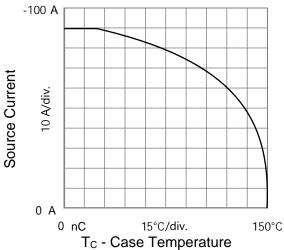
### TYPICAL CHARAC TERISTICS (25 °C, unless otherwise noted)

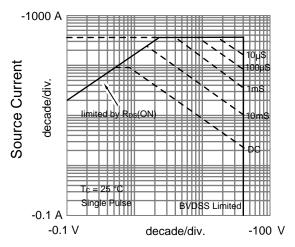




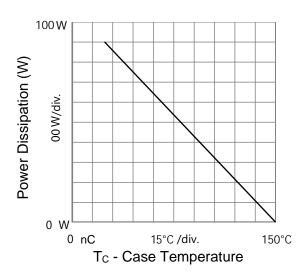
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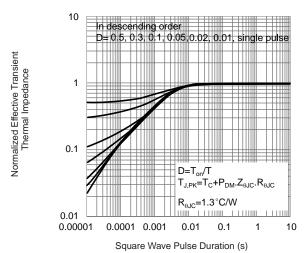






Source to Drain Voltage
Safe Operating Area, Junction-to-Ambient

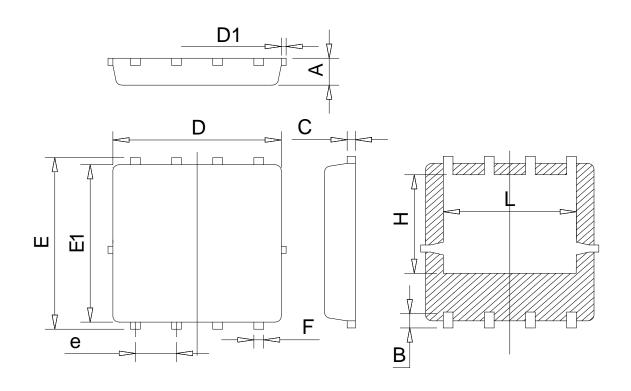




Normalized Thermal Transient Impedance, Junction-to-Ambient



## DFN5X6-8L PACKAGE OUTLINE



## **COMMON DIMENSIONS** (UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Тур	Max
Α	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
Е	5.85	6.05	6.25
E1	5.38	5.55	5.98
е	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25





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