Din-Tek SEMICONDUCTOR

DTQ6D408

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Dual N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (mΩ)(Typ.)	I_D (A)[a]	Q_g (Typ.)
40	7.8 at V_{GS} = 10 V	45	45 nC
	9.6 at V_{GS} = 4.5 V		

FEATURES

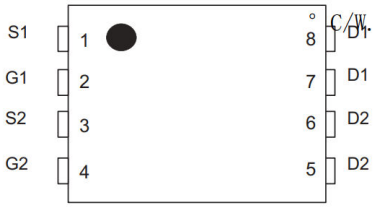
- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

Pb free
RoHS COMPLIANT

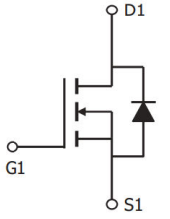
APPLICATIONS

- 12 V Automotive systems
- Motors, lamps and solenoid control
- Transmission control
- Ultra high performance power switching

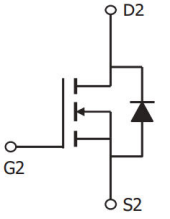
DFN5X6



Top View



N-Channel MOSFET



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted				
Parameter		**Symbol**	**Limit**	**Unit**
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	45[a]	A
	T_C = 70 °C		38	
	T_A = 25 °C		30[b, c]	
	T_A = 70 °C		23[b, c]	
Pulsed Drain Current		I_{DM}	180	
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	40	
	T_A = 25 °C		25[b,c]	
Avalanche Current	L = 0.1 mH	I_{AS}	51	
Single-Pulse Avalanche Energy		E_{AS}	88	mJ
Maximum Power Dissipation	T_C = 25 °C	P_D	75	W
	T_C = 70 °C		48	
	T_A = 25 °C		55[b, c]	
	T_A = 70 °C		43[b, c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		**Symbol**	**Typical**	**Maximum**	**Unit**
Maximum Junction-to-Ambient[b, d]	t ≤ 10 s	R_{thJA}	35	45	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	3	8	

Notes:
a. Package limited.
b. Surface mounted on 1" x 1" FR4 board.
c. t = 10 s.
d. Maximum under Steady State conditions is 85 °C/W.

SPECIFICATIONS T_J = 25 °C, unless otherwise noted						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	40			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA		55		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 6.3		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1		3	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 32V, V_{GS} = 0 V			1	µA
		V_{DS} = 32 V, V_{GS} = 0 V, T_J = 55 °C			10	
On-State Drain Current[a]	$I_{D(on)}$	$V_{DS} \geq$ 5 V, V_{GS} = 10 V	45			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 10 A		7.8	9	mΩ
		V_{GS} = 4.5 V, I_D = 5 A		9.6	13	
Forward Transconductance[a]	g_{fs}	V_{DS} = 32 V, I_D = 10 A		50		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = 32 V, V_{GS} = 0 V, f = 1 MHz		2240		pF
Output Capacitance	C_{oss}			175		
Reverse Transfer Capacitance	C_{rss}			160		
Total Gate Charge	Q_g	V_{DS} = 32 V, V_{GS} = 10V, I_D = 10 A		45		nC
Gate-Source Charge	Q_{gs}			4.5		
Gate-Drain Charge	Q_{gd}			11		
Gate Resistance	R_g	f = 1 MHz		2.5		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 32 V, R_L = 5.4 Ω $I_D \cong$ 5 A, V_{GEN} = 4.5 V, R_g = 1 Ω		13		ns
Rise Time	t_r			9		
Turn-Off DelayTime	$t_{d(off)}$			40		
Fall Time	t_f			2		
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 32 V, R_L = 5.4 Ω $I_D \cong$ 10 A, V_{GEN} = 10 V, R_g = 1 Ω		10		
Rise Time	t_r			6		
Turn-Off DelayTime	$t_{d(off)}$			15		
Fall Time	t_f			5		
Drain-Source Body Diode Characteristics						
Continous Source-Drain Diode Current	I_S	T_C = 25 °C			45	A
Pulse Diode Forward Current[a]	I_{SM}				180	
Body Diode Voltage	V_{SD}	I_S = 1 A		0.6	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 10 A, dI/dt = 100 A/µs, T_J = 25 °C		30	50	ns
Body Diode Reverse Recovery Charge	Q_{rr}			30	50	nC
Reverse Recovery Fall Time	t_a			20		ns
Reverse Recovery Rise Time	t_b			8		

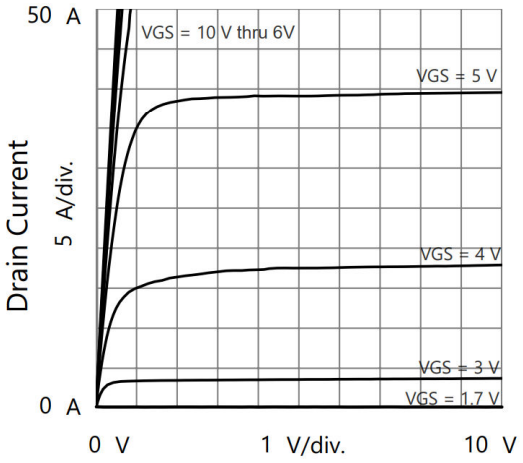
Notes:

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.

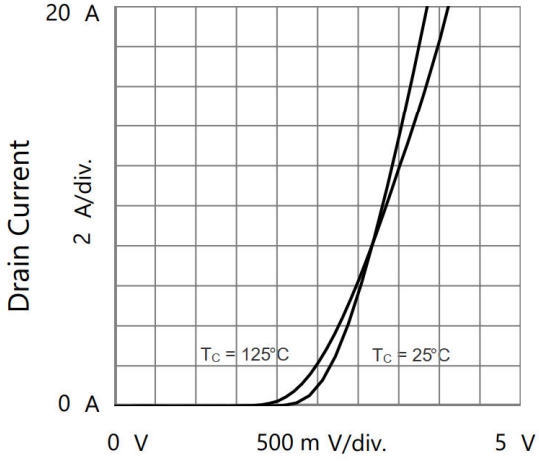
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

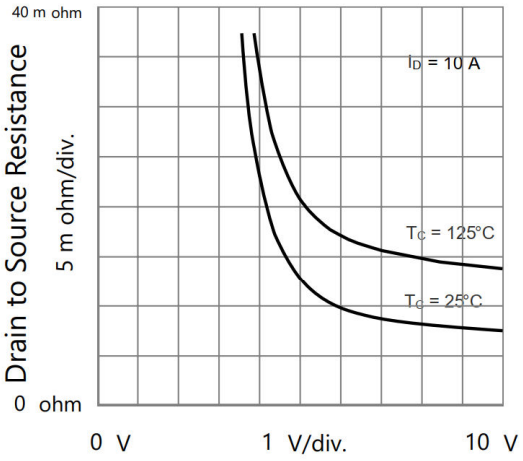
TYPICAL CHARACTERISTICS (25°C , unless otherwise noted)



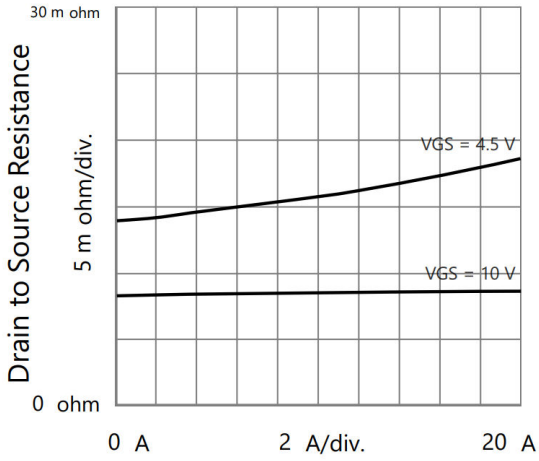
Drain to Source Voltage
Output Characteristics



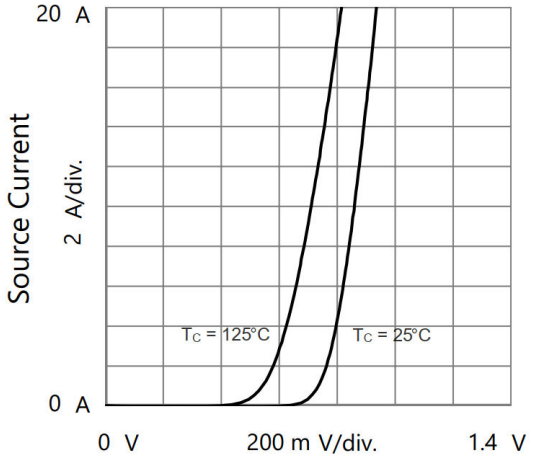
Gate to Source Voltage
Transfer Characteristics



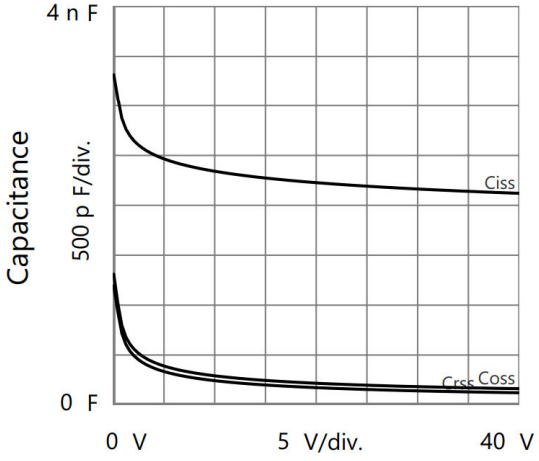
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current
Drain to Source Resistance vs. Drain Current

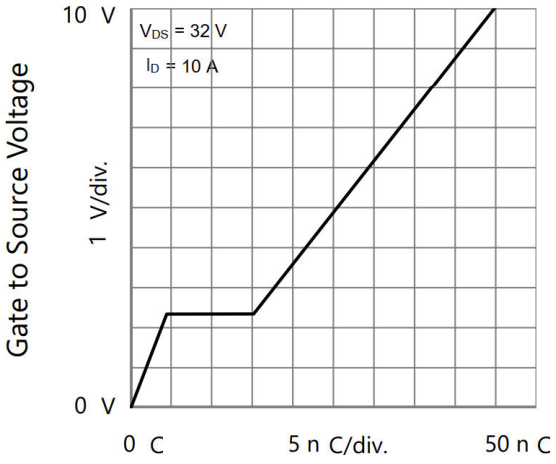


Source to Drain Voltage
Body Diode Forward Characteristics

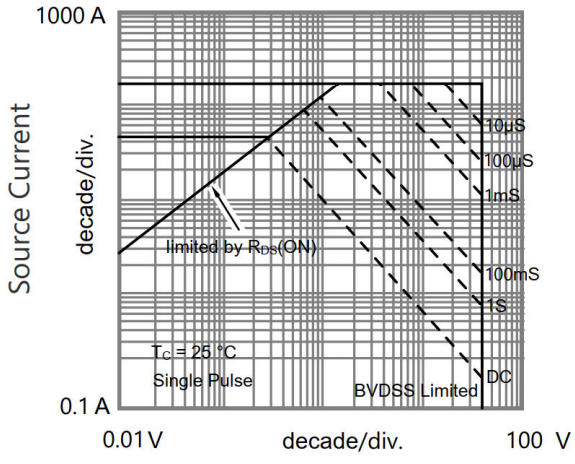


Drain to Source Voltage
Capacitances

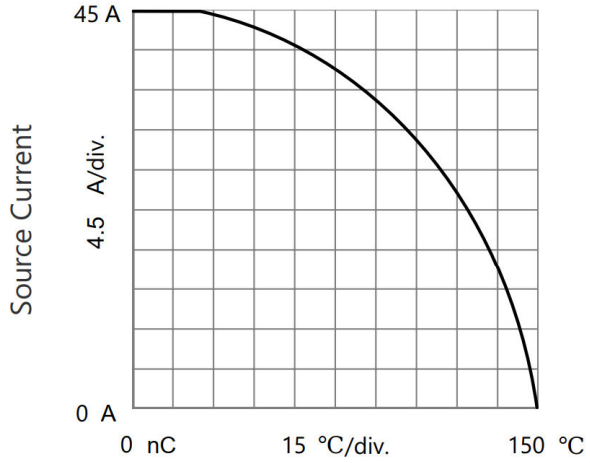
TYPICAL CHARACTERISTICS (25°C , unless otherwise noted)



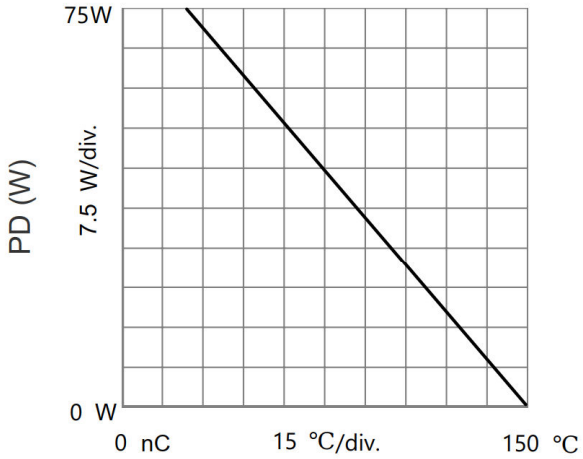
Gate Charge
Gate to Source Voltage vs. GateCharge



Source to Drain Voltage
Safe Operating Area, Junction-to-Ambient



T_C - Case Temperature
Current Derating



T_C - Case Temperature
Power Derating

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