



P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY	
V _{DS} (V)	- 20
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.030
$R_{DS(on)}(\Omega)$ at $V_{GS} = -2.5 \text{ V}$	0.041
I _D (A)	- 7
Configuration	Single

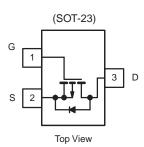
FEATURES

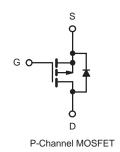
- Halogen-free Option Available
- TrenchFET® Power MOSFET



APPLICATIONS

• Load Switch for Portable Devices





ABSOLUTE MAXIMUM RATING	GS (T _C = 25 °C, unless	s otherwise noted	(k	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	- 20	V
Gate-Source Voltage		V _{GS}	± 8	V
0 11 0 1	T _C = 25 °C	I _D	- 7	
Continuous Drain Current	T _C = 125 °C		- 5	
Continuous Source Current (Diode Conduction)		I _S	- 4.5	Α
Pulsed Drain Currenta		I _{DM}	- 25	
Single Pulse Avalanche Current	. 0.411	I _{AS}	- 15	
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	7	mJ
W : B B: : :: :	T _C = 25 °C	P _D	2.3	10/
Maximum Power Dissipation ^a	T _C = 125 °C		0.72	W
Operating Junction and Storage Temperatu	ire Range	T _J , T _{sta}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^b	R _{thJA}	165	°C/W
Junction-to-Foot (Drain)		R _{thJF}	70	C/VV

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PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	'				l.	l	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA		- 20	-	-	.,
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	V _{GS} , I _D = - 250 μA	- 0.45	-	- 2	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$		-	-	± 100	nA
		V _{GS} = 0 V	V _{DS} = - 12 V	-	-	- 1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = - 12 V, T _J = 125 °C	-	-	- 50	μΑ
		V _{GS} = 0 V	V _{DS} = - 12 V, T _J = 175 °C	-	-	- 150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = - 4.5 V	$V_{DS} \le -5 V$	- 25	-	-	Α
		V _{GS} = - 4.5 V	I _D = - 3.5 A	-	0.028	0.030	
		V _{GS} = - 4.5 V	I _D = - 3.5 A, T _J = 125 °C	-	0.037	0.045	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V	I _D = - 3.5 A, T _J = 175 °C	-	0.042	0.049	Ω
		V _{GS} = - 2.5 V	I _D = - 3 A	-	0.036	0.041	-
		V _{GS} = - 1.8 V	I _D = - 2 A	-	0.047	0.052	
Forward Transconductance ^b	9 _{fs}	V _{DS} =	- 5 V, I _D = - 1.6 A	-	7		S
Dynamic ^b							
Input Capacitance	C _{iss}			-	690	-	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	0 V V _{DS} = - 6 V, f = 1 MHz	-	266	-	pF
Reverse Transfer Capacitance	C _{rss}			-	191		
Total Gate Charge ^c	Qg			-	8.3		
Gate-Source Charge ^c	Q _{gs}	V _{GS} = - 4.5 V	$V_{DS} = -6 \text{ V}, I_{D} = -3.85 \text{ A}$	-	1		nC
Gate-Drain Charge ^c	Q _{gd}			-	2.4	-	
Gate Resistance	R _g		f = 1 MHz	4.1	8.2	13.3	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	17		
Rise Time ^c	t _r	$V_{DD} = -6 \text{ V}, R_L = 1.6 \Omega$ $I_D \cong -3.85 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		-	19		- ns
Turn-Off Delay Time ^c	t _{d(off)}			-	28		
Fall Time ^c	t _f			-	13	-	
Source-Drain Diode Ratings and Chara	acteristics ^b						
Pulsed Current ^a	I _{SM}			-	-	25	Α
Forward Voltage	V _{SD}	I _F =	- 2 A, V _{GS} = 0 V	-	- 0.8	- 1.2	V

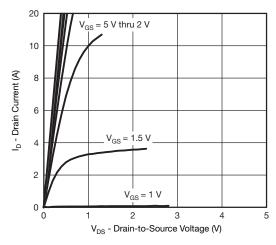
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

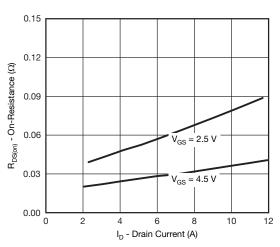
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



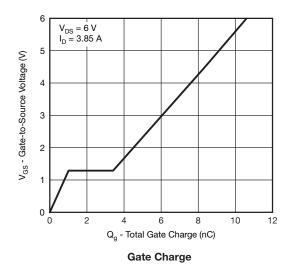
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Output Characteristics



On-Resistance vs. Drain Current



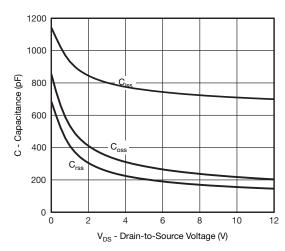
T_C = 25 °C

T_C = 125 °C

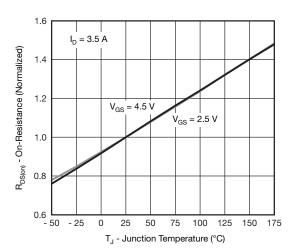
T_C = 125 °C

T_C = -55 °C V_{GS} - Gate-to-Source Voltage (V)

Transfer Characteristics



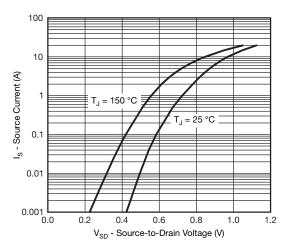
Capacitance



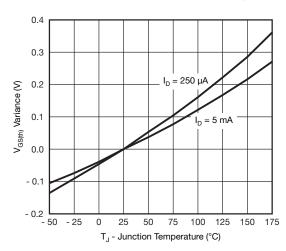
On-Resistance vs. Junction Temperature



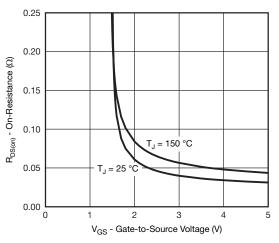
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



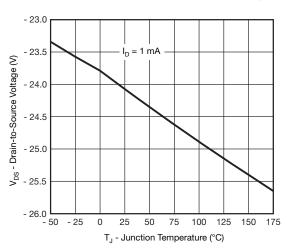
Source-Drain Diode Forward Voltage



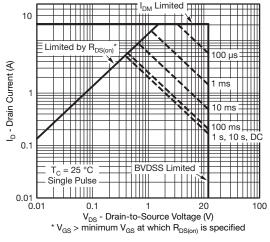
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



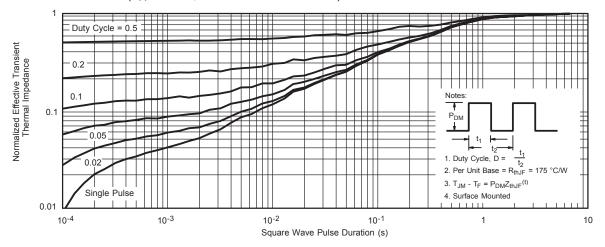
Drain Source Breakdown vs. Junction Temperature



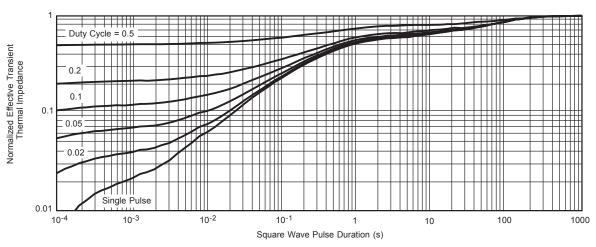
Safe Operating Area



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot



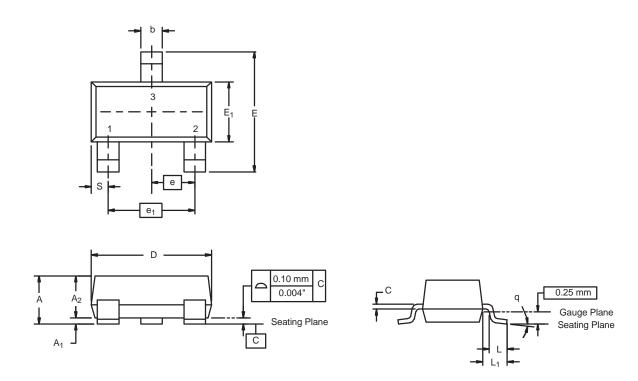
Normalized Thermal Transient Impedance, Junction-to-Ambient

Note

- The characteristics shown in the two graphs
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

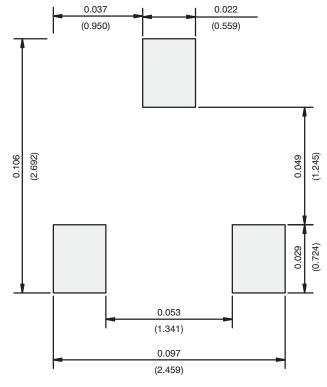
SOT-23: 3-LEAD



Dim -	MILLIM	MILLIMETERS		INCHES		
	Min	Max	Min	Max		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.037	4 Ref		
e ₁	1.90	1.90 BSC		8 Ref		
L	0.40	0.60	0.016	0.024		
L ₁	0.64 Ref		0.025	Ref		
S	0.50 Ref		0.020) Ref		
q	3°	8°	3°	8°		

ECN: S-03946-Rev. K, 09-Jul-01 DWG: 5479

RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)



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