

P-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)
- 100	0.08 at V _{GS} = - 10 V	- 25	17.9
	0.09 at V _{GS} = - 4.5 V	- 20	

FEATURES

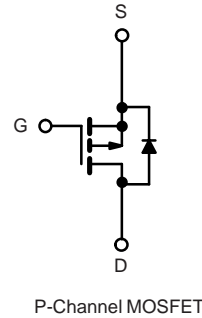
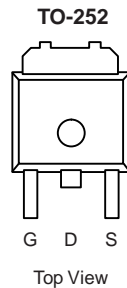
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Power Switch
- DC/DC Converters



ABSOLUTE MAXIMUM RATINGS T _C = 25 °C, unless otherwise noted				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 100	V	
Gate-Source Voltage	V _{GS}	± 20		
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	- 25	A
		T _C = 70 °C	- 17.3	
Pulsed Drain Current	I _{DM}	- 75		
Avalanche Current	I _{AS}	- 20		
Single Avalanche Energy ^a	L = 0.1 mH	E _{AS}	19.2	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	P _D	37.1 ^b	W
	T _A = 25 °C ^c		2.9	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)	R _{thJC}	2.9		

Notes:

a. Duty cycle ≤ 1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).

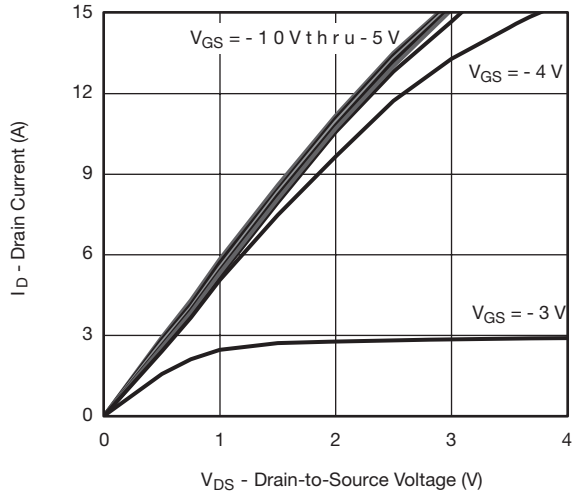
SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{DS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-1		-3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -100\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -100\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$			-50	
		$V_{DS} = -100\text{ V}, V_{GS} = 0\text{ V}, T_J = 150\text{ }^\circ\text{C}$			-250	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \leq -10\text{ V}, V_{GS} = -10\text{ V}$	-15			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -3.6\text{ A}$		0.08	0.09	Ω
		$V_{GS} = -4.5\text{ V}, I_D = -3.4\text{ A}$		0.09	0.10	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15\text{ V}, I_D = -3.6\text{ A}$		16		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = -50\text{ V}, f = 1\text{ MHz}$		1043		μF
Output Capacitance	C_{oss}			65		
Reverse Transfer Capacitance	C_{rss}			41		
Total Gate Charge ^c	Q_g	$V_{DS} = -50\text{ V}, V_{GS} = -10\text{ V}, I_D = -3.6\text{ A}$		23.2	34.8	nC
		$V_{DS} = -50\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -3.6\text{ A}$		11.7	17.9	
Gate-Source Charge ^c	Q_{gs}			3.5		
Gate-Drain Charge ^c	Q_{gd}			4.8		
Gate Resistance	R_g	$f = 1\text{ MHz}$	1.2	5.7	11.5	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = -50\text{ V}, R_L = 17.2\text{ }\Omega$ $I_D \cong -2.9\text{ A}, V_{GEN} = -10\text{ V}, R_g = 1\text{ }\Omega$		7	14	ns
Rise Time ^c	t_r			12	18	
Turn-Off Delay Time ^c	$t_{d(off)}$			33	50	
Fall Time ^c	t_f			9	18	
Drain-Source Body Diode Ratings and Characteristics $T_C = 25\text{ }^\circ\text{C}^b$						
Continuous Current	I_S				-20	A
Pulsed Current	I_{SM}				-75	
Forward Voltage ^a	V_{SD}	$I_F = -2.9\text{ A}, V_{GS} = 0\text{ V}$		-0.8	-1.5	V
Reverse Recovery Time	t_{rr}	$I_F = -2.9\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		50	75	ns
Peak Reverse Recovery Current	$I_{RM(REC)}$			-4	-6	A
Reverse Recovery Charge	Q_{rr}				98	147

Notes:

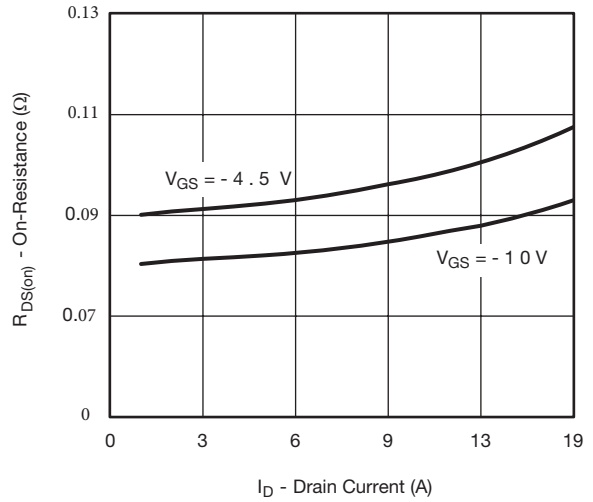
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

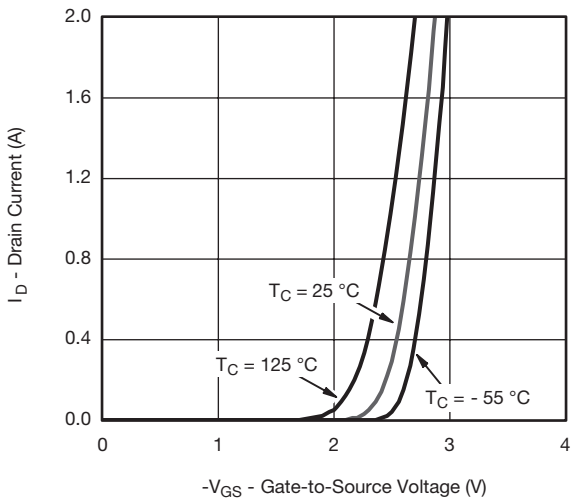
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



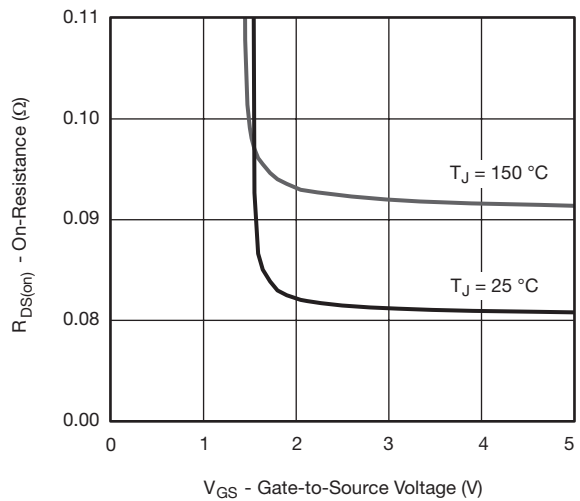
Output Characteristics



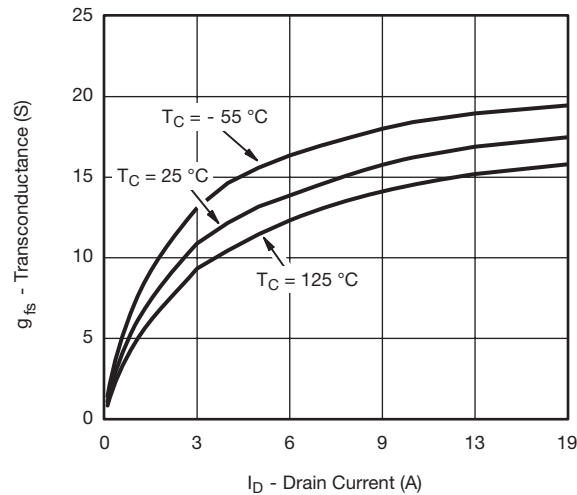
On-Resistance vs. Drain Current



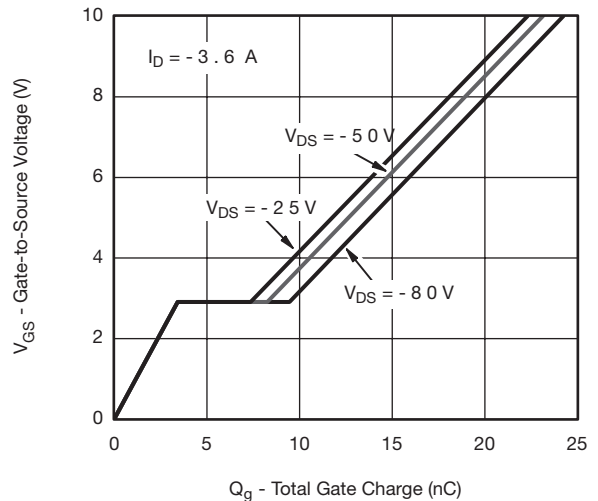
Transfer Characteristics



On-Resistance vs. Gate-to-Source Voltage

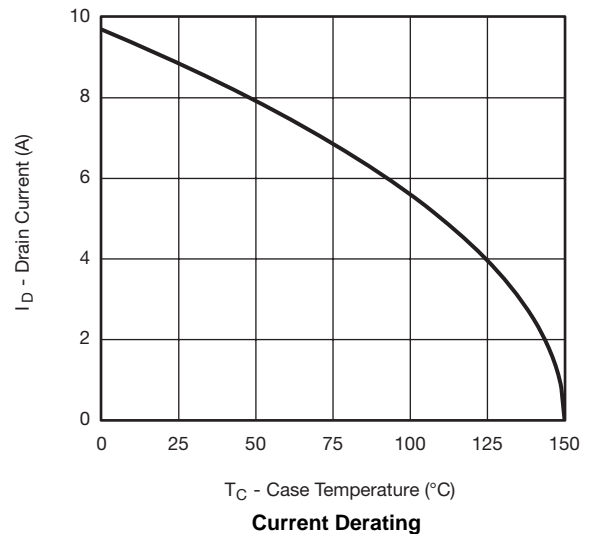
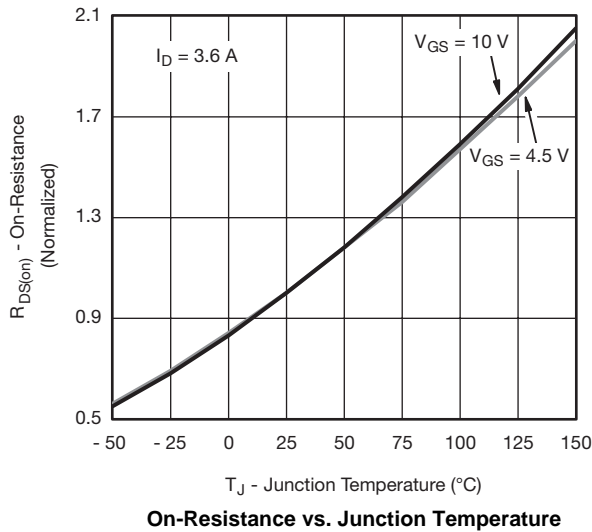
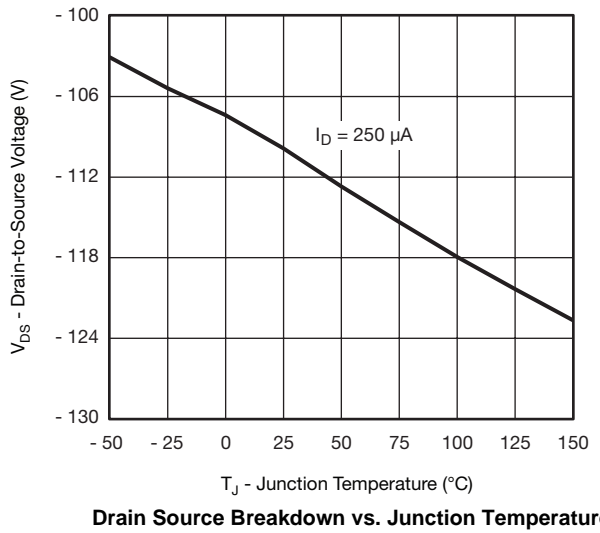
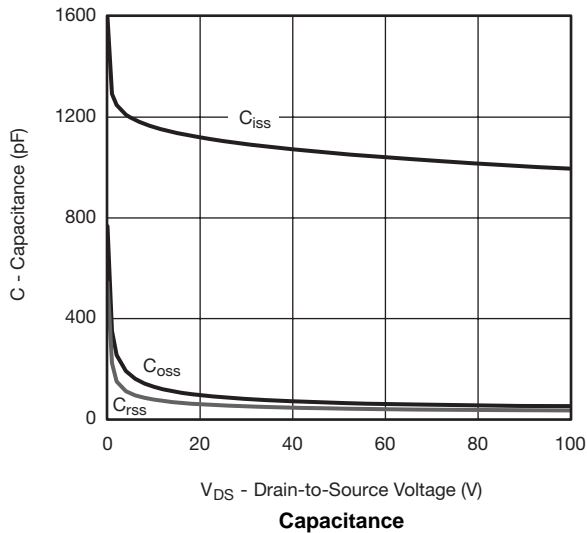
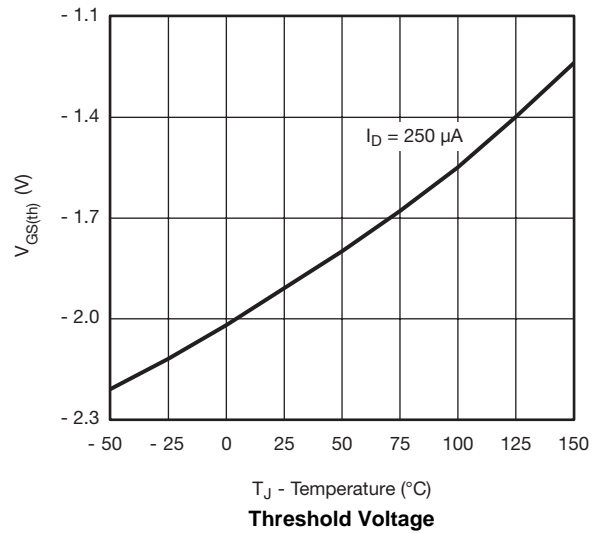
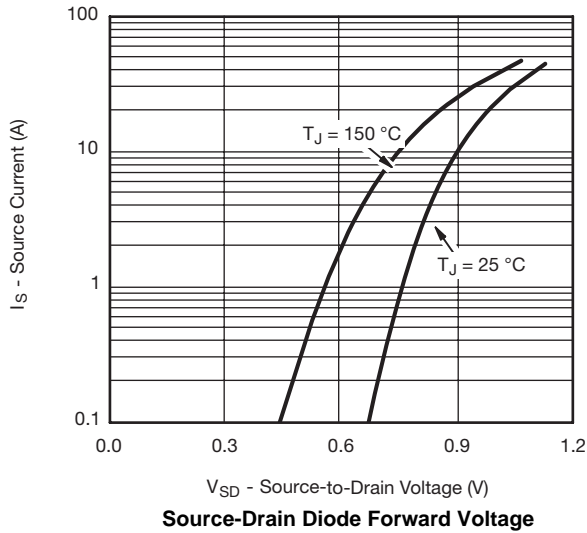


Transconductance

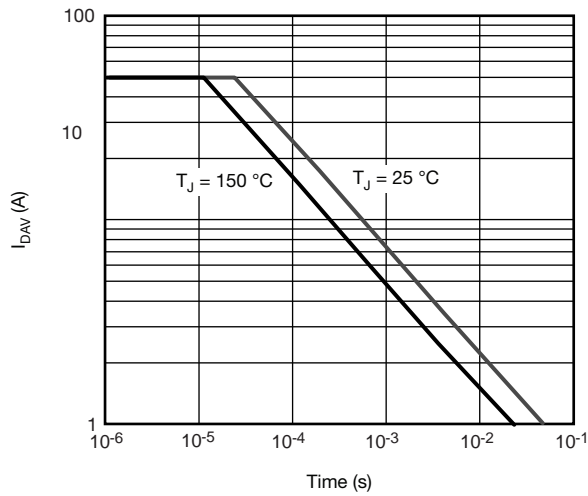


Gate Charge

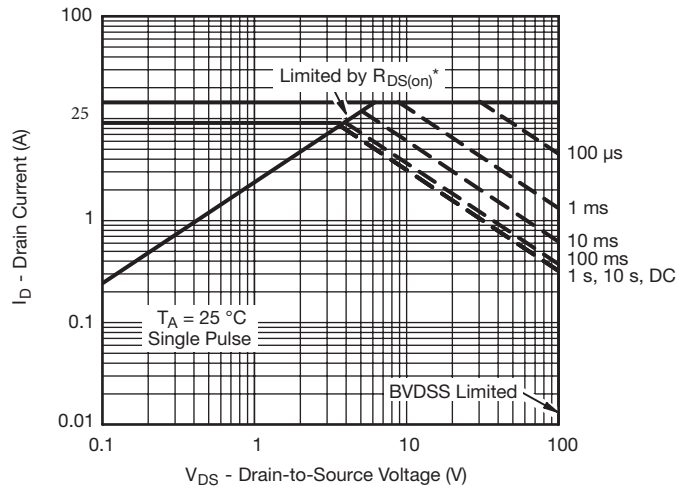
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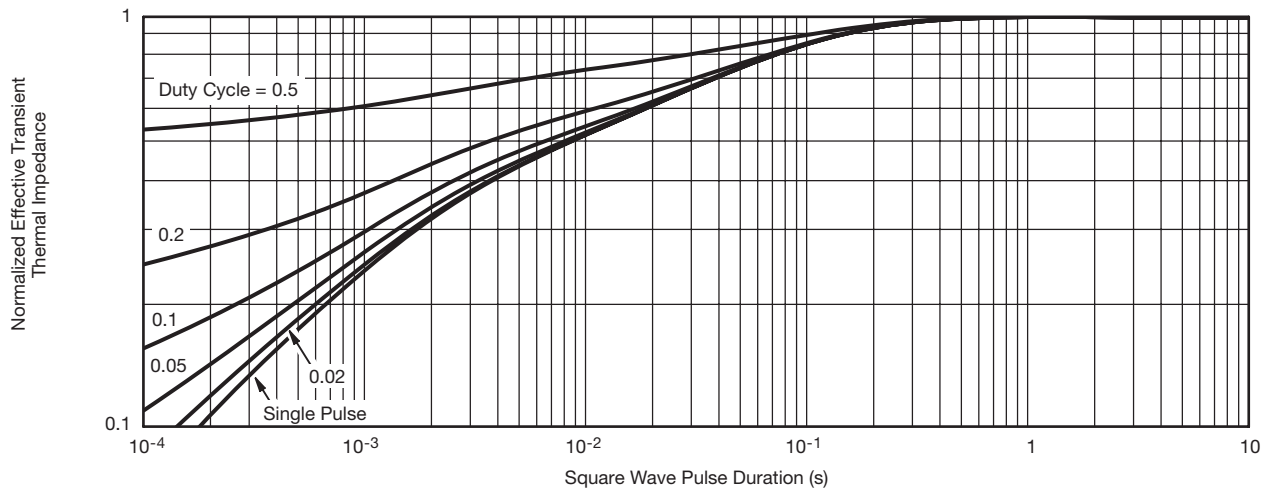


Single Pulse Avalanche Current Capability vs. Time



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

TO-252AA CASE OUTLINE



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.38	0.086	0.094
A1	-	0.127	-	0.005
b	0.64	0.88	0.025	0.035
b2	0.76	1.14	0.030	0.045
b3	4.95	5.46	0.195	0.215
C	0.46	0.61	0.018	0.024
C2	0.46	0.89	0.018	0.035
D	5.97	6.22	0.235	0.245
D1	5.21	-	0.205	-
E	6.35	6.73	0.250	0.265
E1	4.32	-	0.170	-
H	9.40	10.41	0.370	0.410
e	2.28 BSC		0.090 BSC	
e1	4.56 BSC		0.180 BSC	
L	1.40	1.78	0.055	0.070
L3	0.89	1.27	0.035	0.050
L4	-	1.02	-	0.040
L5	1.14	1.52	0.045	0.060
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347				

Note

- Dimension L3 is for reference only.

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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